

# UNISONIC TECHNOLOGIES CO., LTD

Z00607 **Preliminary TRIAC** 

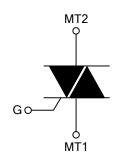
# 0.8A TRIAC

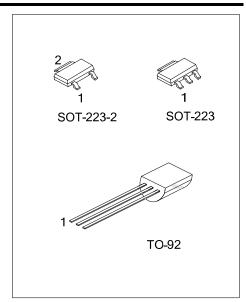
#### DESCRIPTION

The UTC **Z00607** is a 0.8A triac, it uses UTC's advanced technology to provide customers with low gate trigger current.

The UTC **Z00607** is suitable for low power AC switching applications and driving microcontrollers.

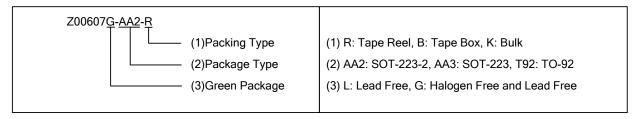
#### **SYMBOL**



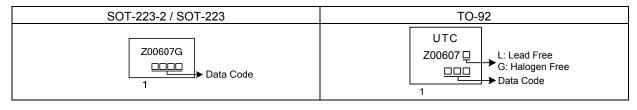


## **ORDERING INFORMATION**

| Ordering Number |               | Daakaga   | Pin Assignment |      |      | Dooking   |
|-----------------|---------------|-----------|----------------|------|------|-----------|
| Lead Free       | Halogen Free  | Package   | 1              | 2    | 3    | Packing   |
| -               | Z00607G-AA2-R | SOT-223-2 | MT1            | MT2  | GATE | Tape Reel |
| -               | Z00607G-AA3-R | SOT-223   | MT1            | MT2  | GATE | Tape Reel |
| Z00607L-T92-B   | Z00607G-T92-B | TO-92     | MT1            | GATE | MT2  | Tape Box  |
| Z00607L-T92-K   | Z00607G-T92-K | TO-92     | MT1            | GATE | MT2  | Bulk      |



#### **MARKING**



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## ABSOLUTE MAXIMUM RATINGS

| PARAMETER   |                      |                       | SYMBOL           | RATINGS    | UNIT   |
|---|----------------------|-----------------------|------------------|------------|--------|
| Repetitive Peak Off-State Voltage   |                      |                       | $V_{DRM}$        | 600        | ٧      |
| RMS On-State Current (Full Sine Wave) T <sub>MB</sub> =50°C   |                      | I <sub>T(RMS)</sub>   | 0.8              | Α          |        |
| Non Repetitive Surge Peak   | F=50Hz               | t=20ms                |                  | 9          |        |
| On-State Current (Full Cycle, T <sub>J</sub> initial=25°C)  | F=60Hz               | t=16.7ms              | I <sub>TSM</sub> | 9.5        | Α      |
| I <sup>2</sup> t Value for Fusing   | t <sub>P</sub> =10ms |                       | $I_{t}^{2}$      | 0.45       | $A^2s$ |
| Critical Rate of Rise of On-State Current I <sub>G</sub> =2×I <sub>GT</sub> , t <sub>r</sub> ≤100ns | F=120Hz              | T <sub>J</sub> =110°C | dl/dt            | 20         | A/μs   |
| Peak Gate Current   | t <sub>P</sub> =20µs | T <sub>J</sub> =110°C | $I_{GM}$         | 1          | Α      |
| Average Gate Power Dissipation T <sub>J</sub> =110°C  |                      | $P_{G(AV)}$           | 0.1              | W          |        |
| Operating Junction Temperature Range  |                      |                       | $T_J$            | -40 ~ +110 | °C     |
| Storage Junction Temperature Range  |                      |                       | $T_{STG}$        | -40 ~ +150 | °C     |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## **■ THERMAL DATA**

| PARAMETER             |                      | SYMBOL               | RATINGS | UNIT |
|-----------------------|----------------------|----------------------|---------|------|
| Junction to Lead (AC) | SOT-223-2<br>SOT-223 | $\theta_{\sf JLEAD}$ | 25      | °C/W |
|                       | TO-92                |                      | 60      | °C/W |
| Junction to Ambient   | SOT-223-2<br>SOT-223 | $	heta_{JA}$         | 60      | °C/W |
|                       | TO-92                |                      | 150     | °C/W |

# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub>=25°C, unless otherwise specified)

| PARAMETER                          | SYMBOL           | TEST CONDITIONS   |                       | MIN | TYP | MAX  | UNIT |
|------------------------------------|------------------|---|-----------------------|-----|-----|------|------|
| Cata Trigger Current (Note 1)      |                  |   | 1-11-111              |     |     | 5    | mA   |
| Gate Trigger Current (Note 1)      | I <sub>GT</sub>  | $V_D$ =12V, $R_L$ =30 $\Omega$  | IV                    |     |     | 7    |      |
| Gate Trigger Voltage               | $V_{GT}$         |   | ALL                   |     |     | 1.3  | V    |
| Gate Non-Trigger Voltage           | $V_{\text{GD}}$  | $V_D=V_{DRM}, R_L=3.3K\Omega,$<br>$T_J=110^{\circ}C$                      | ALL                   | 0.2 |     |      | V    |
| Holding Current (Note 2)           | $I_{H}$          | I <sub>T</sub> =200mA   |                       |     | 5   | mA   |      |
| Latabia a Comment                  | IL               | I <sub>G</sub> =1.2I <sub>GT</sub>  | I-III-IV              |     |     | 10   | A    |
| Latching Current                   |                  |   | II                    |     |     | 20   | mA   |
| Critical Rate of Rise of Off-State | dV/dt            | V <sub>D</sub> =67%V <sub>DRM</sub> , Gate Open,<br>T <sub>J</sub> =110°C |                       | 10  |     |      | \//\ |
| Voltage (Note 2)                   | u v/ut           |   |                       | 10  |     |      | V/µs |
| Critical Rate of Rise of Off-State | (dV/dt)c         | (dV/dt)c=0.35A/ms, T <sub>.i</sub> =110°C                                 |                       | 1.5 |     |      | V/µs |
| Voltage at Commutation (Note 2)    | (                | (   | 1                     |     |     |      |      |
| Peak On-State Voltage (Note 2)     | $V_{TM}$         | I <sub>TM</sub> =1.1A, t <sub>p</sub> =380μs                              | T <sub>J</sub> =25°C  |     |     | 1.5  | V    |
| Threshold Voltage (Note 2)         | $V_{TO}$         |   | T <sub>J</sub> =110°C |     |     | 0.95 | V    |
| Dynamic Resistance (Note 2)        | $R_D$            |   | T <sub>J</sub> =110°C |     |     | 420  | mΩ   |
| Repetitive Peak Off-State Current  | I <sub>DRM</sub> | V <sub>DRM</sub> =V <sub>RRM</sub> =600V                                  | TJ=25°C               |     |     | 5    | μΑ   |
| Trepetitive Feak Oil-State Cullent | $I_{RRM}$        | V DKW- V KKW-000 V  | T <sub>J</sub> =110°C |     |     | 0.1  | mA   |

Notes: 1. Minimum  $I_{\text{GT}}$  is guaranteed at 5% of  $I_{\text{GT}}$  max.

2. For both polarities of MT2 referenced to MT1.

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