

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

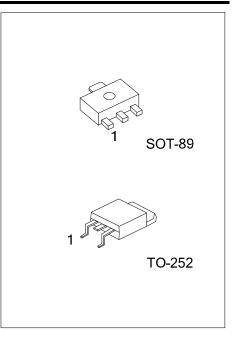
## UN1066

## NPN SILICON TRANSISTOR

# **HIGH SPEED SWITCHING** TRANSISTOR

#### **FEATURES**

- \* Low  $V_{CE(SAT)}$  voltage, up to 3A
- \* Suitable for fast switching applications
- \* High current gain



#### **ORDERING INFORMATION**

Ordering Number		Deekege	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	UN1066G-AB3-R	SOT-89	В	С	Е	Tape Reel	
UN1066L-TN3-R	UN1066G-TN3-R	TO-252	В	С	Е	Tape Reel	

Note: Pin Assignment: B: Base C: Collector E: Emitter

UN1066 <u>G-AB3-R</u>			
(1)Packing Type	(1) T: Tube, R: Tape Reel		
(2)Package Type	(2) AB3: SOT-89, TN3: TO-252		
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free		

#### MARKING

SOT-89	TO-252		
UN1066G → Date Code	UTC UN1066□ C: L: Lead Free C: Halogen Free Lot Code ← Data Code 1		

#### ■ ABSOLUTE MAXIMUM RATING (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector to Base Voltage	BV <sub>CBO</sub>	20	V
Collector to Emitter Voltage	BV <sub>CEO</sub>	15	V
Emitter to Base Voltage	BV <sub>EBO</sub>	5	V
Collector Current	Ιc	6	А
Collector Current (Pulse)	I <sub>CP</sub>	9	А
Base Current	I <sub>B</sub>	600	mA
Collector Dissipation (T <sub>C</sub> =25°C)	Pc	3.5	W
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

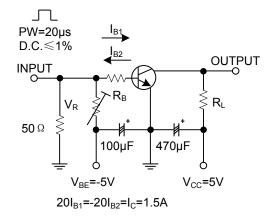
#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

					1	
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector to Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =10μA, I <sub>E</sub> =0	20			V
Collector to Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =1mA, R <sub>BE</sub> =∞	15			V
Emitter to Base Breakdown Voltage	$BV_{EBO}$	I <sub>E</sub> =10μA, I <sub>C</sub> =0	5			V
Collector-to-Emitter Saturation Voltage	$V_{CE(SAT)}$	I <sub>C</sub> =1.5A, I <sub>B</sub> =30mA			180	mV
		I <sub>C</sub> =3A, I <sub>B</sub> =60mA			300	mV
Base-to-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	I <sub>C</sub> =1.5A, I <sub>B</sub> =30mA			1.2	V
Collector Cutoff Current	I <sub>CBO</sub>	V <sub>CB</sub> =12V, I <sub>E</sub> =0			0.1	μA
Emitter Cutoff Current	I <sub>EBO</sub>	$V_{EB}=4V$ , $I_{C}=0$			0.1	μA
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> =0.5V, I <sub>C</sub> =5A	250			
Gain-Bandwidth Product	f⊤	V <sub>CE</sub> =2V, I <sub>C</sub> =500mA	100			MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, f=1MHz			50	рF
Turn-on Time	t <sub>on</sub>	Refer to Test Circuit			50	ns
Storage Time	t <sub>stg</sub>	Refer to Test Circuit			250	ns
Fall Time	t <sub>F</sub>	Refer to Test Circuit			25	ns



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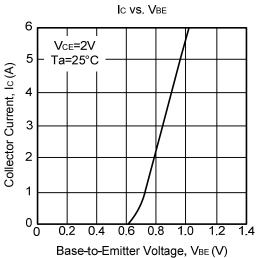
### TEST CIRCUIT

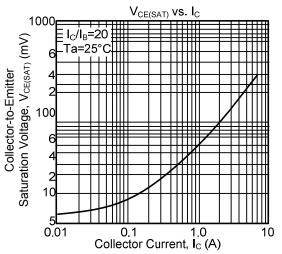


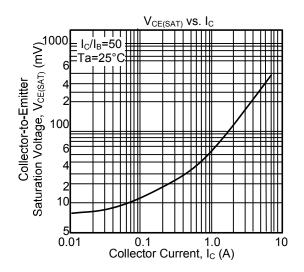


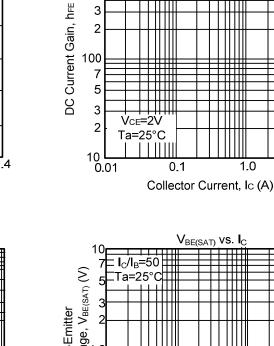
## **UN1066**

#### TYPICAL CHARACTERISTICS









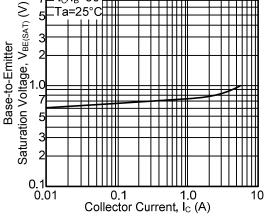
1000

7

5

3

2





10

hfe vs. Ic

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