

# DTC143E

# NPN SILICON TRANSISTOR

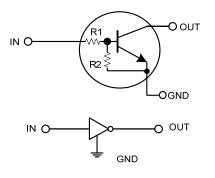
# NPN DIGITAL TRANSISTOR (BUILT-IN RESISTORS)

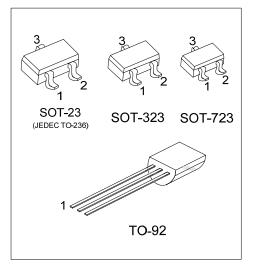
## FEATURES

\* Built-in bias resistors that implies easy ON/OFF applications.

\* The bias resistors are thin-film resistors with complete isolation to allow negative input.

## EQUIVALENT CIRCUIT





#### ORDERING INFORMATION

Ordering	Package	Pin Assignment			Packing		
Lead Free	Halogen Free	1 2		3	Facking		
DTC143EG-AE3-R	DTC143EG-AE3-R	SOT-23	Ι	G	0	Tape Reel	
DTC143EG-AL3-R	DTC143EG-AL3-R	SOT-323	Ι	G	0	Tape Reel	
DTC143EG-AQ3-R	DTC143EG-AQ3-R	SOT-723	Ι	G	0	Tape Reel	
DTC143EL-T92-B	DTC143EG-T92-B	TO-92	G	0	I	Tape Box	
DTC143EL-T92-K	DTC143EL-T92-K DTC143EG-T92-K		G	0	I	Bulk	
Note: Pin Assignment: I: IN G: GND O: OUT							
DTC143E <u>G</u> - <u>AE3</u> -R	(1) B: Tape Box	, K: Bull	k, R: Ta	pe Reel			

DIC143EG- <u>AE3</u> -R		(1) B: Tape Box, K: Bulk, R: Tape Reel
	(1)Packing Type	(2) AE3: SOT-23, AL3: SOT-323, AQ3: SOT-723
	(2)Package Type	T92: TO-92
	(3)Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

#### MARKING

SOT-23 / SOT-323 / SOT-723	TO-92			
CE3E E: Lead Free E: Halogen Free	UTC DTC143E G: Halogen Free Date Code			

#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Supply Voltage		V <sub>CC</sub>	50	V	
Input Voltage		V <sub>IN</sub>	-10 ~ +30	V	
Output Current		Ιc	100	mA	
Power Dissipation	SOT-23/ SOT-323	P <sub>D</sub>	400	mW	
	SOT-723		125	mW	
	TO-92		625	mW	
Junction Temperature		TJ	+150	°C	
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Device mounted on PCB 50mm × 50mm × 1.6mm

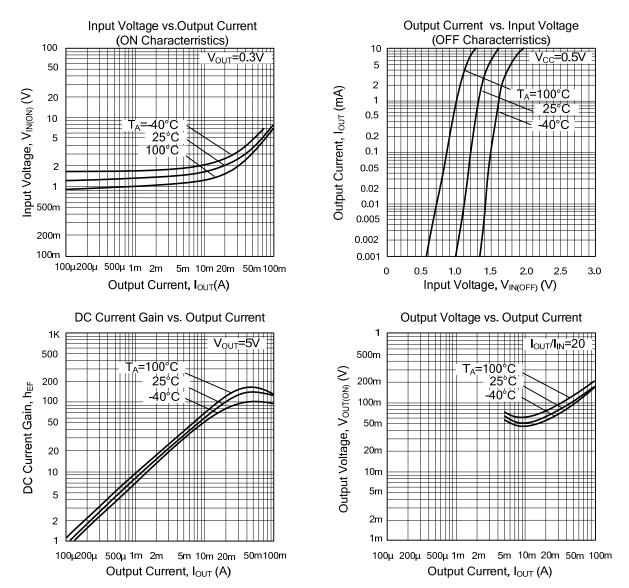
#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V <sub>IN(OFF)</sub>	V <sub>CC</sub> = 5V, I <sub>OUT</sub> =100µA			0.5	V
	V <sub>IN(ON)</sub>	V <sub>OUT</sub> = 0.3V, I <sub>OUT</sub> = 20mA	3			V
Output Voltage	V <sub>OUT(ON)</sub>	I <sub>OUT</sub> /I <sub>IN</sub> = 10mA/0.5 mA		0.1	0.3	V
Input Current	I <sub>IN</sub>	V <sub>IN</sub> = 5V			1.8	mA
Output Current	I <sub>OUT(OFF)</sub>	$V_{CC} = 50V$ , $V_{IN} = 0V$			0.5	μA
DC Current Gain	h <sub>FE</sub>	V <sub>OUT</sub> = 5V, I <sub>OUT</sub> = 10mA	20			
Input Resistance	R <sub>1</sub>		3.29	4.7	6.11	KΩ
Resistance Ratio	R2 R1		0.8	1	1.2	
Transition Frequency	f <sub>T</sub>	V <sub>CE</sub> =10V, I <sub>E</sub> = -5mA ,f =100MHz (Note)		250		MHz

Note: Transition frequency of the device



# TYPICAL CHARACTERISTIC



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