

#### **Ultra-Low Capacitance TVS Diode**

- Avalanche diode with low clamping / trigger voltage designed for replacement of polymer suppressor devices
- ESD / transient protection of high-speed data lines exceeding IEC61000-4-2 (ESD): 16 kV (contact) IEC61000-4-4 (EFT): 2.5 kV / 50 A (5/50 ns)
- No degradation or shifting of characteristics even after 1000 ESD pulses and lower peak voltage than polymer devices (see curve on page 4)
- Very low capacitance: 0.2 pF typ. @ 1.8 GHz
- Smallest form factor: 0.6 x 0.3 x 0.3 mm
- Working voltage: 5 V (can be extended to 60 V)
- Response time typ. < 0.5 ns @ 8 kV</li>
- Pb-free (RoHS) compliant) package

#### **Applications**

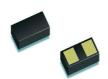
- 10/100/1000 Ethernet
- HDMI & DVI Interfaces
- Mobile communication and LCD displays
- Consumer products (STB, MP3, DVD, DSC...)
- Notebooks and desktop computers, peripherals



#### ESD5V0H1U-02LS



Туре	Package	Configuration	Marking	
ESD5V0H1U-02LS	TSSLP-2-1	1 line, uni-directional	Р	





**Maximum Ratings** at  $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
ESD contact discharge <sup>1)</sup>	V <sub>ESD</sub>	16	kV
Operating temperature range	$T_{op}$	-55125	°C
Storage temperature	$T_{\rm stg}$	-65150	

**Electrical Characteristics** at  $T_A = 25$ °C, unless otherwise specified

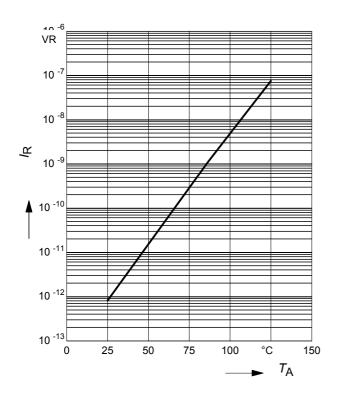
Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics	·	•	•	•	•
Reverse working voltage	$V_{RWM}$	-	-	5	V
Avalanche breakdown voltage	$V_{(BR)}$	-	200	-	
$I_{(BR)}$ = 1 mA, from pin 2 to 1					
Reverse current	I <sub>R</sub>	-	-	0.1	μA
$V_{R}$ = 5 V					
Clamping voltage <sup>1)</sup> after 30 ns	$V_{\mathrm{CL}}$	-	40	-	V
$V_{\rm ESD}$ = 8 kV, contact, from pin 2 to 1					
Line capacitance <sup>2)</sup>	C <sub>T</sub>				pF
$V_{R} = 0 \text{ V}, f = 1.8 \text{ GHz}$		_	0.2	0.4	
$V_{R} = 0 \text{ V}, f = 1 \text{ MHz}$		_	0.27	0.42	
Series inductance	L <sub>S</sub>	-	0.2	-	nH

 $<sup>^{1}</sup>V_{\mathrm{ESD}}$  according to IEC61000-4-2

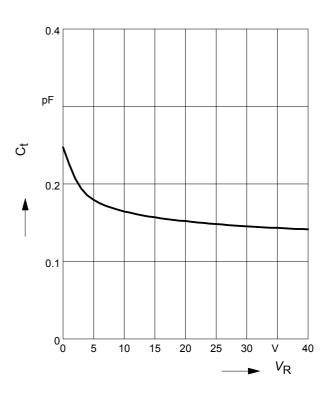
<sup>&</sup>lt;sup>2</sup>Total capacitance line to ground



# Reverse current $I_R = f(T_A)$

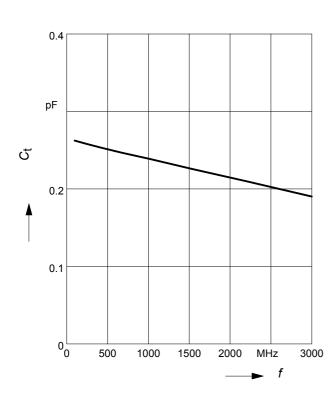


# Diode capacitance $C_T = f(V_R)$



# Line capacitance $C_T = f(f)$

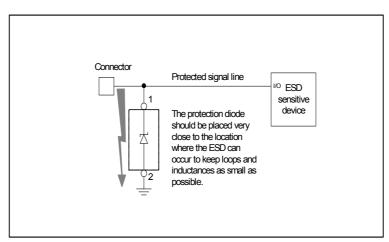
$$V_R = 0 V$$





## **Application example**

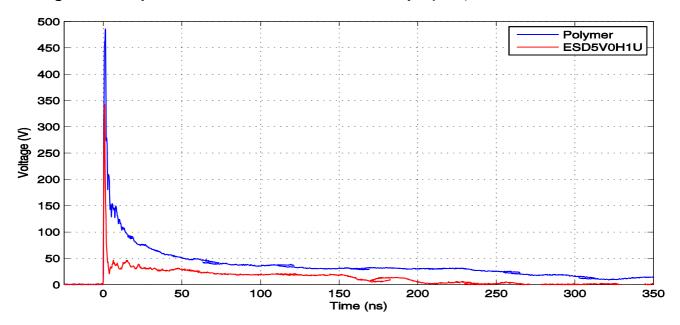
single channel, uni-directional





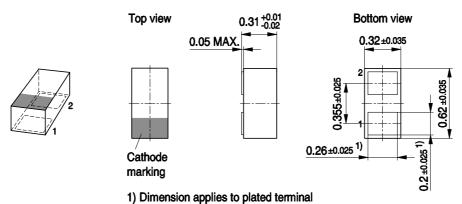
Clamping voltage at real ESD event according to IEC61000-4-2, 8 kV contact discharge: comparison with polymer suppressor.

ESD gun: C=150pF/R=330 $\Omega$ ... with 6 GHz oscilloscope (50 $\Omega$ )





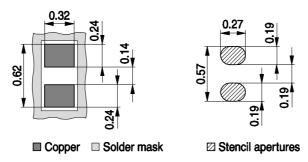
## Package Outline



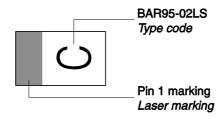
#### , ....

#### **Foot Print**

For board assembly information please refer to Infineon website "Packages"

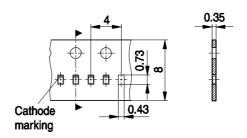


## Marking Layout (Example)



## Standard Packing

Reel ø180 mm = 15.000 Pieces/Reel





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