International Rectifier

AUTOMOTIVE MOSFET

AUIRFR2607Z

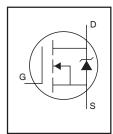
HEXFET® Power MOSFET

Features

- Advanced Process Technology
- Ultra Low On-Resistance
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified*

Description

Specifically designed for Automotive applications, this HEXFET® Power MOSFET utilizes the latest processing techniques to achieve extremely low on-resistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating . These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.



_/\:		
V _{(BR)DSS}		75V
R _{DS(on)}	typ.	17.6m $Ω$
	max.	22m Ω
I _{D (Silicon}	45A ⑨	
I _{D (Package}	42A	



G	D	S
Gate	Drain	Source

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is $25^{\circ}C$, unless otherwise specified.

Ambientiempere	ature (TA) 13 23 O, unless otherwise specified.		
$I_D @ T_C = 25^{\circ}C$	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	45 ⑨	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V (Silicon Limited)	32	Α
$I_D @ T_C = 25^{\circ}C$	Continuous Drain Current, V _{GS} @ 10V (Package Limited)	42	
I _{DM}	Pulsed Drain Current ①	180	Ĭ
$P_D @ T_C = 25^{\circ}C$	Power Dissipation	110	W
	Linear Derating Factor	0.72	W/°C
V_{GS}	Gate-to-Source Voltage	± 20	V
E _{AS}	Single Pulse Avalanche Energy (Thermally limited) ②	96	mJ
E _{AS} (Tested)	Single Pulse Avalanche Energy Tested Value ®	96	
I _{AR}	Avalanche Current ①	See Fig.12a, 12b, 15, 16	Α
E _{AR}	Repetitive Avalanche Energy ®		mJ
T _J	Operating Junction and	-55 to + 175	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	1
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case ®		1.38	
$R_{\theta JA}$	Junction-to-Ambient (PCB mount) ⑦		50	°C/W
$R_{\theta JA}$	Junction-to-Ambient		110	

HEXFET® is a registered trademark of International Rectifier.

^{*}Qualification standards can be found at http://www.irf.com/

Static Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	75			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_{J}$	Breakdown Voltage Temp. Coefficient		0.074		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance		17.6	22	mΩ	$V_{GS} = 10V, I_D = 30A$ ③
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 50\mu A$
gfs	Forward Transconductance	36			S	$V_{DS} = 25V, I_D = 30A$
I _{DSS}	Drain-to-Source Leakage Current			20		$V_{DS} = 75V, V_{GS} = 0V$
				250	μA	$V_{DS} = 75V, V_{GS} = 0V, T_{J} = 125^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			200		$V_{GS} = 20V$
İ	Gate-to-Source Reverse Leakage			-200	nA	V _{GS} = -20V

Dynamic Electrical Characteristics @ $T_J = 25$ °C (unless otherwise specified)

	Parameter Min. Typ. Max. Units Conditions						
	Parameter	Min.	Тур.	Max.	Units	Conditions	
Q_g	Total Gate Charge		34	51		$I_D = 30A$	
Q_{gs}	Gate-to-Source Charge		8.9		nC	$V_{DS} = 60V$	
Q_{gd}	Gate-to-Drain ("Miller") Charge		14			V _{GS} = 10V ③	
t _{d(on)}	Turn-On Delay Time		14			$V_{DD} = 38V$	
t _r	Rise Time		59		ns	$I_D = 30A$	
t _{d(off)}	Turn-Off Delay Time		39		115	$R_G = 15 \Omega$	
t _f	Fall Time	I —	28]	V _{GS} = 10V ③	
L_D	Internal Drain Inductance		4.5			Between lead,	
			4.5		nH	6mm (0.25in.)	
L _S	Internal Source Inductance		7.5] ''''	from package	
			7.5			and center of die contact	
C _{iss}	Input Capacitance		1440			$V_{GS} = 0V$	
C _{oss}	Output Capacitance	I —	190		1	$V_{DS} = 25V$	
C _{rss}	Reverse Transfer Capacitance		110			f = 1.0MHz	
C _{oss}	Output Capacitance		720		pF	$V_{GS} = 0V, V_{DS} = 1.0V, f = 1.0MHz$	
Coss	Output Capacitance		130			$V_{GS} = 0V, V_{DS} = 60V, f = 1.0MHz$	
C _{oss} eff.	Effective Output Capacitance		230			$V_{GS} = 0V, V_{DS} = 0V \text{ to } 60V $	

Diode Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	_		45 9		MOSFET symbol showing the
I _{SM}	Pulsed Source Current (Body Diode) ①	_	_	180	A	integral reverse p-n junction diode.
V_{SD}	Diode Forward Voltage			1.3	V	$T_J = 25^{\circ}C$, $I_S = 30A$, $V_{GS} = 0V$ ③
t _{rr}	Reverse Recovery Time		30	45	ns	$T_J = 25$ °C, $I_F = 30A$, $V_{DD} = 38V$
Q _{rr}	Reverse Recovery Charge		28	42	nC	di/dt = 100A/µs ③
ton	Forward Turn-On Time	Intrinsio	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)			

Notes ① through ⑨ are on page 3

Qualification Information[†]

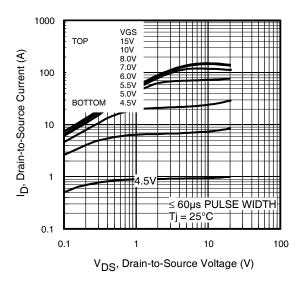
		Automotive (per AEC-Q101) ††			
addinioution Level		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
Moisture Sensit	isture Sensitivity Level DPA		MSL1		
	Machine Model		Class M4(425V)		
		(per AEC-Q101-002)			
505	Human Body Model	Class H1B(1000V)			
Charged Device Model		(per AEC-Q101-001)			
		Class C5(1125V)			
		(per AEC-Q101-005)			
RoHS Complian	t	Yes			

- † Qualification standards can be found at International Rectifier's web site: http://www.irf.com/
- †† Exceptions to AEC-Q101 requirements are noted in the qualification report.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11).
- ② Limited by T_{Jmax} , starting T_J = 25°C, L = 0.21mH R_G = 25 Ω , I_{AS} = 30A, V_{GS} =10V. Part not recommended for use above this value.
- $\ \ \,$ $\ \ \,$ $\ \ \,$ $\ \ \,$ Coss eff. is a fixed capacitance that gives the same charging time as $\ \ \,$ Coss while $\ \ \,$ VDS is rising from 0 to 80% VDSS .
- S Limited by T_{Jmax}, see Fig.12a, 12b, 15, 16 for typical repetitive avalanche performance.
- ⑤ This value determined from sample failure population. 100% tested to this value in production.
- When mounted on 1" square PCB (FR-4 or G-10 Material) . For recommended footprint and soldering techniques refer to application note #AN-994
- Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 42A

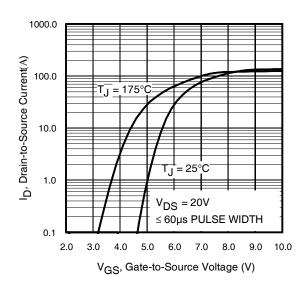
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 $(V) = 0.1000 \label{eq:constraints} (V) \label$

Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics



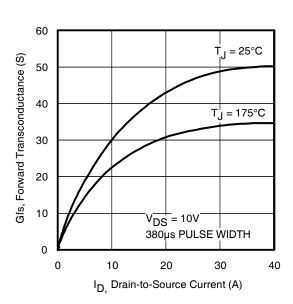


Fig 3. Typical Transfer Characteristics

Fig 4. Typical Forward Transconductance Vs. Drain Current

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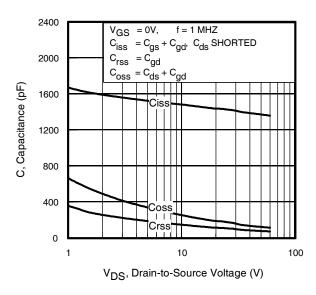


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

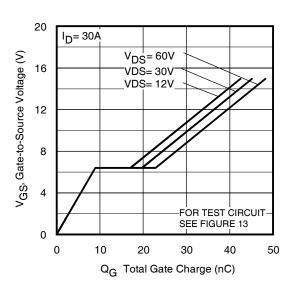


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

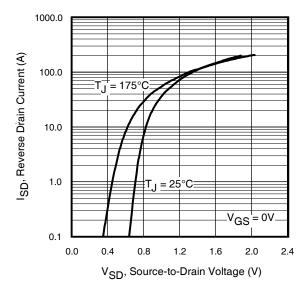


Fig 7. Typical Source-Drain Diode Forward Voltage

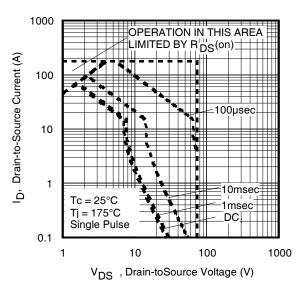


Fig 8. Maximum Safe Operating Area

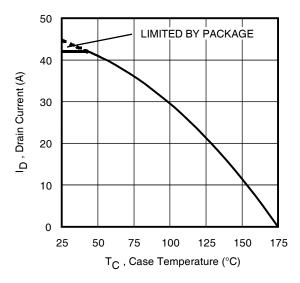


Fig 9. Maximum Drain Current Vs. Case Temperature

Fig 10. Normalized On-Resistance Vs. Temperature

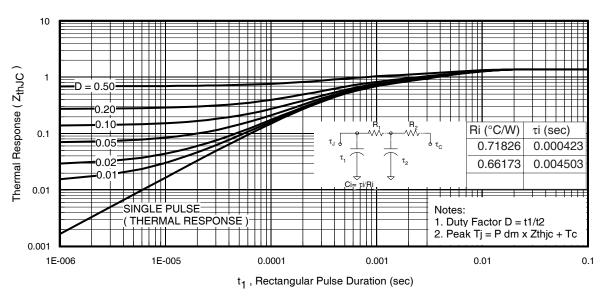


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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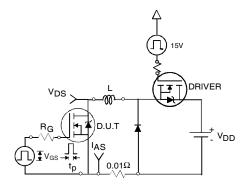


Fig 12a. Unclamped Inductive Test Circuit

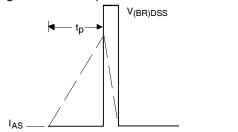


Fig 12b. | Unclamped Inductive Waveforms

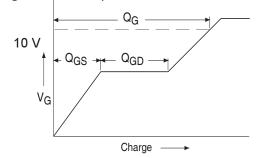


Fig 13a. Basic Gate Charge Waveform

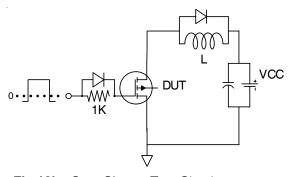


Fig 13b. Gate Charge Test Circuit www.irf.com

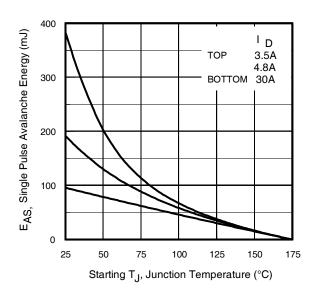


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

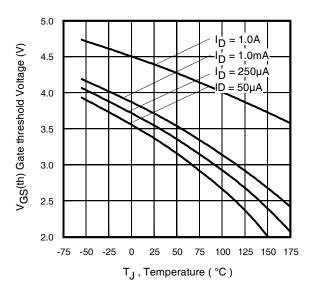


Fig 14. Threshold Voltage Vs. Temperature

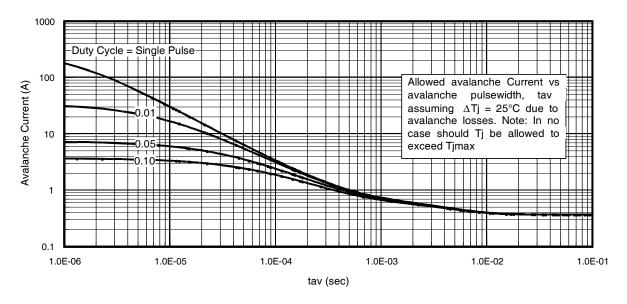


Fig 15. Typical Avalanche Current Vs. Pulsewidth

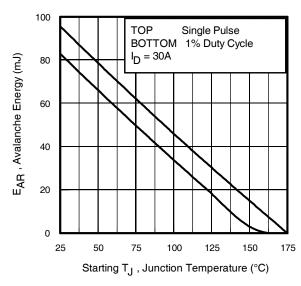


Fig 16. Maximum Avalanche Energy Vs. Temperature

Notes on Repetitive Avalanche Curves, Figures 15, 16: (For further info, see AN-1005 at www.irf.com)

- 1. Avalanche failures assumption: Purely a thermal phenomenon and failure occurs at a temperature far in excess of T_{jmax} . This is validated for every part type.
- 2. Safe operation in Avalanche is allowed as long asT_{imax} is not exceeded.
- 3. Equation below based on circuit and waveforms shown in Figures 12a, 12b.
- 4. P_{D (ave)} = Average power dissipation per single avalanche pulse.
- 5. BV = Rated breakdown voltage (1.3 factor accounts for voltage increase during avalanche).
- 6. I_{av} = Allowable avalanche current.
- 7. ΔT = Allowable rise in junction temperature, not to exceed T_{imax} (assumed as 25°C in Figure 15, 16).

 t_{av} = Average time in avalanche.

 $D = Duty cycle in avalanche = t_{av} \cdot f$

 $Z_{thJC}(D, t_{av})$ = Transient thermal resistance, see figure 11)

$$\begin{split} P_{D \; (ave)} = 1/2 \; (\; 1.3 \cdot \text{BV} \cdot \text{I}_{av}) &= \triangle \text{T} / \, \text{Z}_{thJC} \\ \text{I}_{av} = 2 \triangle \text{T} / \; [1.3 \cdot \text{BV} \cdot \text{Z}_{th}] \\ \text{E}_{AS \; (AR)} &= P_{D \; (ave)} \cdot t_{av} \end{split}$$

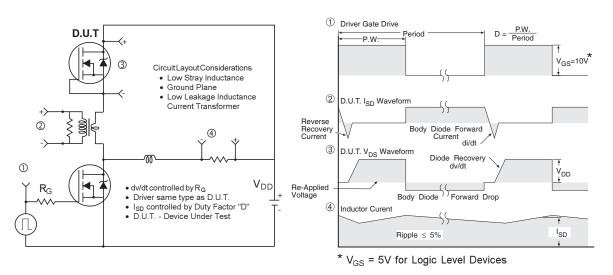


Fig 17. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

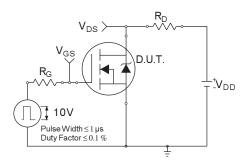


Fig 18a. Switching Time Test Circuit

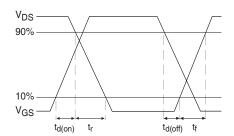
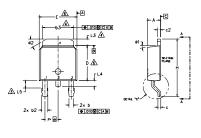


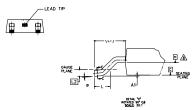
Fig 18b. Switching Time Waveforms

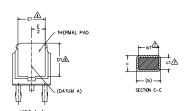
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D-Pak (TO-252AA) Package Outline

Dimensions are shown in millimeters (inches)







- DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- 2.- DIMENSION ARE SHOWN IN INCHES [MILLIMETERS].

- SIDE. THESE DIMENSIONS ARE MEASURED AT THE COLL.

 DIMENSION BY & C1 APPLIED TO BASE METAL ONLY.

 DATUM A & B TO BE DETERMINED AT DATUM PLANE H.

 9. OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.

S M B O	DIMENSIONS					
B	MILLIM	ETERS	INC	HES	Ď	
L	MIN.	MAX.	MIN.	MAX.	É	
Α	2.18	2.39	.086	.094		
A1	-	0.13	-	.005		
ь	0.64	0.89	.025	.035		
ь1	0.65	0.79	.025	,031	7	
b2	0.76	1,14	.030	.045		
b3	4,95	5,46	.195	.215	4	
С	0.46	0.61	.018	.024		
c1	0,41	0,56	.016	.022	7	
c2	0.46	0.89	.018	.035		
D	5.97	6.22	.235	.245	6	
D1	5.21	-	.205	-	4	
Ε	6.35	6.73	.250	.265	6	
E1	4.32	-	.170	-	4	
e	2.29	BSC	.090	BSC		
н	9.40	10,41	.370	.410		
L	1.40	1,78	,055	.070		
L1	2,74	BSC	.108	REF.		
L2	0.51	BSC	.020	BSC		
L3	0.89	1.27	.035	.050	4	
L4	-	1.02	-	.040		
L5	1,14	1.52	.045	.060	3	
ø	0.	10*	0,	10*		
ø1	0.	15*	0.	15*		
ø2	25*	35*	25"	35*		

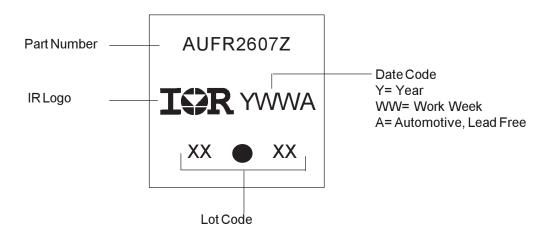
LEAD ASSIGNMENTS

- 1.- GATE 2.- DRAIN 3.- SOURCE 4.- DRAIN

IGBT & CoPAK

- 1.- GATE 2.- COLLECTOR 3.- EMITTER 4.- COLLECTOR

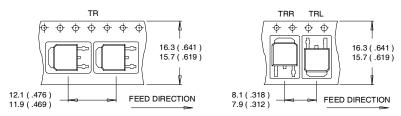
D-Pak (TO-252AA) Part Marking Information



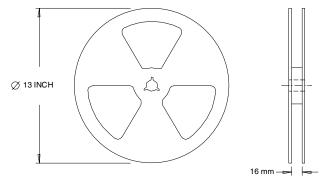
Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

D-Pak (TO-252AA) Tape & Reel Information

Dimensions are shown in millimeters (inches)



- NOTES:
 1. CONTROLLING DIMENSION: MILLIMETER.
 2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
 3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



NOTES:
1. OUTLINE CONFORMS TO EIA-481.

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Ordering Information

Base part	Package Type	Standard Pack		Standard Pack		Complete Part Number
		Form	Quantity			
AUIRFR2607Z	DPak	Tube	75	AUIRFR2607Z		
		Tape and Reel	2000	AUIRFR2607ZTR		
		Tape and Reel Left	3000	AUIRF2607ZTRL		
		Tape and Reel Right	3000	AUIRF2607ZTRR		

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AUIRFR2607Z

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