

# PMEG2020EPA

# 2 A low V<sub>F</sub> MEGA Schottky barrier rectifier Rev. 01 — 27 January 2010

**Product data sheet** 

# **Product profile**

#### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOT1061 leadless small Surface-Mounted Device (SMD) plastic package.

#### 1.2 Features

- Average forward current: I<sub>F(AV)</sub> ≤ 2 A
- Reverse voltage: V<sub>R</sub> ≤ 20 V
- Low forward voltage
- Exposed heat sink (cathode pad) for excellent thermal and electrical conductivity
- Leadless small SMD plastic package with medium power capability
- AEC-Q101 qualified

#### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications
- Battery chargers for mobile equipment

#### 1.4 Quick reference data

Quick reference data  $T_i = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz				
		$T_{amb} \le 80  ^{\circ}C$	<u>[1]</u> -	-	2	Α
		$T_{sp} \le 140  ^{\circ}C$	-	-	2	Α
$V_R$	reverse voltage		-	-	20	V
$V_{F}$	forward voltage	I <sub>F</sub> = 2 A	-	385	420	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 20 V	-	335	1900	μΑ

<sup>[1]</sup> Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



# 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	anode		<b>-</b> .
2	anode	3	3
3	cathode		006aab624
		Transparent top view	

# 3. Ordering information

Table 3. Ordering information

Type number	Package	Package				
	Name	Description	Version			
PMEG2020EPA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; three terminals; body 2 $\times$ 2 $\times$ 0.65 mm	SOT1061			

# 4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG2020EPA	AK

# 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage	$T_j = 25  ^{\circ}C$	-	20	V
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; $f$ = 20 kHz			
		T <sub>amb</sub> ≤ 80 °C	[1] _	2	Α
		T <sub>sp</sub> ≤ 140 °C	-	2	Α
I <sub>FRM</sub>	repetitive peak forward current	$\begin{array}{l} t_p \leq 1 \text{ ms;} \\ \delta \leq 0.25 \end{array}$	[2] _	7	Α
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	[2][3]	17	Α
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	[4][5]	500	mW
			[4][6]	960	mW
			[1][4]	1800	mW

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 Table 5.
 Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$T_j$	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2] Both anode pins connected.
- [3]  $T_i = 25$  °C prior to surge.
- [4] Reflow soldering is the only recommended soldering method.
- [5] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [6] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

#### 6. Thermal characteristics

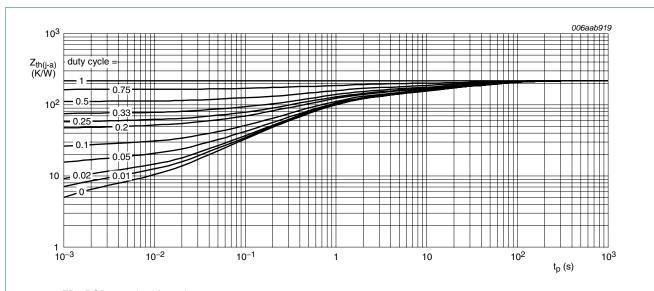
Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub> thermal resistance from junction to ambient		in free air	[1][2]				
		[3]	-	-	250	K/W	
			<u>[4]</u>	-	-	130	K/W
			<u>[5]</u>	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[6]</u>	-	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [6] Soldering point of cathode tab.

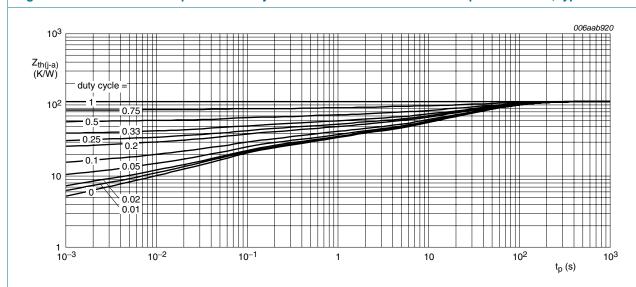
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FR4 PCB, standard footprint

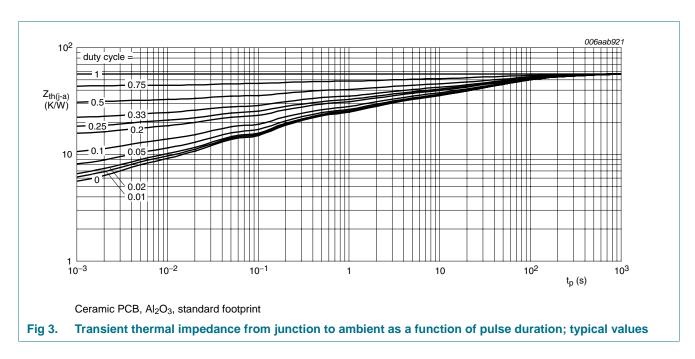
Transient thermal impedance from junction to ambient as a function of pulse duration; typical values Fig 1.



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

**Product data sheet** 

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



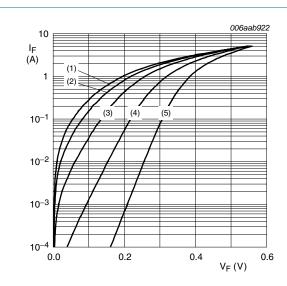
# 7. Characteristics

 Table 7.
 Characteristics

 $T_i = 25$  °C unless otherwise specified.

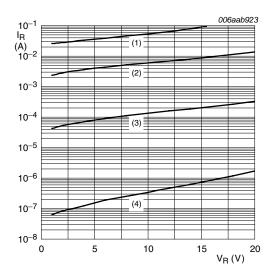
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{F}$	forward voltage	$I_F = 0.5 A$	-	280	-	mV
		I <sub>F</sub> = 2 A	-	385	420	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 10 V	-	135	-	μΑ
		V <sub>R</sub> = 20 V	-	335	1900	μΑ
$C_d$	C <sub>d</sub> diode capacitance	f = 1 MHz				
		V <sub>R</sub> = 1 V	-	175	-	pF
		V <sub>R</sub> = 10 V	-	65	-	pF
t <sub>rr</sub>	reverse recovery time		<u>[1]</u> _	50	-	ns

<sup>[1]</sup> When switched from  $I_F$  = 10 mA to  $I_R$  = 10 mA;  $R_L$  = 100  $\Omega$ ; measured at  $I_R$  = 1 mA.



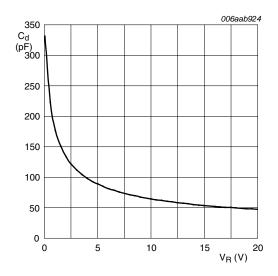
- (1)  $T_j = 150 \, ^{\circ}\text{C}$
- (2)  $T_i = 125 \, ^{\circ}\text{C}$
- (3)  $T_i = 85 \, ^{\circ}C$
- (4)  $T_j = 25 \, ^{\circ}C$
- (5)  $T_i = -40 \, ^{\circ}\text{C}$

Fig 4. Forward current as a function of forward voltage; typical values



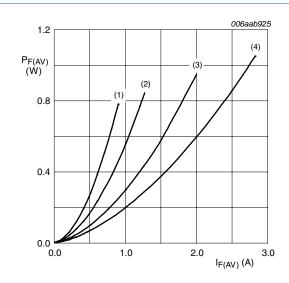
- (1)  $T_j = 125 \,^{\circ}\text{C}$
- (2)  $T_i = 85 \, ^{\circ}C$
- (3)  $T_j = 25 \,^{\circ}C$
- (4)  $T_j = -40 \, ^{\circ}C$

Fig 5. Reverse current as a function of reverse voltage; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^{\circ}\text{C}$ 

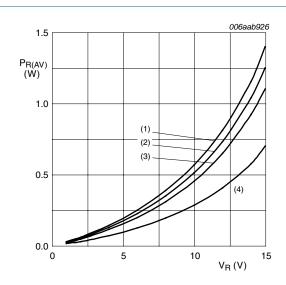
Fig 6. Diode capacitance as a function of reverse voltage; typical values



T<sub>i</sub> = 150 °C

- (1)  $\delta = 0.1$
- (2)  $\delta = 0.2$
- (3)  $\delta = 0.5$
- (4)  $\delta = 1$

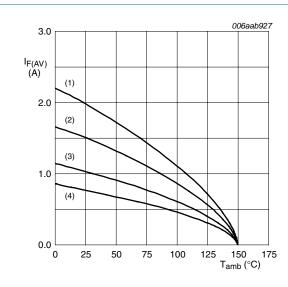
Fig 7. Average forward power dissipation as a function of average forward current; typical values



T<sub>i</sub> = 125 °C

- (1)  $\delta = 1$
- (2)  $\delta = 0.9$
- (3)  $\delta = 0.8$
- (4)  $\delta = 0.5$

Fig 8. Average reverse power dissipation as a function of reverse voltage; typical values

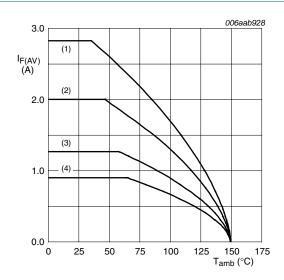


FR4 PCB, standard footprint

T<sub>i</sub> = 150 °C

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ; f = 20 kHz
- (3)  $\delta = 0.2$ ; f = 20 kHz
- (4)  $\delta = 0.1$ ; f = 20 kHz

Fig 9. Average forward current as a function of ambient temperature; typical values

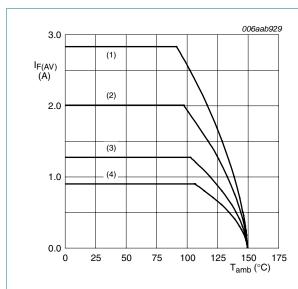


FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

T<sub>i</sub> = 150 °C

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ; f = 20 kHz
- (3)  $\delta = 0.2$ ; f = 20 kHz
- (4)  $\delta = 0.1$ ; f = 20 kHz

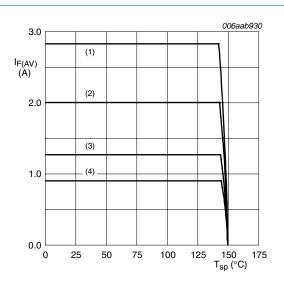
Fig 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ; f = 20 kHz
- (3)  $\delta = 0.2$ ; f = 20 kHz
- (4)  $\delta = 0.1$ ; f = 20 kHz

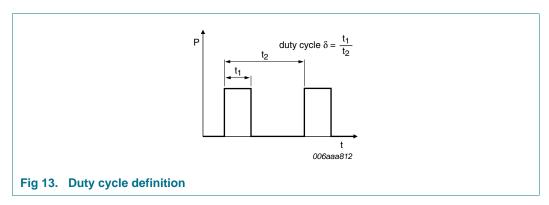
Fig 11. Average forward current as a function of ambient temperature; typical values



- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ; f = 20 kHz
- (3)  $\delta = 0.2$ ; f = 20 kHz
- (4)  $\delta = 0.1$ ; f = 20 kHz

Fig 12. Average forward current as a function of solder point temperature; typical values

#### 8. Test information



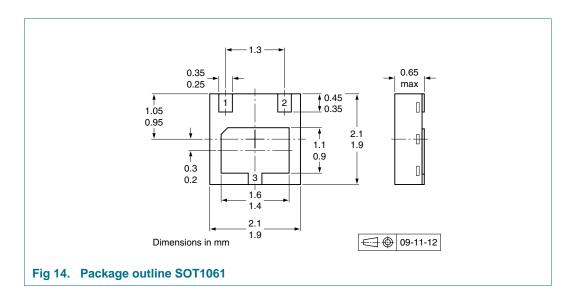
The current ratings for the typical waveforms as shown in Figure 8, 9, 10 and 11 are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,

 $I_{RMS}=I_{F(AV)}$  at DC, and  $I_{RMS}=I_{M} imes\sqrt{\delta}$  with I<sub>RMS</sub> defined as RMS current.

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 9. Package outline



# 10. Packing information

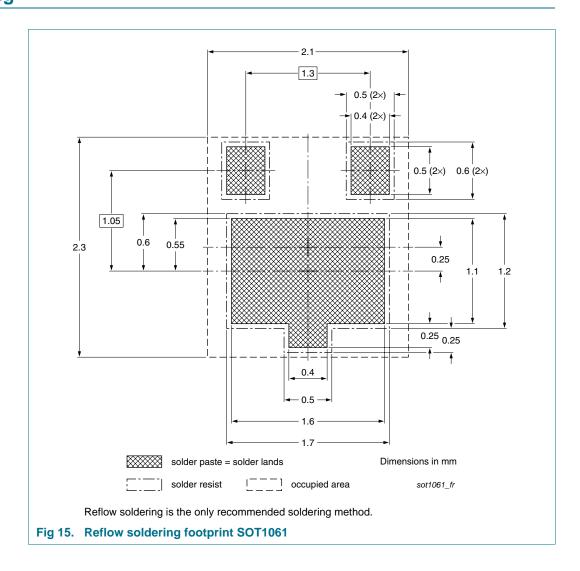
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			3000
PMEG2020EPA	SOT1061	4 mm pitch, 8 mm tape and reel	-115

<sup>[1]</sup> For further information and the availability of packing methods, see Section 14.

# 11. Soldering





# 12. Revision history

#### Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2020EPA_1	20100127	Product data sheet	-	-

# 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.



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