

# **C3D02060F**Silicon Carbide Schottky Diode

# Z-REC<sup>TM</sup> RECTIFIER (Full-Pak)

 $V_{RRM} = 600 \text{ V}$   $I_{F}(T_{c}=128^{\circ}C) = 2 \text{ A}$   $Q_{c} = 4.8 \text{ nC}$ 

#### **Features**

- 600-Volt Schottky Rectifier
- Optimized for PFC Boost Diode Application
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>E</sub>
- Fully Isolated Case

## Package







TO-220-F2

#### **Benefits**

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- No Additional Isolation Required

# PIN 1 O CASE

#### **Applications**

- Switch Mode Power Supplies
- Power Factor Correction
  - Typical PFC P<sub>out</sub>: 150W-300W
- Motor Drives

Part Number	Package	Marking
C3D02060F	TO-220-F2	C3D02060

# **Maximum Ratings** ( $T_c = 25$ °C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	600	V		
$V_{RSM}$	Surge Peak Reverse Voltage	600	V		
V <sub>DC</sub>	DC Blocking Voltage	600	V		
I <sub>F</sub>	Continuous Forward Current	4 2 1.8	А	T <sub>c</sub> =25°C T <sub>c</sub> =128°C T <sub>c</sub> =135°C	
$\mathrm{I}_{FRM}$	Repetitive Peak Forward Surge Current	12 8	А	$T_c$ =25°C, $t_p$ = 10 ms, Half Sine Wave, D=0.3 $T_c$ =110°C, $t_p$ = 10 ms, Half Sine Wave, D=0.3	
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	20 16	А	$T_c$ =25°C, $t_p$ = 10 ms, Half Sine Wave, D=0.3 $T_c$ =110°C, $t_p$ = 10 mS, Half Sine Wave, D=0.3	
$I_{FSM}$	Non-Repetitive Peak Forward Surge Current	65	А	T <sub>c</sub> =25°C, t <sub>p</sub> = 10 μs, Pulse	
P <sub>tot</sub>	Power Dissipation	10.8 4.7	W	T <sub>c</sub> =25°C T <sub>c</sub> =110°C	
$T_{_{\mathtt{J}}}$ , $T_{_{\mathtt{stg}}}$	Operating Junction and Storage Temperature	-55 to +175	°C		
	TO-220 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	



#### **Electrical Characteristics**

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V <sub>F</sub>	Forward Voltage	1.5 1.8	1.7 2.4	V	$I_F = 2 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 2 \text{ A } T_J = 175^{\circ}\text{C}$	
$I_R$	Reverse Current	10 20	50 100	μΑ	$V_R = 600 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 600 \text{ V } T_J = 175^{\circ}\text{C}$	
Q <sub>c</sub>	Total Capacitive Charge	4.8		nC	$V_R = 600 \text{ V, } I_F = 2A$ $di/dt = 500 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	
С	Total Capacitance	120 12 11		pF	$V_R = 0 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 200 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 400 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$	

#### Note:

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Unit
$R_{_{\theta JC}}$	Thermal Resistance from Junction to Case	13.8	°C/W

#### **Typical Performance**

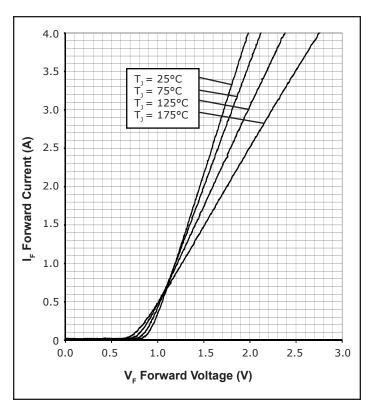


Figure 1. Forward Characteristics

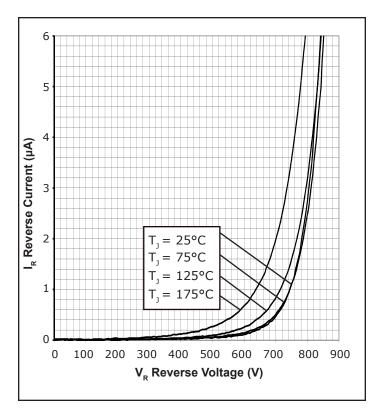
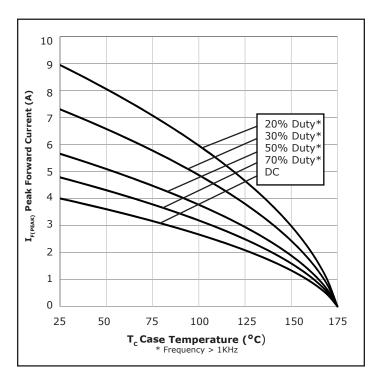


Figure 2. Reverse Characteristics

<sup>1.</sup> This is a majority carrier diode, so there is no reverse recovery charge.



#### **Typical Performance**





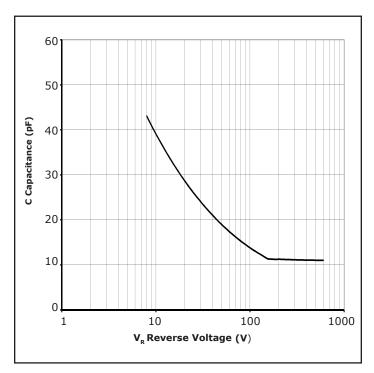


Figure 4. Capacitance vs. Reverse Voltage

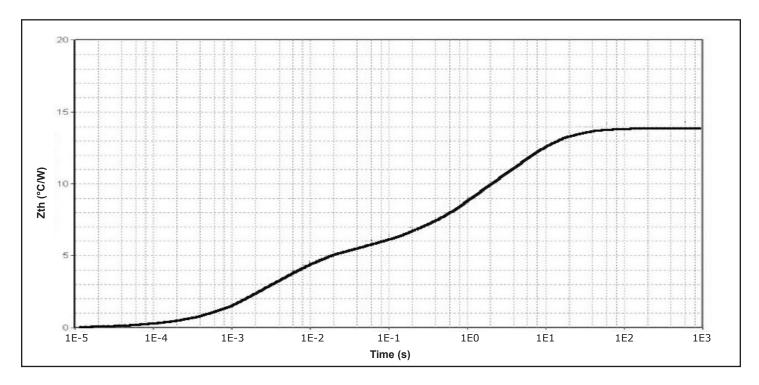


Figure 5. Transient Thermal Impedance



#### **Typical Performance**

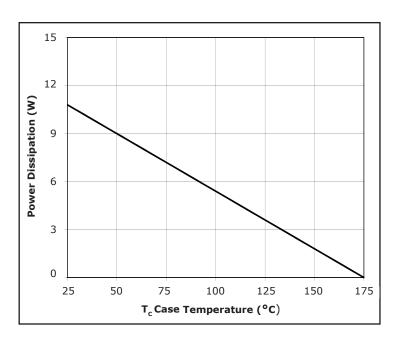
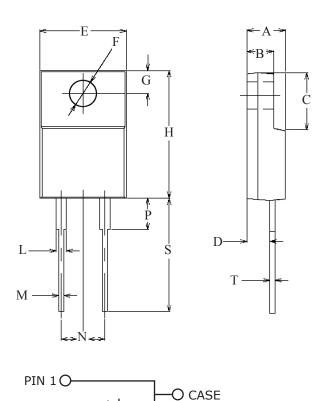


Figure 6. Power Derating

## **Package Dimensions**

Package TO-220-F2



POS	Inc	hes	Millimeters		
POS	Min	Max	Min	Max	
А	.177	.193	4.5	4.9	
В	.092	.108	2.34	2.74	
С	.248	.272	6.3	6.9	
D	.098	.114	2.5	2.9	
E	.390	.406	9.9	10.3	
F	.118	.134	3.0	3.4	
G	.122	.137	3.1	3.5	
Н	.617	.633	15.67	16.07	
L	.039	.055	1.0	1.4	
М	.016	.031	0.4	0.8	
N	.185	.217	4.7	5.5	
Р	0	.154	0	3.9	
S	.476	.508	12.1	12.9	
Т	.016	.031	0.4	0.8	

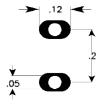
#### NOTE:

1. Dimension L, M, T apply for Solder Dip Finish

PIN 2 O-



#### **Recommended Solder Pad Layout**



TO-220-F2

Part Number	Package	Marking
C3D02060F	TO-220-F2	C3D02060

Note: Recommended soldering profiles can be found in the applications note here: http://www.cree.com/power\_app\_notes/soldering



#### **Diode Model**

$$\begin{array}{c|c} - & & \\ \hline V_T & R_T \\ \end{array}$$

$$Vf_T = V_T + If * R_T$$

$$R_T = 0.21 + (T_j *1.71*10^{-3})$$

$$V_T = 0.98 + (T_j * -1.7 * 10^{-3})$$

Note:  $T_i$  = Diode Junction Temperature In Degrees Celsius



#### **Notes**

#### RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.

#### REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

• This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, air traffic control systems, or weapons systems.

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