



DMN601VK

DUAL N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

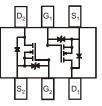
Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

SOT-563







TOP VIEW Internal Schematic

Ordering Information (Note 4)

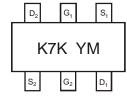
Part Number	Case	Packaging
DMN601VK-7	SOT-563	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

SOT-563



K7K = Marking Code YM = Date Code Marking Y = Year ex: S = 2005 M = Month ex: 9 = September

Date Code Key

Year	2005		2006	2007		2008	2009)	2010	2011		2012
Code	S		T	U		V	W		Χ	Υ		Z
Month	Jan	Feb	Mar	Apr	Ma	y Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	60	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Drain Current (Note 5)	Continuous Pulsed (Note 6)	I _D	305 800	mA

Thermal Characteristics (@T_A = +25°C unless otherwise specified.)

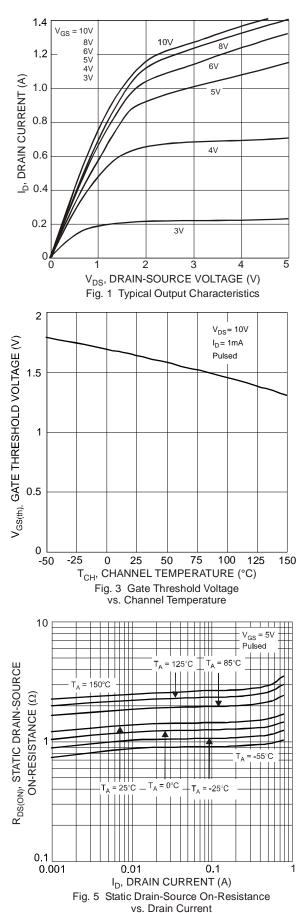
Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_d	250	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	T_j , T_{STG}	-65 to +150	°C

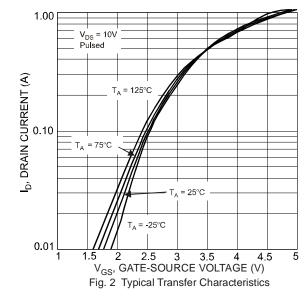
Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60			V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	IDSS			250	nΑ	$V_{DS} = 50V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}			±500	i na	$V_{GS} = \pm 10V, V_{DS} = 0V$
Gate-Goulde Leakage				±100		$V_{GS} = \pm 5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	1.0	1.6	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	R _{DS} (ON)		_	2.0	Ω	$V_{GS} = 10V, I_D = 0.5A$
Static Drain-Source On-Resistance			—	3.0		$V_{GS} = 4.5V, I_D = 200mA$
Forward Transfer Admittance	Y _{fs}	_	284	_	ms	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage (Note 7)	V_{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss			50	pF	05)/)/ 0)/
Output Capacitance	Coss			25	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		_	5.0	pF	1 - 1.0ivii iz

- $\label{eq:Notes:section} \begin{array}{ll} \text{Notes:} & \text{5. Device mounted on FR-4 PCB.} \\ \text{6. Pulse width } \leq 10 \mu \text{S, Duty Cycle} \leq 1\%. \\ \text{7. Short duration pulse test used to minimize self-heating effect.} \end{array}$







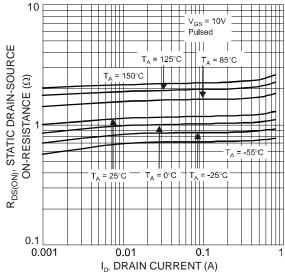
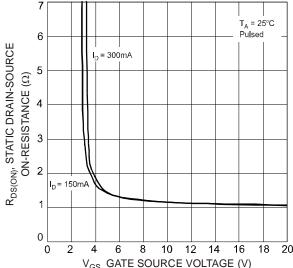


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current



V_{GS,} GATE SOURCE VOLTAGE (V) Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage



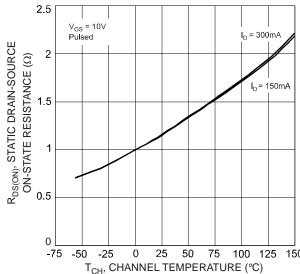
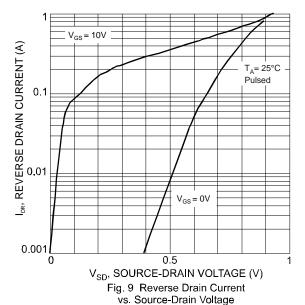
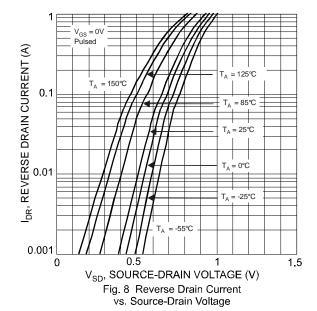


Fig. 7 Static Drain-Source On-State Resistance vs. Channel Temperature





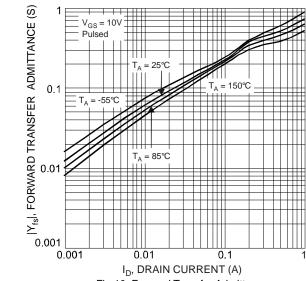
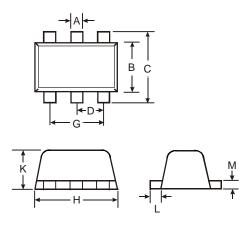


Fig.10 Forward Transfer Admittance vs. Drain Current



Package Outline Dimensions

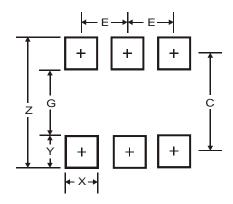
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	SOT-563							
Dim	Min	Max	Тур					
Α	0.15	0.30	0.20					
В	1.10	1.25	1.20					
С	1.55 1.70 1.							
D	0.50							
G	0.90	1.10	1.00					
Н	1.50	1.70	1.60					
K	0.55	0.60	0.60					
L	0.10	0.30	0.20					
М	0.10	0.18	0.11					
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Υ	0.5
С	1.7
E	0.5



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