SS16T3G, SBRA8160T3G

Surface Mount Schottky Power Rectifier

SMA Power Surface Mount Package

These devices employ the Schottky Barrier principle in a large area metal-to-silicon power diode. State of the art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity diodes in surface mount applications where compact size and weight are critical to the system.

Features

- Small Compact Surface Mountable Package with J-Bent Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Guardring for Stress Protection
- AEC-Q101 Qualified and PPAP Capable
- SBRA8 Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- All Packages are Pb-Free*

Mechanical Characteristics

- Case: Epoxy, Molded, Epoxy Meets UL 94 V-0
- Weight: 70 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in 12 mm tape, 5000 units per 13 inch reel
- Polarity: Cathode Lead Indicated by Polarity Band
- ESD Ratings:
 - ♦ Machine Model = C
 - ♦ Human Body Model = 3B
- Device Meets MSL 1 Requirements



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SCHOTTKY BARRIER RECTIFIER 1.0 AMPERES 60 VOLTS



SMA CASE 403D PLASTIC

MARKING DIAGRAM



SS16 = Specific Device Code A = Assembly Location

Y = Year WW = Work

= Work Week

■ = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
SS16T3G	SMA (Pb-Free)	5,000 / Tape & Reel
SBRA8160T3G	SMA (Pb-Free)	5,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V	
Average Rectified Forward Current (At Rated V_R , $T_C = 105^{\circ}C$)	I _O	1.0	А	
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	40	Α	
Storage/Operating Case Temperature	T _{stg} , T _C	-55 to +150	°C	
Operating Junction Temperature	TJ	-55 to +150	°C	
Voltage Rate of Change (Rated V_R , $T_J = 25$ °C)	dv/dt	10,000	V/µs	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 1) Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{ hetaJL} \ R_{ hetaJA}$	35 86	°C/W

^{1.} Mounted on 2 in Square PC Board with 1 in Square Total Pad Size, PC Board FR4.

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Va	lue	Unit
Maximum Instantaneous Forward Voltage (Note 2)	V _F	T _J = 25°C		V
(I _F = 0.1 A) (I _F = 1.0 A)		0.51 0.72		
Maximum Instantaneous Reverse Current	I _R	T _J = 25°C	T _J = 100°C	mA
(V _R = 60 V)		0.2	5.0	

^{2.} Pulse Test: Pulse Width \leq 250 $\mu s,$ Duty Cycle \leq 2.0%.

SS16T3G, SBRA8160T3G

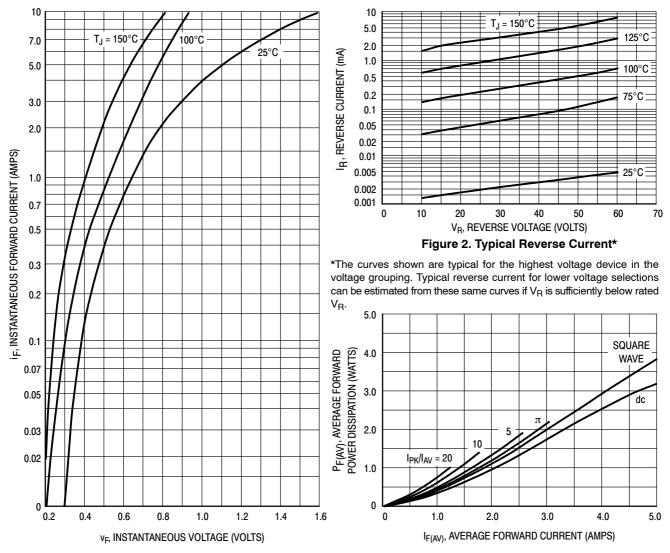


Figure 1. Typical Forward Voltage

Figure 3. Forward Power Dissipation

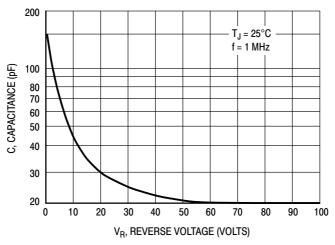
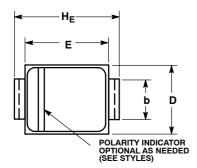


Figure 4. Typical Capacitance

SS16T3G, SBRA8160T3G

PACKAGE DIMENSIONS

SMA CASE 403D-02 **ISSUE F**

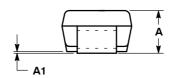




- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 403D-01 OBSOLETE, NEW STANDARD IS 403D-02.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	MOM	MAX
Α	1.97	2.10	2.20	0.078	0.083	0.087
A1	0.05	0.10	0.15	0.002	0.004	0.006
b	1.27	1.45	1.63	0.050	0.057	0.064
С	0.15	0.28	0.41	0.006	0.011	0.016
D	2.29	2.60	2.92	0.090	0.103	0.115
E	4.06	4.32	4.57	0.160	0.170	0.180
HE	4.83	5.21	5.59	0.190	0.205	0.220
L	0.76	1.14	1.52	0.030	0.045	0.060

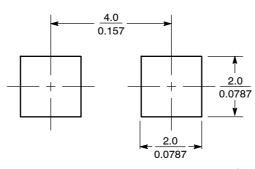




STYLE 1: PIN 1. CATHODE (POLARITY BAND)

2. ANODE

SOLDERING FOOTPRINT*



mm_` SCALE 8:1

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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