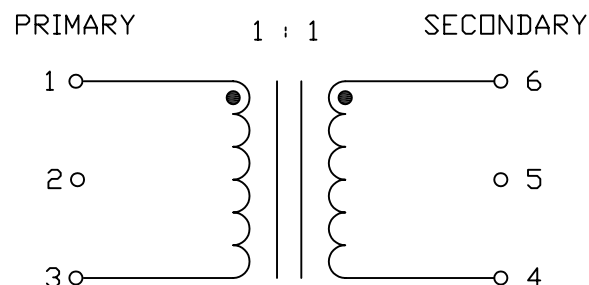




ELECTRICAL CHARACTERISTICS @ 25°C

SCHEMATIC



URNS RATIO

PRIMARY : SECONDARY

1 : 1

INDUCTANCE

PRIMARY

80μH MIN. @100kHz, L_s, 100mV

60μH MIN. @1.7MHz, L_p, 170mV

AT -40°C

40μH MIN. @100kHz, L_s, 100mV

LEAKAGE INDUCTANCE (L_e)

PRIMARY (SECONDARY SHORTED)

0.09μH MAX. @1MHz, 100mV

INTERWINDING CAPACITANCE (C_{w/w})

PRIMARY - SECONDARY

10pF MAX. @1MHz, 100mV

DC RESISTANCE

(1-3) = (6-4)

0.25 Ω MAX.

HIPOT

PER HAND-WORK-03

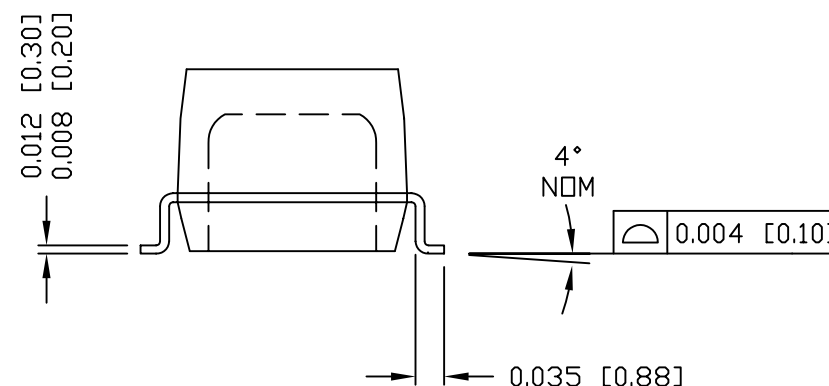
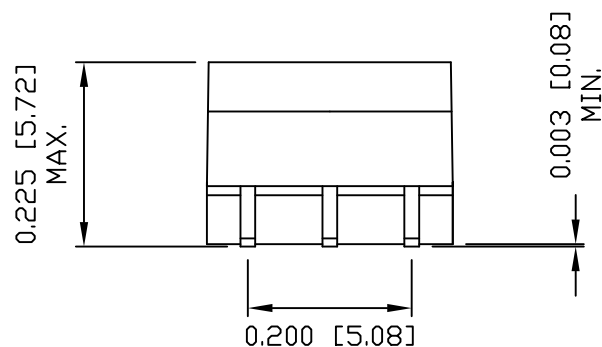
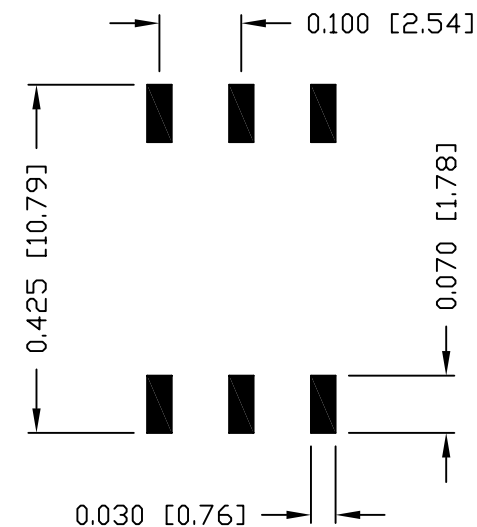
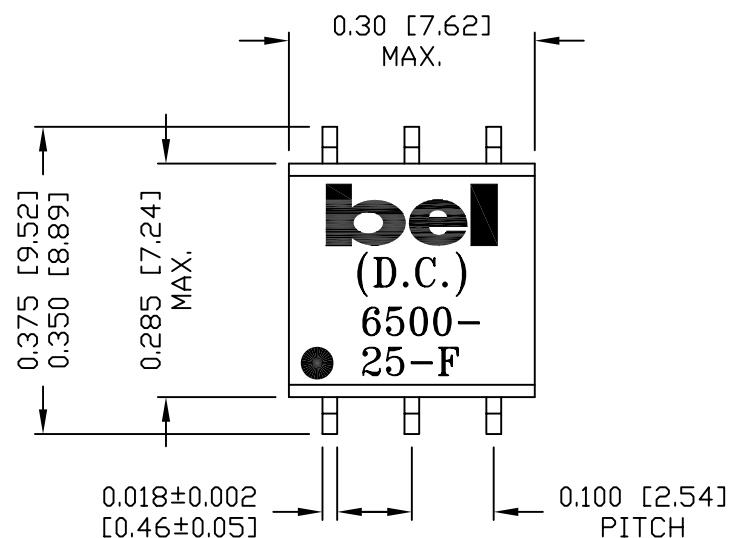
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE
CHAN CH	04-20-09	ELECTRICAL SPECIFICATION S554-6500-25-F	X554650025-F	.X	UNIT : INCH [mm] REV. : A
DRAWN BY	DATE		FILE NAME	.XX	SCALE : N/A SIZE : A4
YH Lai	04-20-09		X554650025FA.DWG	.XXX	PAGE : 2



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SUGGESTED PCB PAD LAYOUT



NOTES:

1. STANDARD MARKING REFER TO DDC. HAND-WORK-04.
2. PACKAGE CODE: "QAS002".

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REFERENCE	
Lawrence Tsang	04-20-09	MECHANICAL OUTLINE S554-6500-25-F	X554650025-F	TOL. IN INCH	UNIT : INCH [mm]	REV. : A
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A	SIZE : A4
YH Lai	04-20-09		X554650025FA.DWG	.XX ±0.01		PAGE : 3
				.XXX ±0.005		



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