TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WZ32FU, TC7WZ32FK

### 2-Input OR Gate

### **Features**

• High output current : ±24 mA (min) at V<sub>CC</sub> = 3 V

Super high speed operation : t<sub>pd</sub> = 2.4 ns (typ.)

at  $V_{CC}$  = 5 V, 50 pF

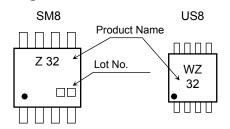
Operating voltage range : V<sub>CC</sub> = 1.65 to 5.5 V

• 5.5-V tolerant inputs

• 5.5-V power down protection outputs

• Matches the performance of TC74LCX series when operated at  $3.3 \ V \ V_{CC}$ 

# Marking



# TC7WZ32FU SSOP8-P-0.65 TC7WZ32FK (US8) SSOP8-P-0.50A

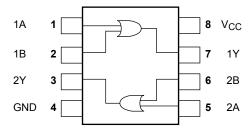
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit		
Supply voltage	V <sub>CC</sub>	−0.5 to 6	V		
DC input voltage	V <sub>IN</sub>	−0.5 to 6	V		
DC output voltage	\/a=	-0.5 to 6 (Note 1)	V		
	Vout	-0.5 to VCC+0.5 (Note 2)	V		
Input diode current	lık	-20	mA		
Output diode current	lok	-20 (Note 3)	mA		
DC output current	lout	±50	mA		
DC V <sub>CC</sub> /ground current	Icc	±50	mA		
Power dissipation	P <sub>D</sub>	300 (SM8) 200 (US8)	mW		
Storage temperature	T <sub>stg</sub>	-65 to 150	°C		
Lead temperature (10s)	TL	260	°C		

### Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0V$ 

Note 2: High or Low state. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND Start of com

Start of commercial production 2000-08

# **IEC Logic Symbol**



### **Truth Table**

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

### **Operating Ranges**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	1.65 to 5.5	V	
Supply voltage	VCC	1.5 to 5.5 (Note 4)	]	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to 5.5 (Note 5)	V	
		0 to V <sub>CC</sub> (Note 6)		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
	dt/dv	0 to 20 ( $V_{CC}$ = 1.80 V $\pm$ 0.15 V, 2.5 V $\pm$ 0.2 V)		
Input rise and fall time		0 to 10 ( $V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$ )	ns/V	
		0 to 5 ( $V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$ )		

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or low state



### **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol Test Condition		Symbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
		Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit		
High lovel		V			1.65 to 1.95	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	- V
Input voltage	V <sub>IH</sub>		_		V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7	_		
	.,			1.65 to 1.95	_	_	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25		
	Low level	V <sub>IL</sub>	_		2.3 to 5.5	_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	
					1.65	1.55	1.65	_	1.55		
				100	2.3	2.2	2.3	_	2.2	_	
				I <sub>OH</sub> = -100 μA	3.0	2.9	3.0	_	2.9	_	
					4.5	4.4	4.5	_	4.4		
	High level	level V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -4 mA	1.65	1.29	1.52	_	1.29		V
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15	_	1.9		
				$I_{OH} = -16 \text{ mA}$	3.0	2.4	2.8	_	2.4		
				I <sub>OH</sub> = -24 mA	3.0	2.3	2.68	_	2.3		
Output voltage				$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8		
Output voitage		ow level V <sub>OL</sub> \	V <sub>IN</sub> = V <sub>IL</sub>	Ι <sub>ΟL</sub> = 100 μΑ	1.8		0	0.1	_	0.1	
					2.3		0	0.1	_	0.1	
					3.0		0	0.1	_	0.1	
					4.5		0	0.1	_	0.1	
	Low level			I <sub>OL</sub> = 4 mA	1.65		0.08	0.24	_	0.24	
				I <sub>OL</sub> = 8 mA	2.3		0.1	0.3	_	0.3	
				I <sub>OL</sub> = 16 mA	3.0		0.15	0.4	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0		0.22	0.55	_	0.55	
			$I_{OL} = 32 \text{ mA}$	4.5		0.22	0.55	_	0.55		
Input leakage co	urrent	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±1	_	±10	μА
Power off leaka	ge current	loff	V <sub>IN</sub> or V <sub>OL</sub>	<sub>JT</sub> = 5.5 V	0.0			1	_	10	μА
Quiescent supply current I <sub>CC</sub> V <sub>IN</sub> = 5.5 V or GND		1.65 to 5.5	_	_	1	_	10	μА			

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### AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Cumbal	Test Condition		Ta = 25°C			Ta = -40	Unit	
Characteristics	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>	$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	$1.8 \pm 0.15$	2.0	5.8	10.5	2.0	11.0	ns
			$2.5 \pm 0.2$	1.0	3.5	5.8	1.0	6.2	
			$3.3 \pm 0.3$	0.8	2.6	3.9	0.8	4.3	
			$5.0 \pm 0.5$	0.5	1.8	3.1	0.5	3.3	
		C <sub>L</sub> = 50 pF,	$3.3 \pm 0.3$	1.2	3.2	4.8	1.2	5.2	
		$R_L = 500 \Omega$	$5.0 \pm 0.5$	0.8	2.4	3.7	0.8	4.0	
Input capacitance	C <sub>IN</sub>	_	0 to 5.5	_	3.0	_	_	_	pF
Power dissipation capacitance	0	(Noto 7)	3.3	_	20	_	_	_	- pF
	C <sub>PD</sub>	(Note 7)	5.5	_	26	_	_	_	

Note 7: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

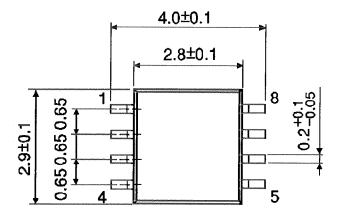
Average operating current can be obtained by the equation:

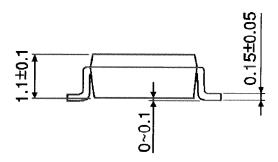
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 



### **Package Dimensions**

SSOP8-P-0.65 Unit: mm



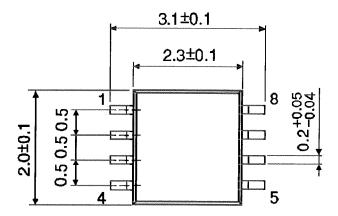


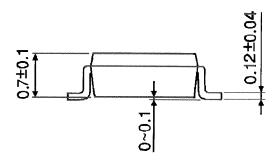
5

Weight: 0.02 g (typ.)

### **Package Dimensions**

SSOP8-P-0.50A Unit: mm





6

Weight: 0.01 g (typ.)

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