TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOS VII)

TK6A65D

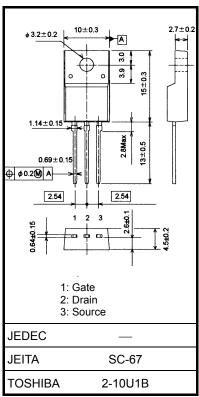
Switching Regulator Applications

Unit: mm

- Low drain-source ON resistance: RDS (ON) = 0.95Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 4.0 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 650 \text{ V)}$
- Enhancement-mode: $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	650	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I _D	6	Α
	Pulse (Note 1)	I_{DP}	24	A
Drain power dissipation	on (Tc = 25°C)	P _D	45	W
Single pulse avalanche energy (Note 2)		E _{AS}	281	mJ
Avalanche current		I _{AR}	6	Α
Repetitive avalanche energy (Note 3)		E _{AR}	4.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



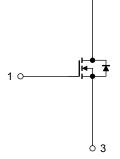
Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Internal Connection



Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 13.8 mH, R_G = 25 Ω , I_{AR} = 6 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

Start of commercial production 2012-01

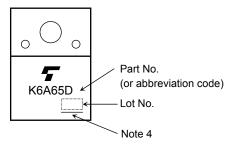
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I _{DSS}	V _{DS} = 650 V, V _{GS} = 0 V	_	_	10	μА
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	650	_	_	٧
Gate threshold ve	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3 A	_	0.95	1.11	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	1.0	4.0	_	S
Input capacitance		C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	1050	_	pF
Reverse transfer capacitance		C _{rss}		_	5	_	
Output capacitance		Coss		_	100	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{I}_D = 3 \text{ A} & \text{Vout} \\ \hline 0 \text{ V} & \text{SO} & \text{SO} & \text{SO} \\ \hline 50 & \text{V} & \text{SO} & \text{SO} \\ \hline \end{array}$ $\begin{array}{c c} R_L = 67 \Omega \\ \hline \end{array}$ $\begin{array}{c c} V_{DD} \approx 200 \text{ V} \\ \hline \end{array}$ Duty $\leq 1\%, \ t_W = 10 \mu\text{s}$		25	_	. ns
	Turn-on time	t _{on}			60		
	Fall time	t _f			10		
	Turn-off time	t _{off}			75		
Total gate charge		Qg		_	20	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$	_	13	_	nC
Gate-drain charge		Q _{gd}		_	7	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

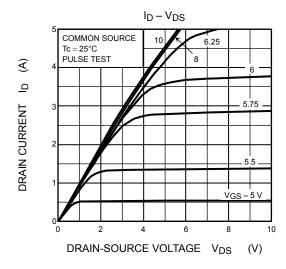
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	6	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	24	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs	_	10	_	μС

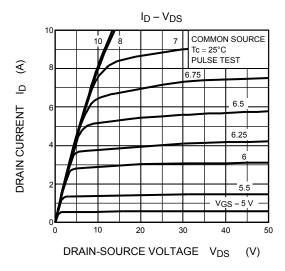
Marking

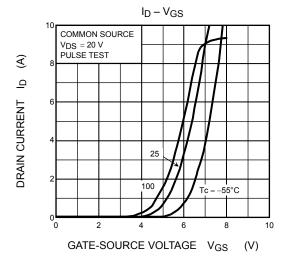


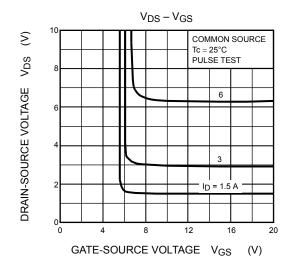
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

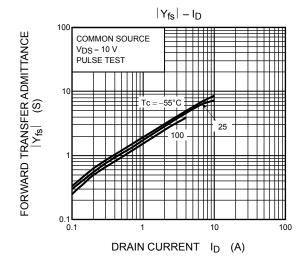
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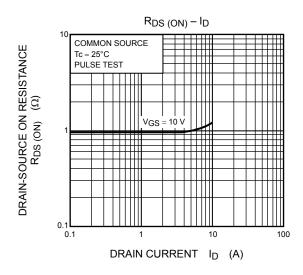


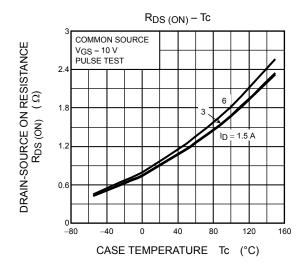


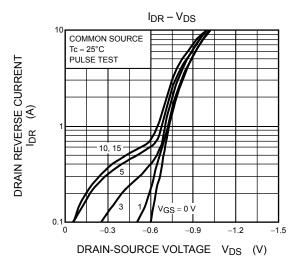


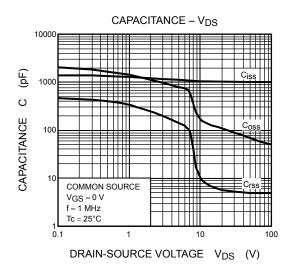


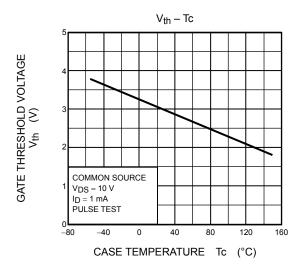


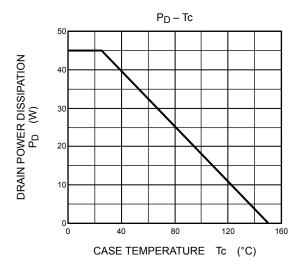


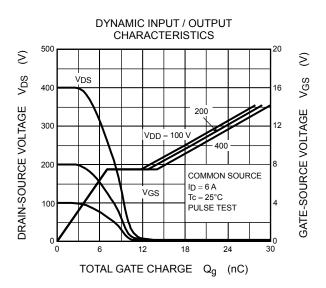


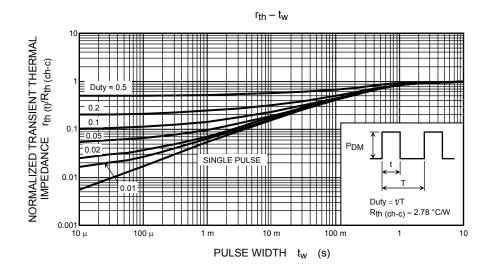


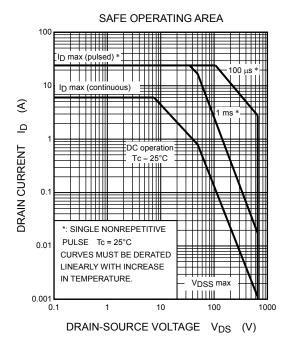


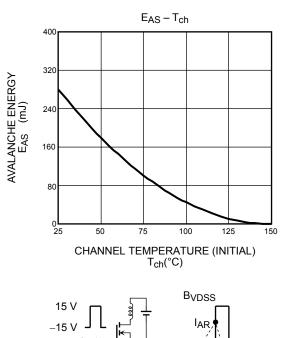


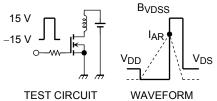












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 13.8~mH \end{aligned} \qquad \text{EAS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right) \end{aligned}$$

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Skype ameyasales1 ameyasales2

Customer Service :

Email service@ameya360.com

Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com