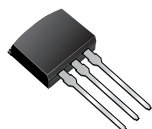
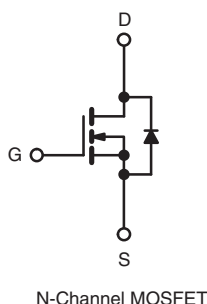
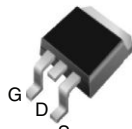


## Power MOSFET

### PRODUCT SUMMARY

|                           |                 |     |
|---------------------------|-----------------|-----|
| $V_{DS}$ (V)              | 900             |     |
| $R_{DS(on)}$ ( $\Omega$ ) | $V_{GS} = 10$ V | 8.0 |
| $Q_g$ (Max.) (nC)         | 38              |     |
| $Q_{gs}$ (nC)             | 4.7             |     |
| $Q_{gd}$ (nC)             | 21              |     |
| Configuration             | Single          |     |

I<sup>2</sup>PAK (TO-262)

D<sup>2</sup>PAK (TO-263)


### FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Surface Mount (IRFBF20S, SiHFBF20S)
- Low-Profile Through-Hole (IRFBF20L, SiHFBF20L)
- Available in Tape and Reel (IRFBF20S, SiHFBF20S)
- Dynamic dV/dt Rating
- 150 °C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Compliant to RoHS Directive 2002/95/EC



**RoHS\***  
COMPLIANT  
**HALOGEN**  
**FREE**  
Available

### DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The D<sup>2</sup>PAK is a surface mount power package capable of accommodating die sizes up to HEX-4. It provides the highest power capability and the lowest possible on-resistance in any existing surface mount package. The D<sup>2</sup>PAK is suitable for high current applications because of its low internal connection resistance and can dissipate up to 2.0 W in a typical surface mount application. The through-hole version (IRFBF20L, SiHFBF20L) is available for low-profile applications.

### ORDERING INFORMATION

| Package                         | D <sup>2</sup> PAK (TO-263) | D <sup>2</sup> PAK (TO-263)   | D <sup>2</sup> PAK (TO-263)   | I <sup>2</sup> PAK (TO-262) |
|---------------------------------|-----------------------------|-------------------------------|-------------------------------|-----------------------------|
| Lead (Pb)-free and Halogen-free | SiHFBF20S-GE3               | SiHFBF20STRL-GE3 <sup>a</sup> | SiHFBF20STRR-GE3 <sup>a</sup> | SiHFBF20L-GE3               |
| Lead (Pb)-free                  | IRFBF20SPbF                 | IRFBF20STRLPbF <sup>a</sup>   | IRFBF20STRRPbF <sup>a</sup>   | IRFBF20LPbF                 |
|                                 | SiHFBF20S-E3                | SiHFBF20STL-E3 <sup>a</sup>   | SiHFBF20STR-E3 <sup>a</sup>   | SiHFBF20L-E3                |

#### Note

a. See device orientation.

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

| PARAMETER  | SYMBOL           | LIMIT            | UNIT |
|--|------------------|------------------|------|
| Drain-Source Voltage <sup>e</sup>                | $V_{DS}$         | 900              | V    |
| Gate-Source Voltage <sup>e</sup>                 | $V_{GS}$         | ± 20             |      |
| Continuous Drain Current                         | $V_{GS}$ at 10 V | $T_C = 25$ °C    | A    |
|  |                  | $T_C = 100$ °C   |      |
| Pulsed Drain Current <sup>a,e</sup>              | $I_{DM}$         | 6.8              |      |
| Linear Derating Factor                           |                  | 0.43             | W/°C |
| Single Pulse Avalanche Energy <sup>b, e</sup>    | $E_{AS}$         | 180              | mJ   |
| Repetitive Avalanche Current <sup>a</sup>        | $I_{AR}$         | 1.7              | A    |
| Repetitive Avalanche Energy <sup>a</sup>         | $E_{AR}$         | 5.4              | mJ   |
| Maximum Power Dissipation                        | $P_D$            | $T_C = 25$ °C    | W    |
|  |                  | $T_A = 25$ °C    |      |
| Peak Diode Recovery dV/dt <sup>c, e</sup>        | dV/dt            | 1.5              | V/ns |
| Operating Junction and Storage Temperature Range | $T_J, T_{stg}$   | - 55 to + 150    | °C   |
| Soldering Recommendations (Peak Temperature)     | for 10 s         | 300 <sup>d</sup> |      |
| Mounting Torque                                  | 6-32 or M3 screw | 10               | N    |

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b.  $V_{DD} = 50$  V; starting  $T_J = 25$  °C,  $L = 117$  mH,  $R_g = 25$   $\Omega$ ,  $I_{AS} = 1.7$  A (see fig. 12).

c.  $I_{SD} \leq 1.7$  A,  $dI/dt \leq 70$  A/ $\mu$ s,  $V_{DD} \leq V_{DS}$ ,  $T_J \leq 150$  °C.

d. 1.6 mm from case.

e. Uses IRFBF20, SiHFBF20 data and test conditions.

\* Pb containing terminations are not RoHS compliant, exemptions may apply

**THERMAL RESISTANCE RATINGS**

| PARAMETER  | SYMBOL     | TYP. | MAX. | UNIT |
|--|------------|------|------|------|
| Maximum Junction-to-Ambient (PCB Mounted, steady-state) <sup>a</sup> | $R_{thJA}$ | -    | 40   | °C/W |
| Maximum Junction-to-Case   | $R_{thJC}$ | -    | 2.3  |      |


**Note**

a. When mounted on 1" square PCB (FR-4 or G-10 material).

**SPECIFICATIONS** ( $T_J = 25\text{ °C}$ , unless otherwise noted)

| PARAMETER                               | SYMBOL                           | TEST CONDITIONS   |  | MIN. | TYP. | MAX.  | UNIT  |
|---|----------------------------------|---|--|------|------|-------|-------|
| Static                                  |                                  |   |  |      |      |       |       |
| Drain-Source Breakdown Voltage          | V <sub>DS</sub>                  | V <sub>GS</sub> = 0, I <sub>D</sub> = 250 μA  |  | 900  | -    | -     | V     |
| V <sub>DS</sub> Temperature Coefficient | ΔV <sub>DS</sub> /T <sub>J</sub> | Reference to 25 °C, I <sub>D</sub> = 1 mA   |  | -    | 1.1  | -     | mV/°C |
| Gate-Source Threshold Voltage           | V <sub>GS(th)</sub>              | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA   |  | 2.0  | -    | 4.0   | V     |
| Gate-Source Leakage                     | I <sub>GSS</sub>                 | V <sub>GS</sub> = ± 20 V  |  | -    | -    | ± 100 | nA    |
| Zero Gate Voltage Drain Current         | I <sub>DSS</sub>                 | V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V  |  | -    | -    | 100   | μA    |
|   |                                  | V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C   |  | -    | -    | 500   |       |
| Drain-Source On-State Resistance        | R <sub>DS(on)</sub>              | V <sub>GS</sub> = 10 V  | I <sub>D</sub> = 1.0 A <sup>b</sup>  | -    | -    | 8.0   | Ω     |
| Forward Transconductance                | g <sub>fs</sub>                  | V <sub>DS</sub> = 50 V, I <sub>D</sub> = 1.0 A <sup>b</sup>   |  | 0.6  | -    | -     | S     |
| Dynamic                                 |                                  |   |  |      |      |       |       |
| Input Capacitance                       | C <sub>iss</sub>                 | V <sub>GS</sub> = 0 V,<br>V <sub>DS</sub> = 25 V,<br>f = 1.0 MHz, see fig. 5  |  | -    | 490  | -     | pF    |
| Output Capacitance                      | C <sub>oss</sub>                 |   |  | -    | 55   | -     |       |
| Reverse Transfer Capacitance            | C <sub>rss</sub>                 |   |  | -    | 18   | -     |       |
| Total Gate Charge                       | Q <sub>g</sub>                   | V <sub>GS</sub> = 10 V  | I <sub>D</sub> = 1.7 A, V <sub>DS</sub> = 360 V,<br>see fig. 6 and 13 <sup>b</sup> | -    | -    | 38    | nC    |
| Gate-Source Charge                      | Q <sub>gs</sub>                  |   |  | -    | -    | 4.7   |       |
| Gate-Drain Charge                       | Q <sub>gd</sub>                  |   |  | -    | -    | 21    |       |
| Turn-On Delay Time                      | t <sub>d(on)</sub>               | V <sub>DD</sub> = 450 V, I <sub>D</sub> = 1.7 A,<br>R <sub>g</sub> = 18 Ω, V <sub>GS</sub> = 10 V, see fig. 10 <sup>b</sup> |  | -    | 8.0  | -     | ns    |
| Rise Time                               | t <sub>r</sub>                   |   |  | -    | 21   | -     |       |
| Turn-Off Delay Time                     | t <sub>d(off)</sub>              |   |  | -    | 56   | -     |       |
| Fall Time                               | t <sub>f</sub>                   |   |  | -    | 32   | -     |       |

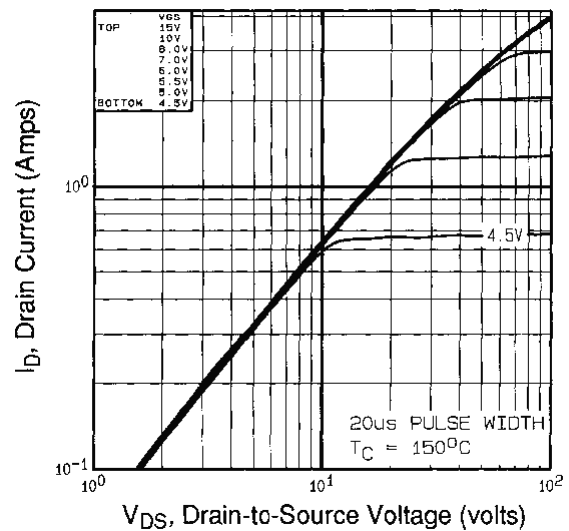
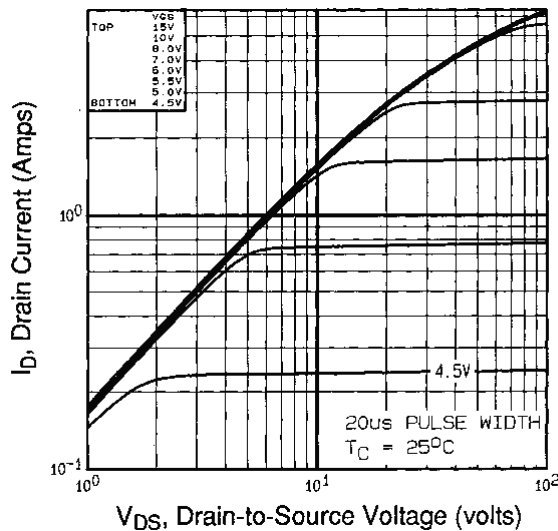


| SPECIFICATIONS ( $T_J = 25^\circ\text{C}$ , unless otherwise noted) |          |   |      |      |      |               |
|---|----------|---|------|------|------|---------------|
| PARAMETER   | SYMBOL   | TEST CONDITIONS   | MIN. | TYP. | MAX. | UNIT          |
| Drain-Source Body Diode Characteristics                             |          |   |      |      |      |               |
| Continuous Source-Drain Diode Current                               | $I_S$    | MOSFET symbol showing the integral reverse p - n junction diode  | -    | -    | 1.7  | A             |
| Pulsed Diode Forward Current <sup>a</sup>                           | $I_{SM}$ |   | -    | -    | 6.8  |               |
| Body Diode Voltage  | $V_{SD}$ | $T_J = 25^\circ\text{C}$ , $I_S = 1.7\text{ A}$ , $V_{GS} = 0\text{ V}^b$   | -    | -    | 1.5  | V             |
| Body Diode Reverse Recovery Time                                    | $t_{rr}$ | $T_J = 25^\circ\text{C}$ , $I_F = 1.7\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}^b$  | -    | 350  | 530  | ns            |
| Body Diode Reverse Recovery Charge                                  | $Q_{rr}$ |   | -    | 0.85 | 1.3  | $\mu\text{C}$ |
| Forward Turn-On Time  | $t_{on}$ | Intrinsic turn-on time is negligible (turn-on is dominated by $L_S$ and $L_D$ )   |      |      |      |               |

## Notes

- Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- Pulse width  $\leq 300\text{ }\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- Uses IRFBF20/SiHFBF20 data and test conditions.

## TYPICAL CHARACTERISTICS ( $25^\circ\text{C}$ , unless otherwise noted)



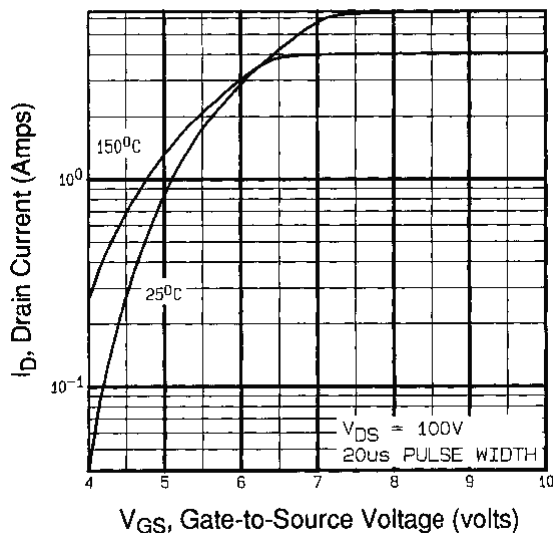


Fig. 3 - Typical Transfer Characteristics

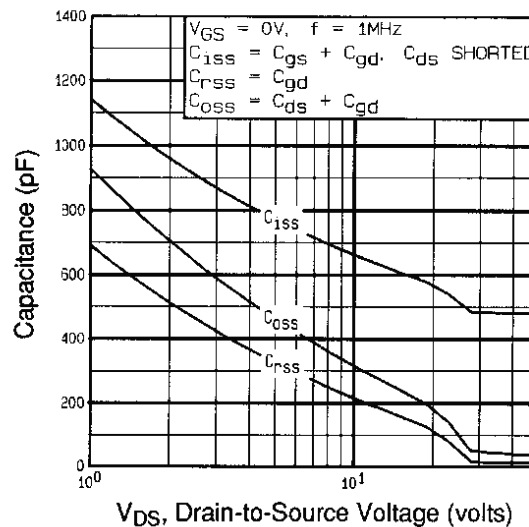


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

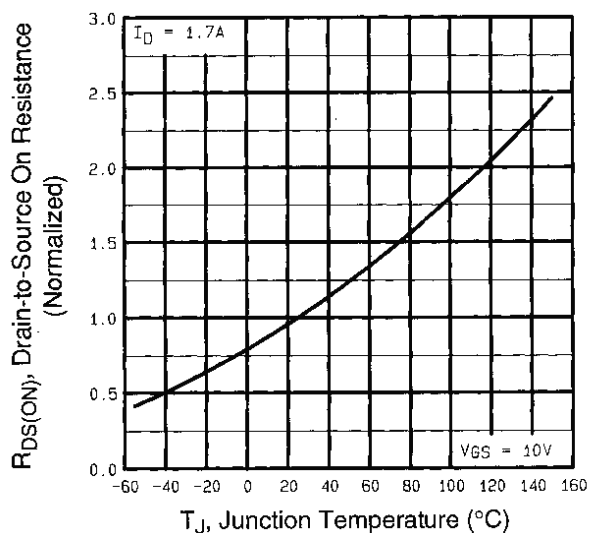


Fig. 4 - Normalized On-Resistance vs. Temperature

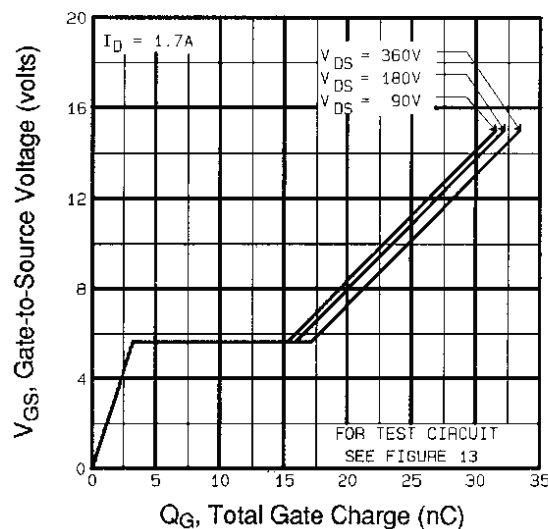


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

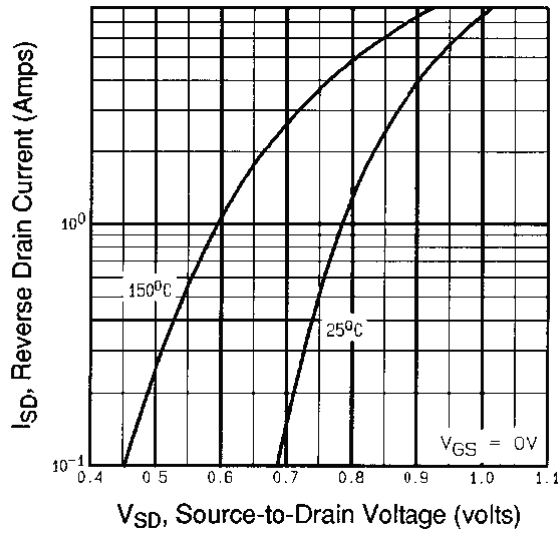


Fig. 7 - Typical Source-Drain Diode Forward Voltage

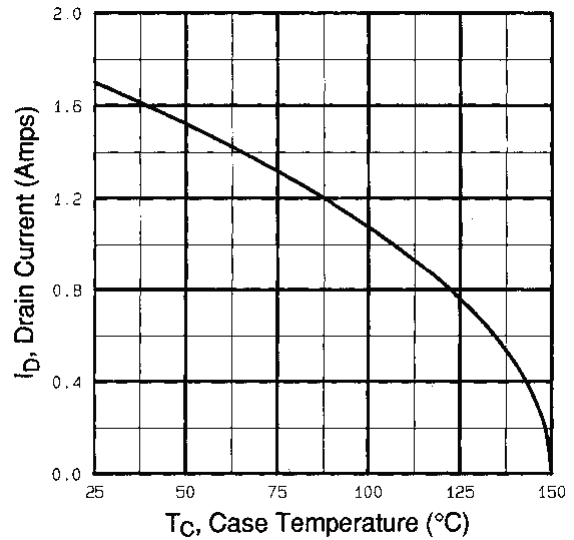


Fig. 9 - Maximum Drain Current vs. Case Temperature

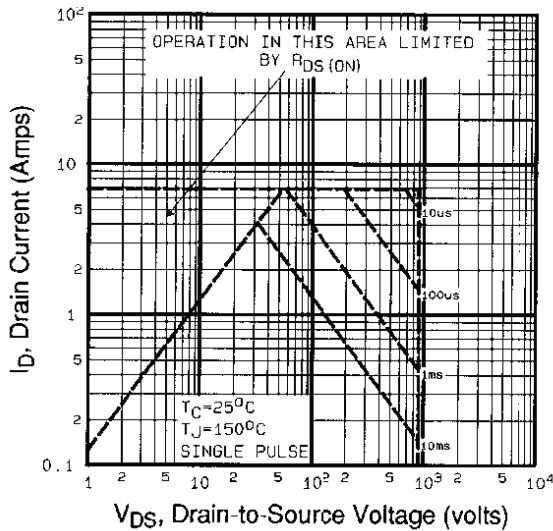


Fig. 8 - Maximum Safe Operating Area

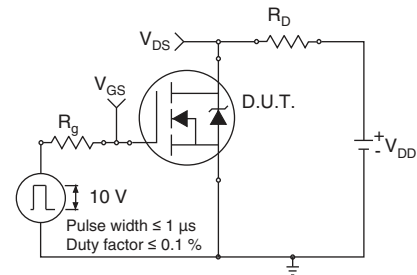


Fig. 10a - Switching Time Test Circuit

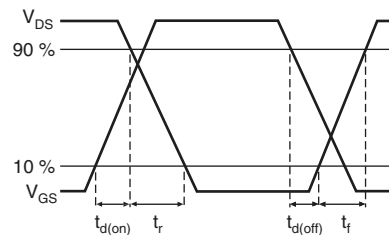


Fig. 10b - Switching Time Waveforms

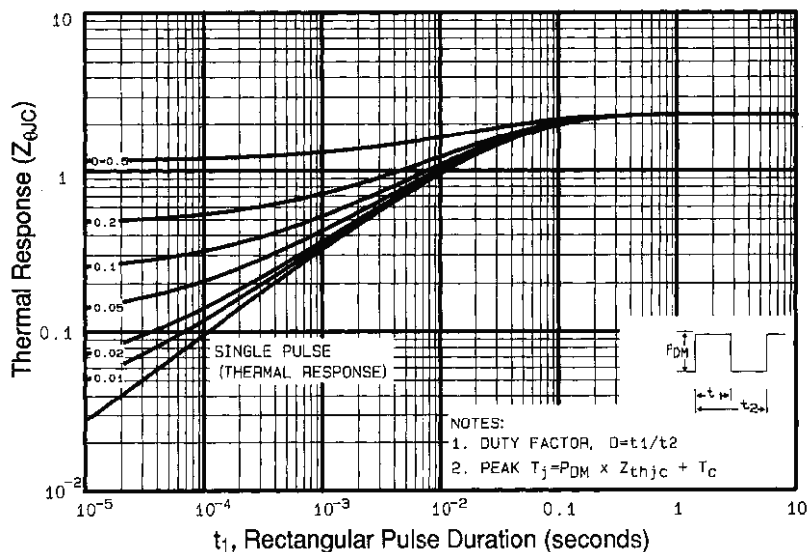


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

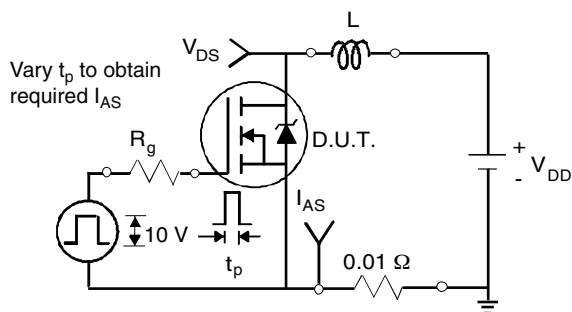


Fig. 12a - Unclamped Inductive Test Circuit

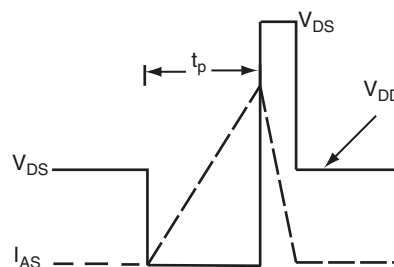


Fig. 12b - Unclamped Inductive Waveforms

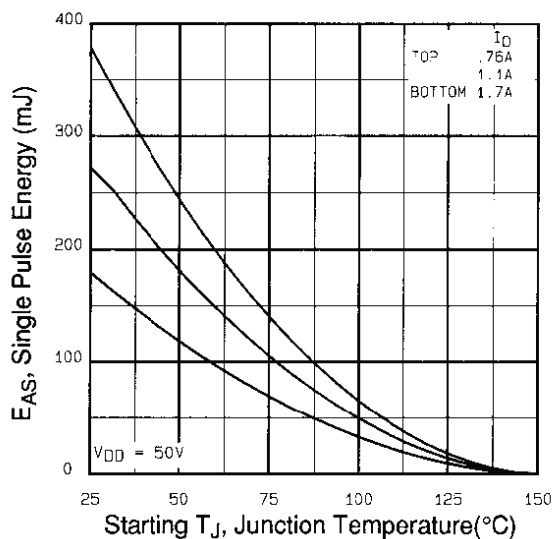


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

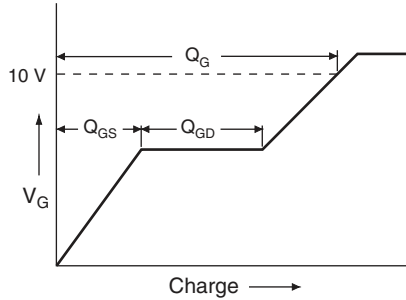


Fig. 13a - Basic Gate Charge Waveform

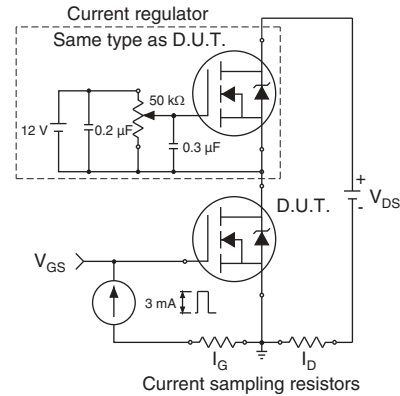
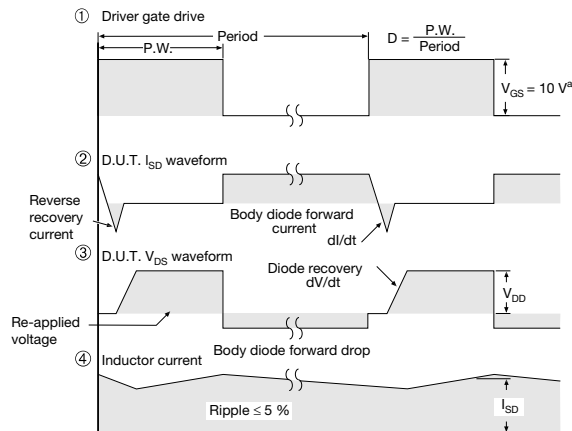
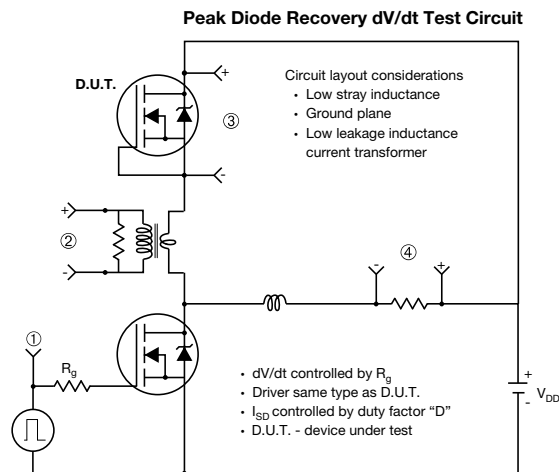


Fig. 13b - Gate Charge Test Circuit



Note  
a.  $V_{GS} = 5\text{ V}$  for logic level devices

Fig. 14 - For N-Channel

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|      | MILLIMETERS |       | INCHES    |       |
|------|-------------|-------|-----------|-------|
| DIM. | MIN.        | MAX.  | MIN.      | MAX.  |
| D1   | 6.86        | -     | 0.270     | -     |
| E    | 9.65        | 10.67 | 0.380     | 0.420 |
| E1   | 6.22        | -     | 0.245     | -     |
| e    | 2.54 BSC    |       | 0.100 BSC |       |
| H    | 14.61       | 15.88 | 0.575     | 0.625 |
| L    | 1.78        | 2.79  | 0.070     | 0.110 |
| L1   | -           | 1.65  | -         | 0.066 |
| L2   | -           | 1.78  | -         | 0.070 |
| L3   | 0.25 BSC    |       | 0.010 BSC |       |
| L4   | 4.78        | 5.28  | 0.188     | 0.208 |



**RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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