


**FCX558**

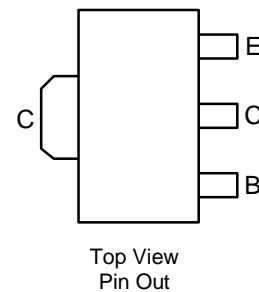
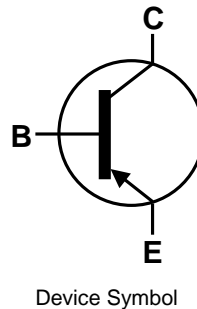
**400V PNP HIGH VOLTAGE TRANSISTOR IN SOT89**

**Features**

- $BV_{CEO} > -400V$
- $I_C = -200mA$  High Continuous Current
- $I_{CM} = -500mA$  Peak Pulse Current
- Excellent  $h_{FE}$  Characteristics up to  $-100mA$
- Low Saturation Voltage  $V_{CE(sat)} < -200mV$  @  $-20mA$
- Complementary NPN Type: FCX458
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

**Mechanical Data**

- Case: SOT89
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 
- Weight: 0.05 grams (Approximate)

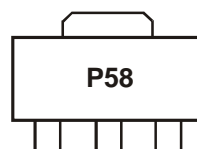


**Ordering Information** (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
FCX558TA	AEC-Q101	P58	7	12	1,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

**Marking Information**



P58 = Product Type Marking Code

**Absolute Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Limit	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-400	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-400	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	I <sub>C</sub>	-200	mA
Peak Pulse Current	I <sub>CM</sub>	-500	mA

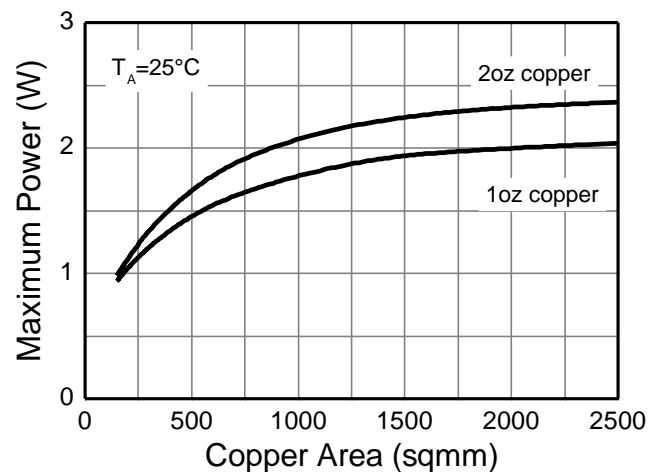
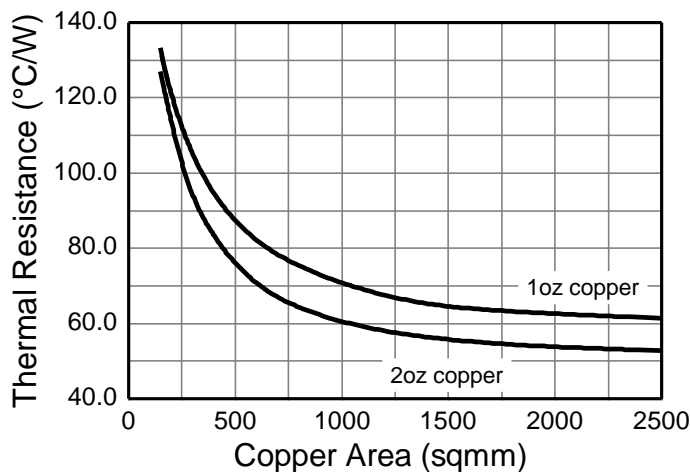
**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	P <sub>D</sub>	0.7	W
		1.0	
		1.5	
		2.0	
Thermal Resistance, Junction to Ambient Air	R <sub>θJA</sub>	178	°C/W
		125	
		83	
		60	
Thermal Resistance, Junction to Lead	R <sub>θJL</sub>	22	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

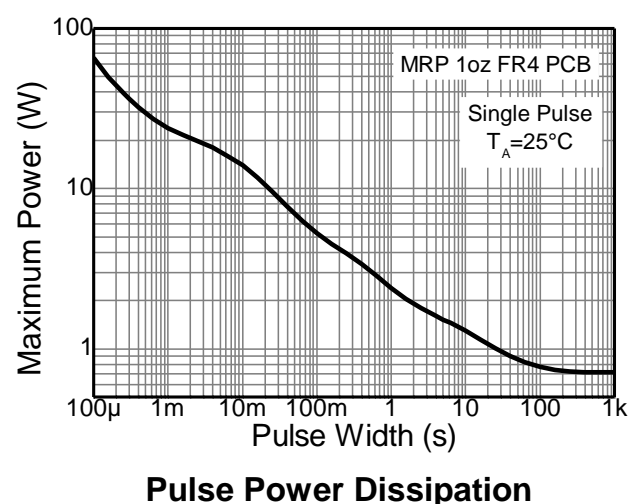
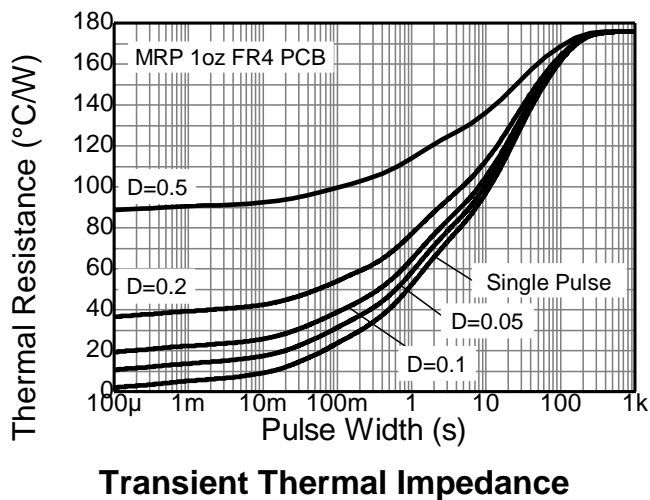
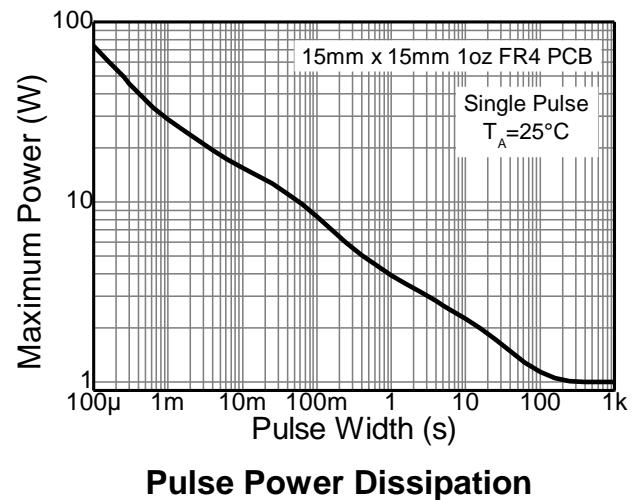
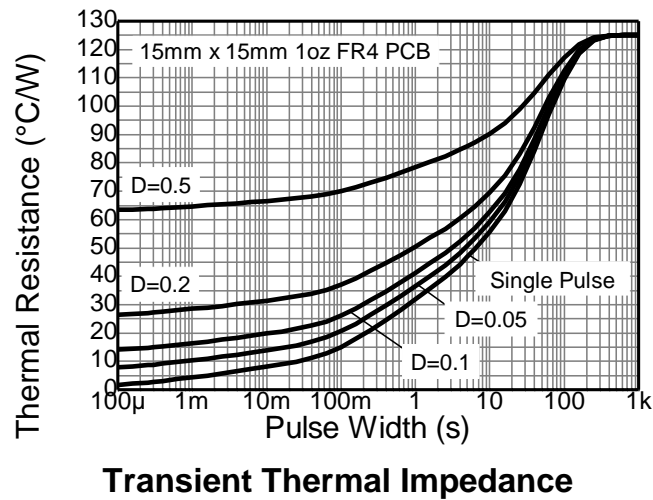
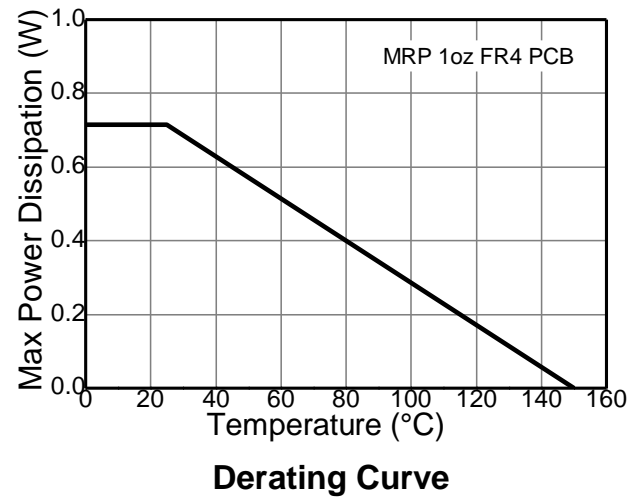
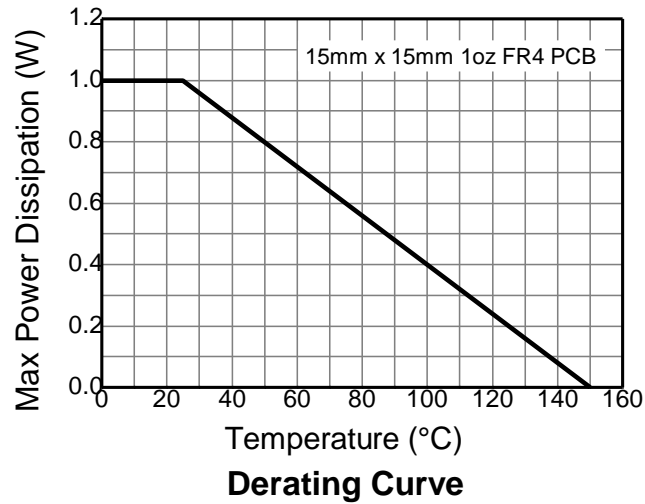
**ESD Ratings** (Note 10)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes:
- For a device mounted with the exposed collector pad on minimum recommended pad layout (MRP) 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
  - Same as Note 5, except the device is mounted with the exposed collector pad on 15mm x 15mm 1oz copper.
  - Same as Note 5, except the device is mounted with the exposed collector pad on 25mm x 25mm 1oz copper.
  - Same as Note 5, except the device is mounted with the exposed collector pad on 50mm x 50mm 1oz copper.
  - Thermal resistance from junction to solder-point (on the exposed collector pad).
  - Refer to JEDEC specification JESD22-A114 and JESD22-A115.

**Thermal Characteristics and Derating Information**


**Thermal Characteristics and Derating Information (cont.)**

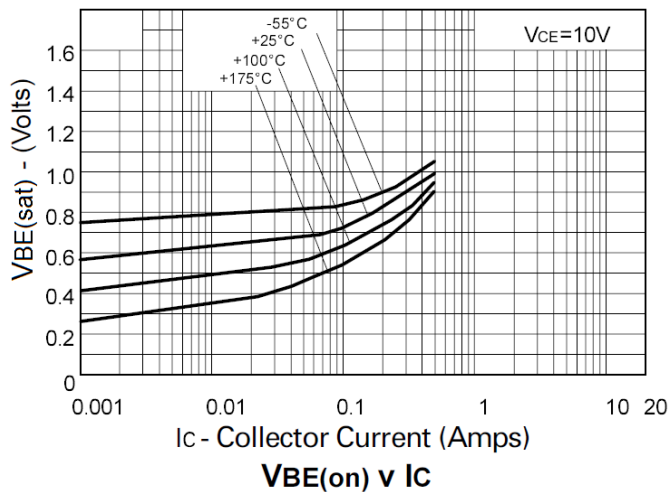
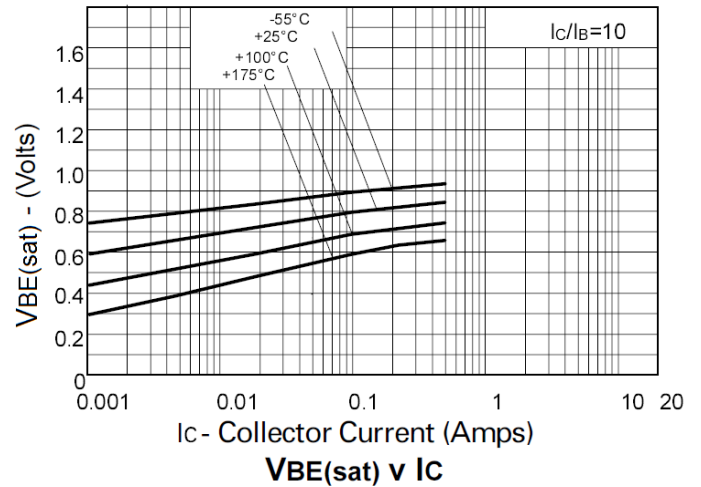
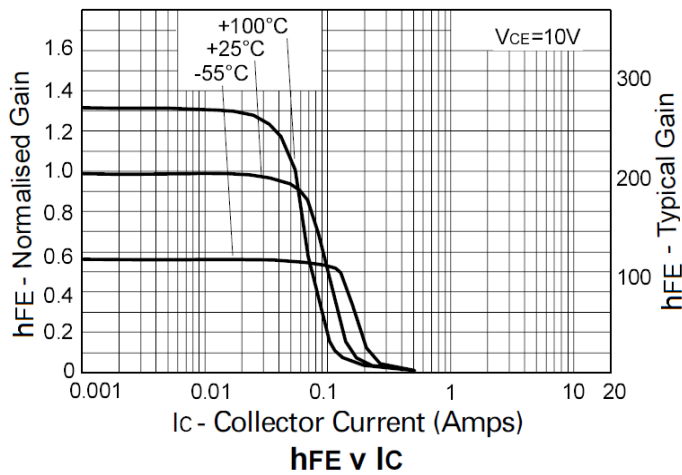
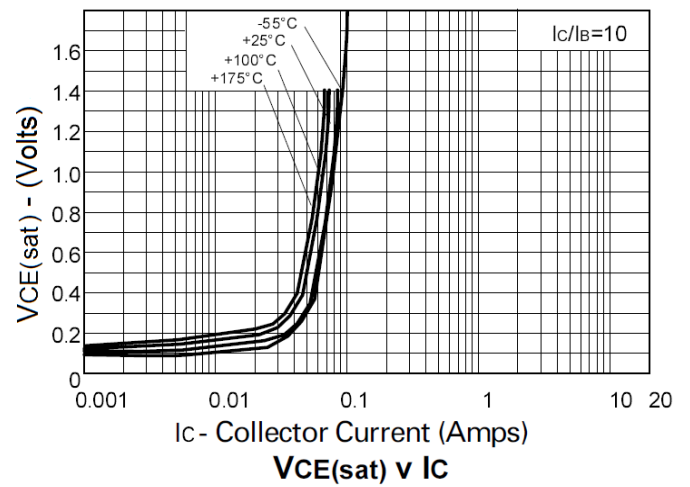
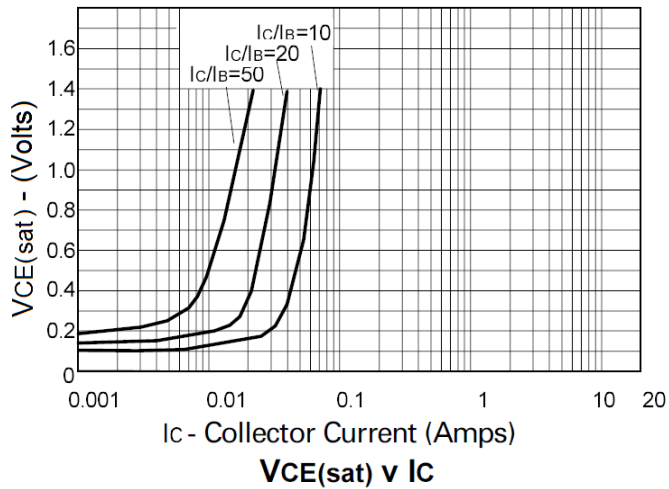


**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ.	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-400	-	-	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage (Note 11)	BV <sub>CEO</sub>	-400	-	-	V	I <sub>C</sub> = -1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-7	-	-	V	I <sub>E</sub> = -100μA
Collector Cutoff Current	I <sub>CBO</sub>	-	<1	-100	nA	V <sub>CB</sub> = -320V
Emitter Cutoff Current	I <sub>EBO</sub>	-	<1	-100	nA	V <sub>EB</sub> = -5V
Emitter Cutoff Current	I <sub>CES</sub>	-	<1	-100	nA	V <sub>CES</sub> = -320V
DC Current Transfer Static Ratio (Note 11)	h <sub>FE</sub>	100	-	-	-	I <sub>C</sub> = -1mA, V <sub>CE</sub> = -10V
		100	-	300		I <sub>C</sub> = -50mA, V <sub>CE</sub> = -10V
		15	-	-		I <sub>C</sub> = -100mA, V <sub>CE</sub> = -10V
Collector-Emitter Saturation Voltage (Note 11)	V <sub>CE(sat)</sub>	-	-	-0.2 -0.5	V	I <sub>C</sub> = -20mA, I <sub>B</sub> = -2mA I <sub>C</sub> = -50mA, I <sub>B</sub> = -6mA
Base-Emitter Saturation Voltage (Note 11)	V <sub>BE(sat)</sub>	-	-	-0.9	V	I <sub>C</sub> = -50mA, I <sub>B</sub> = -5mA
Base-Emitter Turn-on Voltage (Note 11)	V <sub>BE(on)</sub>	-	-	-0.9	V	I <sub>C</sub> = -50mA, V <sub>CE</sub> = -10V
Transitional Frequency	f <sub>T</sub>	50	-	-	MHz	I <sub>E</sub> = -10mA, V <sub>CE</sub> = -20V f = 20MHz
Output Capacitance	C <sub>obo</sub>	-	-	5	pF	V <sub>CB</sub> = -20V, f = 1MHz,
Switching Times	t <sub>on</sub>	-	95	-	nS	I <sub>C</sub> = -50mA, V <sub>C</sub> = -100V I <sub>B1</sub> = -5mA, I <sub>B2</sub> = -10mA
	t <sub>off</sub>		1600			

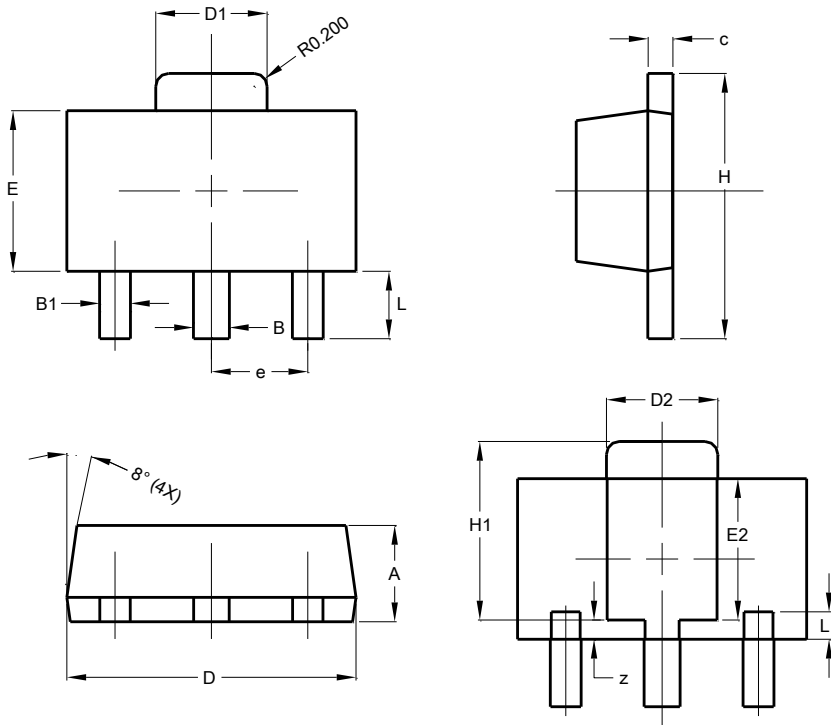
Note: 11. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)



## Package Outline Dimensions

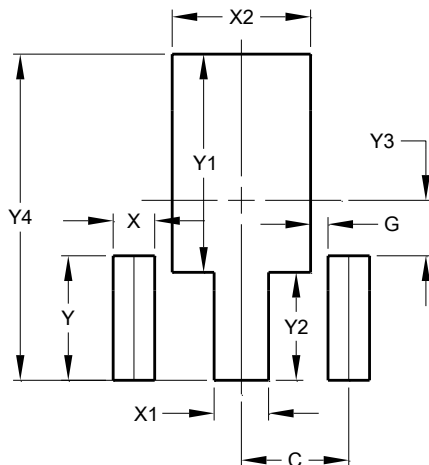
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



SOT89			
Dim	Min	Max	Typ
A	1.40	1.60	1.50
B	0.50	0.62	0.56
B1	0.42	0.54	0.48
c	0.35	0.43	0.38
D	4.40	4.60	4.50
D1	1.62	1.83	1.733
D2	1.61	1.81	1.71
E	2.40	2.60	2.50
E2	2.05	2.35	2.20
e	-	-	1.50
H	3.95	4.25	4.10
H1	2.63	2.93	2.78
L	0.90	1.20	1.05
L1	0.427 REF		
Z	0.30 REF		
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	1.500
G	0.244
X	0.580
X1	0.760
X2	1.933
Y	1.730
Y1	3.030
Y2	1.500
Y3	0.770
Y4	4.530

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.

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