

NTJS4405N, NVJS4405N

Small Signal MOSFET

25 V, 1.2 A, Single, N-Channel, SC-88

Features

- Advance Planar Technology for Fast Switching, Low $R_{DS(on)}$
- Higher Efficiency Extending Battery Life
- AEC-Q101 Qualified and PPAP Capable – NVJS4405N
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Boost and Buck Converter
- Load Switch
- Battery Protection

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	25	V
Gate-to-Source Voltage			V _{GS}	± 8.0	V
Drain Current	t < 5 s	T _A = 25°C	I _D	1.2	A
Continuous Drain Current (Note 1)	Steady State	T _A = 25°C	I _D	1.0	A
		T _A = 75°C		0.80	
Power Dissipation (Note 1)	Steady State		P _D	0.63	W
Power Dissipation (Note 1)	t ≤ 5 s		P _D	0.89	W
Pulsed Drain Current	t _p = 10 μs		I _{DM}	3.7	A
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to +150	°C
Source Current (Body Diode) (Note 1)			I _S	0.8	A
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			T _L	260	°C
ESD Rating – Machine Model				25	V

THERMAL RESISTANCE RATINGS

Rating	Symbol	Max	Unit
Junction-to-Lead – Steady State (Note 1)	$R_{\theta JL}$	102	$^\circ\text{C/W}$
Junction-to-Ambient – Steady State (Note 1)	$R_{\theta JA}$	200	
Junction-to-Ambient – $t \leq 5 \text{ s}$ (Note 1)	$R_{\theta JA}$	140	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

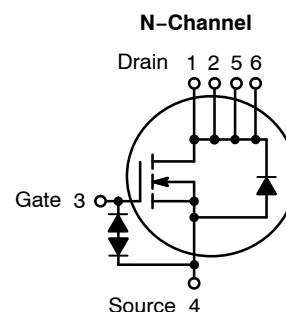
1. Surface mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces).



ON Semiconductor®

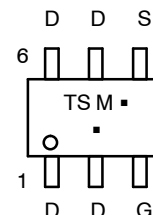
<http://onsemi.com>

$V_{(BR)DS}$	$R_{DS(on)}$ Typ	I_D Max
25 V	249 m Ω @ 4.5 V	1.2 A
	299 m Ω @ 2.7 V	



MARKING DIAGRAM & PIN ASSIGNMENT


SC-88/SOT-363
CASE 419B



TS = Device Code
M = Date Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NTJS4405NT1G	SC-88 (Pb-Free)	3000 / Tape & Reel
NVJS4405NT1G	SC-88 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NTJS4405N, NVJS4405N

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	25			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J			30		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, V _{DS} = 20 V	T _J = 25°C		1.0	μA
			T _J = 125°C		10	
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = 8.0 V			100	nA

ON CHARACTERISTICS (Note 2)

Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250 μA	0.65		1.5	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J			-2.0		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 0.6 A		249	350	mΩ
		V _{GS} = 2.7 V, I _D = 0.2 A		299	400	
		V _{GS} = 4.5 V, I _D = 1.2 A		260		
Forward Transconductance	g _{FS}	V _{DS} = 5.0 V, I _D = 0.5 A		0.5		S

CHARGES AND CAPACITANCES

Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = 10 V		49	60	pF
Output Capacitance	C _{OSS}			22.4	30	
Reverse Transfer Capacitance	C _{RSS}			8.0	12	
Total Gate Charge	Q _{G(TOT)}	V _{GS} = 4.5 V, V _{DS} = 5.0 V, I _D = 0.95 A		0.75	1.5	nC
Threshold Gate Charge	Q _{G(TH)}			0.10		
Gate-to-Source Charge	Q _{GS}			0.30	0.50	
Gate-to-Drain Charge	Q _{GD}			0.20	0.40	

SWITCHING CHARACTERISTICS (Note 3)

Turn-On Delay Time	t _{d(ON)}	V _{GS} = 4.5 V, V _{DS} = 6.0 V, I _D = 0.5 A, R _G = 50 Ω		6.0	12	ns
Rise Time	t _r			4.7	8.0	
Turn-Off Delay Time	t _{d(OFF)}			25	35	
Fall Time	t _f			41	60	

DRAIN-SOURCE DIODE CHARACTERISTICS

Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, I _S = 0.6 A	T _J = 25°C		0.82	1.20	V
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- Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES ($T_J = 25^\circ\text{C}$ unless otherwise noted)

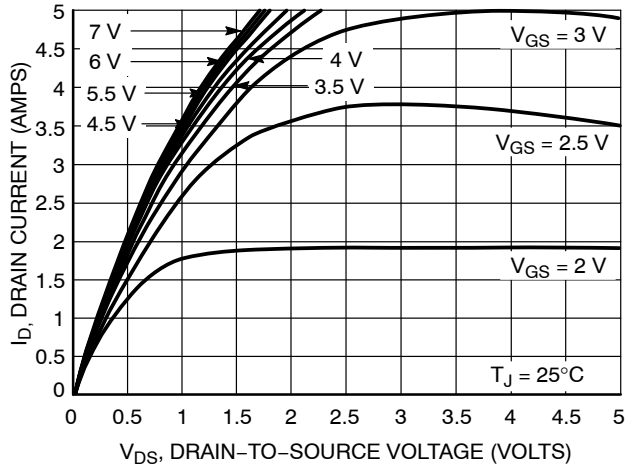


Figure 1. On-Region Characteristics

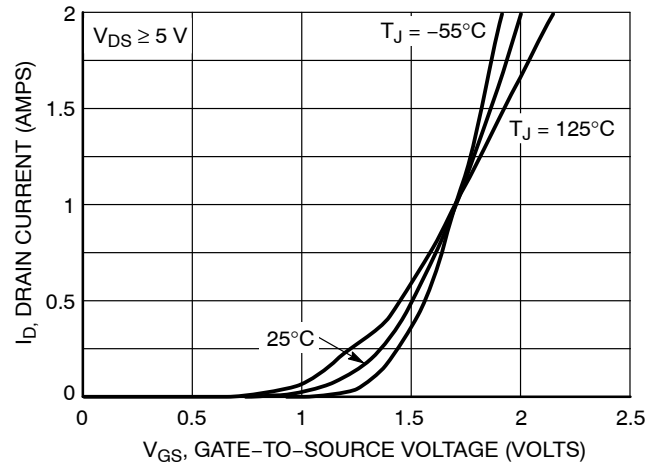


Figure 2. Transfer Characteristics

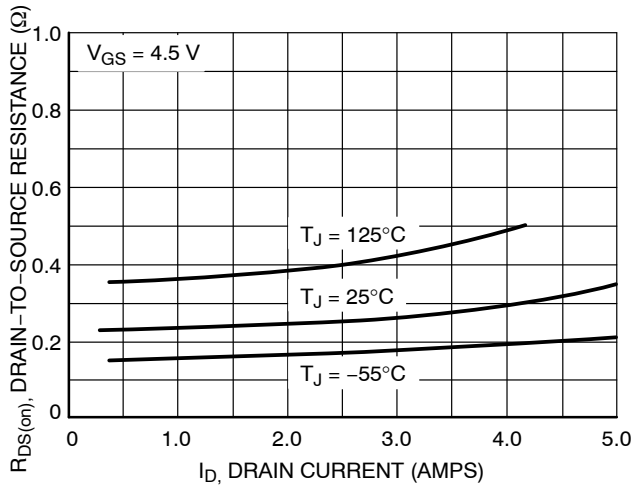


Figure 3. On-Resistance vs. Drain Current and Temperature

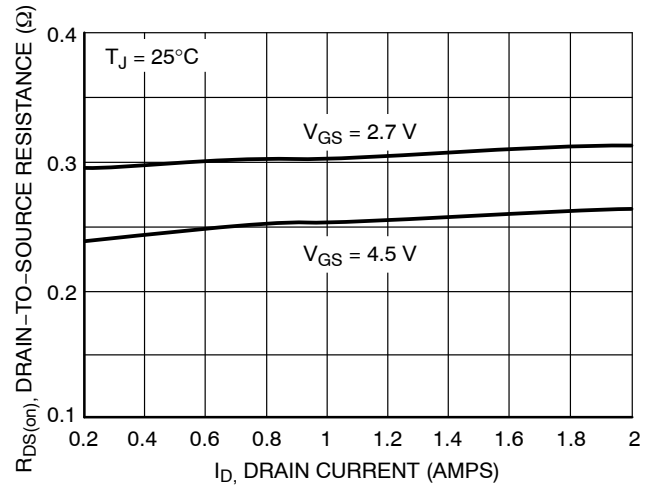


Figure 4. On-Resistance vs. Drain Current and Gate Voltage

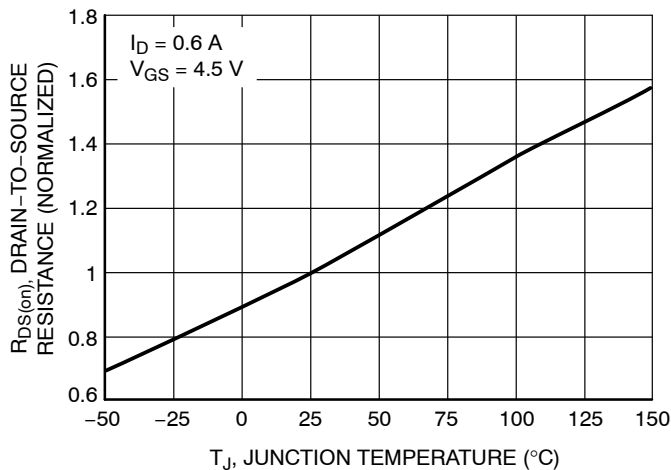


Figure 5. On-Resistance Variation with Temperature

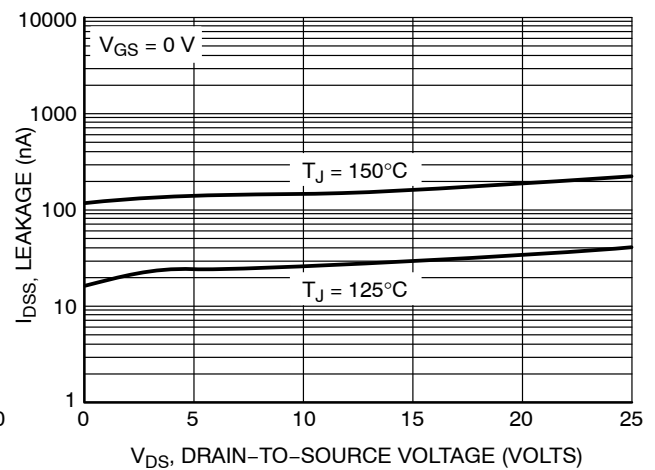
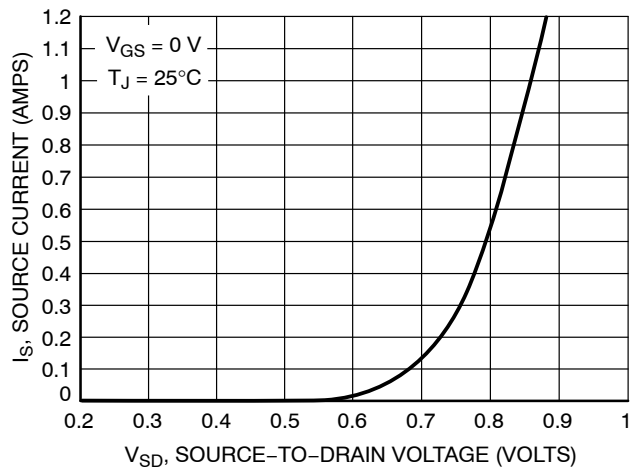
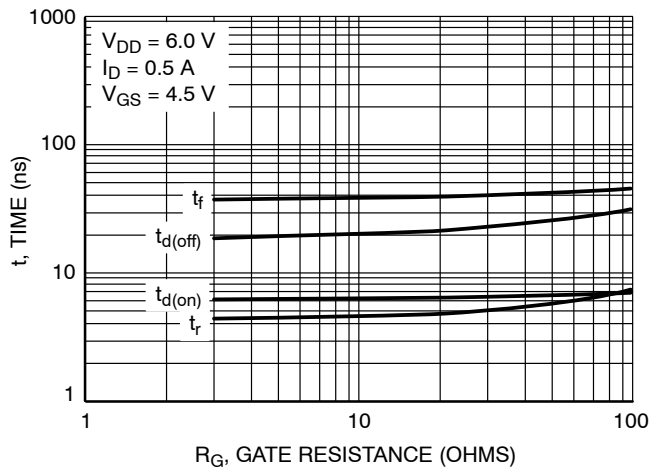
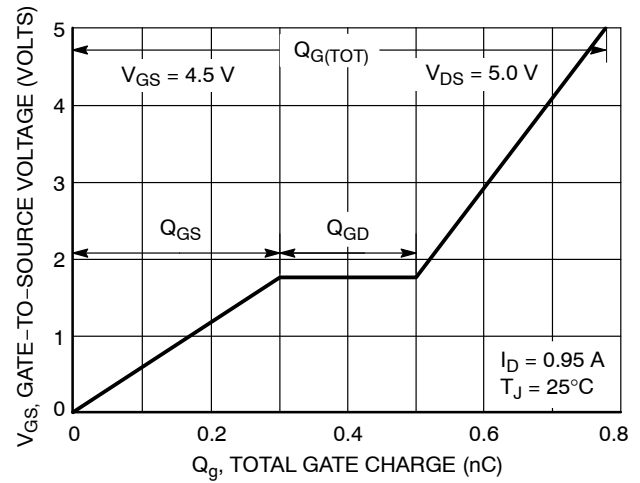
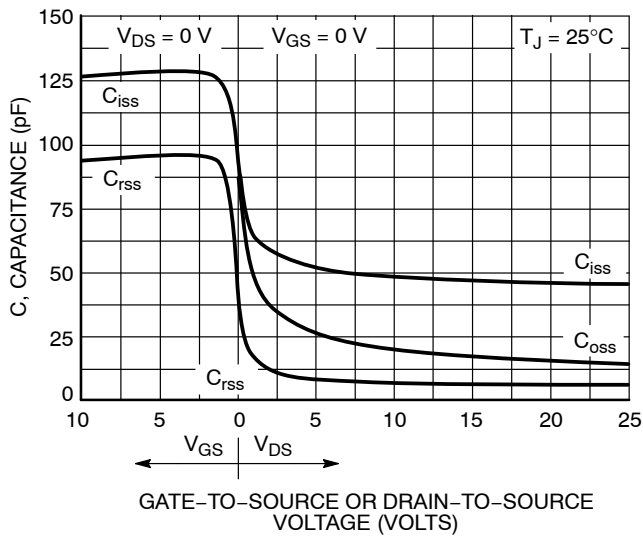


Figure 6. Drain-to-Source Leakage Current vs. Voltage

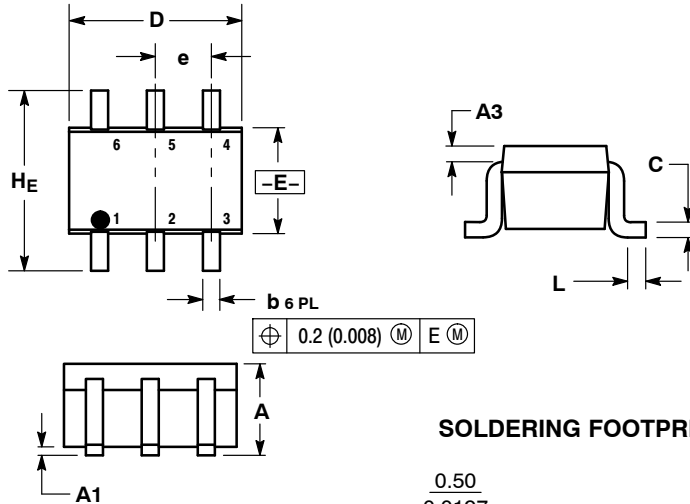
TYPICAL PERFORMANCE CURVES ($T_J = 25^\circ\text{C}$ unless otherwise noted)



NTJS4405N, NVJS4405N

PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363
CASE 419B-02
ISSUE W

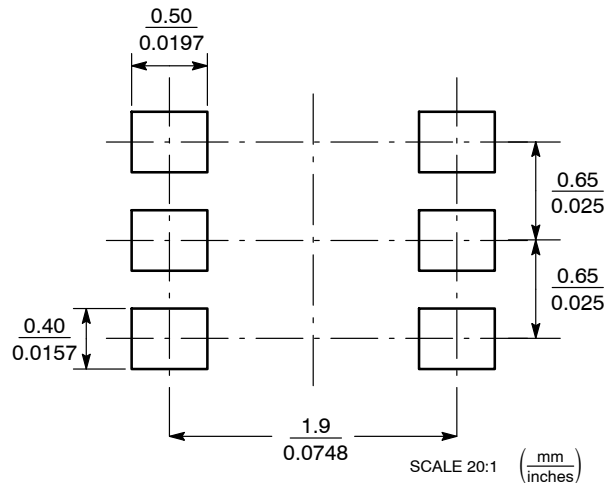


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
C	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
E	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65 BSC			0.026 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	2.00	2.10	2.20	0.078	0.082	0.086

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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