

# FDB33N25

## N-Channel UniFET™ MOSFET

250 V, 33 A, 94 mΩ



### Features

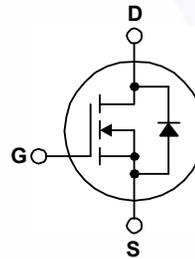
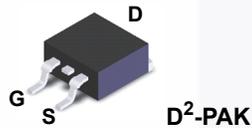
- $R_{DS(on)} = 94 \text{ m}\Omega$  (Max.) @  $V_{GS} = 10 \text{ V}$ ,  $I_D = 16.5 \text{ A}$
- Low Gate Charge (Typ. 36.8 nC)
- Low  $C_{rss}$  (Typ. 39 pF)
- 100% Avalanche Tested

### Applications

- PDP TV
- Lighting
- Uninterruptible Power Supply
- AC-DC Power Supply

### Description

UniFET™ MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter		FDB33N25	Unit
$V_{DSS}$	Drain-Source Voltage		250	V
$I_D$	Drain Current	- Continuous ( $T_C = 25^\circ\text{C}$ )	33	A
		- Continuous ( $T_C = 100^\circ\text{C}$ )	20.4	A
$I_{DM}$	Drain Current	- Pulsed (Note 1)	132	A
$V_{GSS}$	Gate-Source voltage		$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)		918	mJ
$I_{AR}$	Avalanche Current (Note 1)		33	A
$E_{AR}$	Repetitive Avalanche Energy (Note 1)		23.5	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$ (Note 3)		4.5	V/ns
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	235	W
		- Derate Above $25^\circ\text{C}$	1.89	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	FDB33N25	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.53	$^\circ\text{C}/\text{W}$
$R_{\theta JA}^*$	Thermal Resistance, Junction-to-Ambient (1 in <sup>2</sup> Pad of 2-oz Copper), Max.	40	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	

## Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDB33N25TM	FDB33N25	D <sup>2</sup> -PAK	Tape and Reel	330 mm	24 mm	800 units

## Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted.

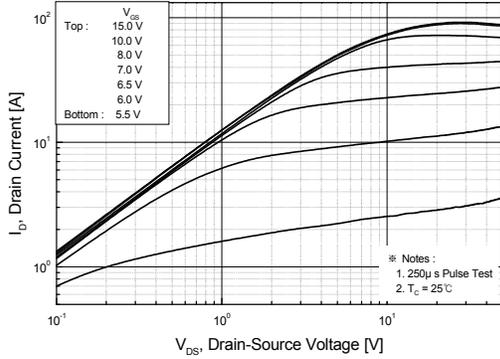
Symbol	Parameter	Conditions	Min.	Typ.	Max	Unit
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$	250	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\ \mu\text{A}$ , Referenced to $25^\circ\text{C}$	--	0.25	--	$\text{V}/^\circ\text{C}$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 250\text{ V}, V_{GS} = 0\text{ V}$ $V_{DS} = 200\text{ V}, T_C = 125^\circ\text{C}$	--	--	1 10	$\mu\text{A}$ $\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
$I_{GSSR}$	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	3.0	--	5.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 16.5\text{ A}$	--	0.077	0.094	$\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 40\text{ V}, I_D = 16.5\text{ A}$	--	26.6	--	S
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$	--	1640	2135	pF
$C_{oss}$	Output Capacitance		--	330	430	pF
$C_{riss}$	Reverse Transfer Capacitance		--	39	59	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 125\text{ V}, I_D = 33\text{ A},$ $V_{GS} = 10\text{ V}, R_G = 25\ \Omega$	--	35	80	ns
$t_r$	Turn-On Rise Time		--	230	470	ns
$t_{d(off)}$	Turn-Off Delay Time		--	75	160	ns
$t_f$	Turn-Off Fall Time		(Note 4)	--	120	250
$Q_g$	Total Gate Charge	$V_{DS} = 200\text{ V}, I_D = 33\text{ A},$ $V_{GS} = 10\text{ V}$	--	36.8	48	nC
$Q_{gs}$	Gate-Source Charge		--	10	--	nC
$Q_{gd}$	Gate-Drain Charge		(Note 4)	--	17	--
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		--	--	33	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current		--	--	132	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 33\text{ A}$	--	--	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS} = 0\text{ V}, I_S = 33\text{ A},$ $di_F/dt = 100\text{ A}/\mu\text{s}$	--	220	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	1.71	--	$\mu\text{C}$

### Notes:

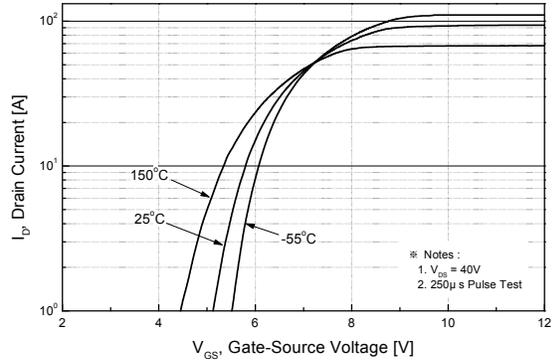
1. Repetitive rating: pulse-width limited by maximum junction temperature.
2.  $L = 1.35\text{ mH}, I_{AS} = 33\text{ A}, V_{DD} = 50\text{ V}, R_G = 25\ \Omega$ , starting  $T_J = 25^\circ\text{C}$ .
3.  $I_{SD} \leq 33\text{ A}, di/dt \leq 200\text{ A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$ , starting  $T_J = 25^\circ\text{C}$ .
4. Essentially independent of operating temperature typical characteristics.

## Typical Performance Characteristics

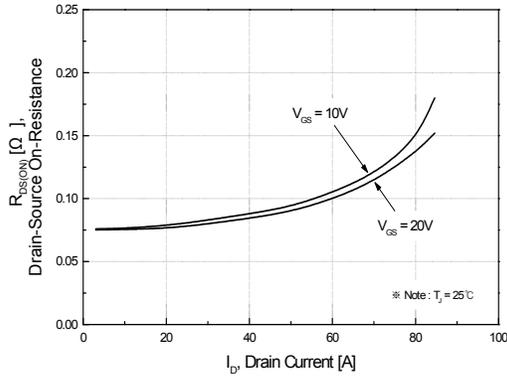
**Figure 1. On-Region Characteristics**



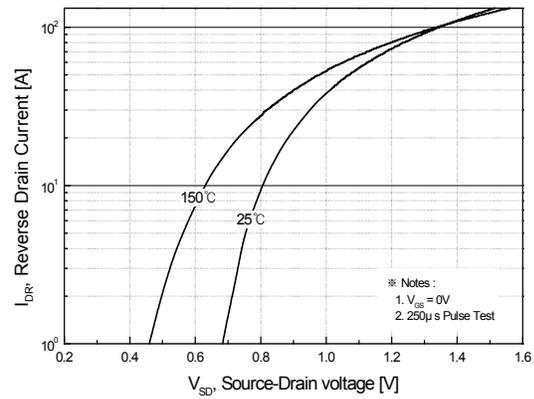
**Figure 2. Transfer Characteristics**



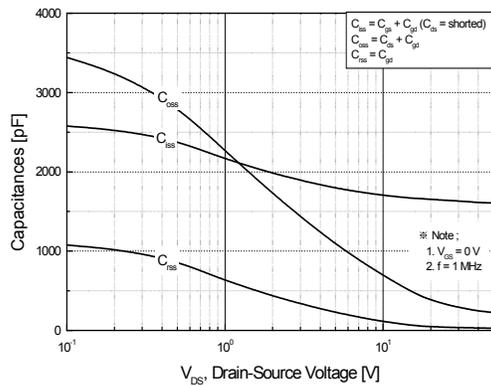
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



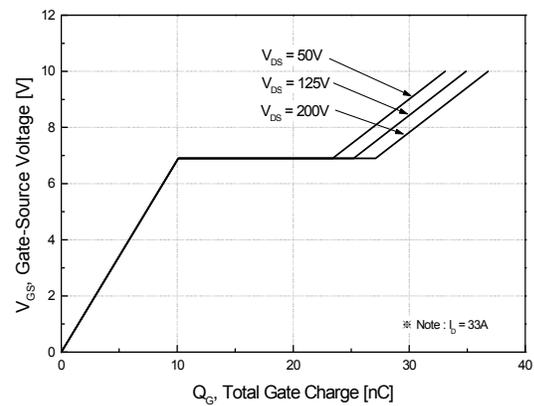
**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**



**Figure 5. Capacitance Characteristics**

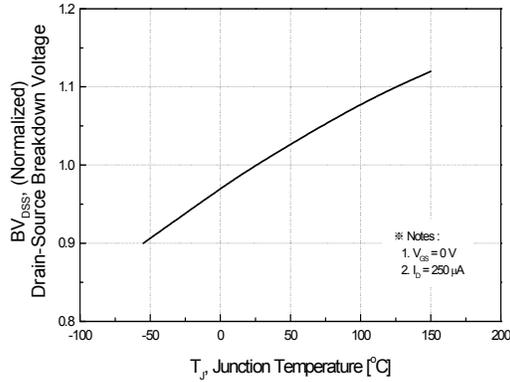


**Figure 6. Gate Charge Characteristics**

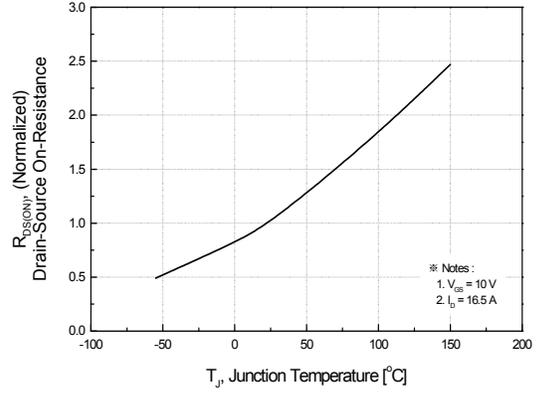


**Typical Performance Characteristics** (Continued)

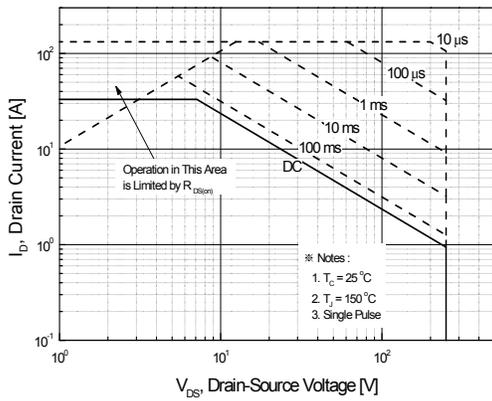
**Figure 7. Breakdown Voltage Variation vs. Temperature**



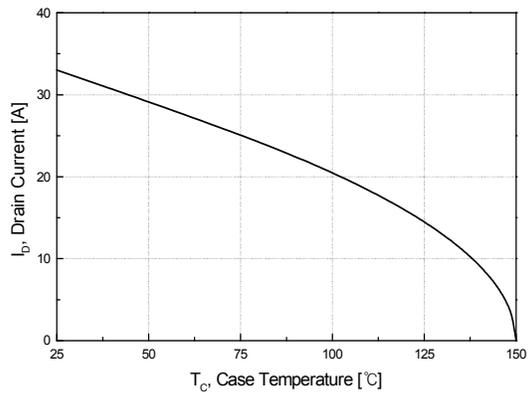
**Figure 8. On-Resistance Variation vs. Temperature**



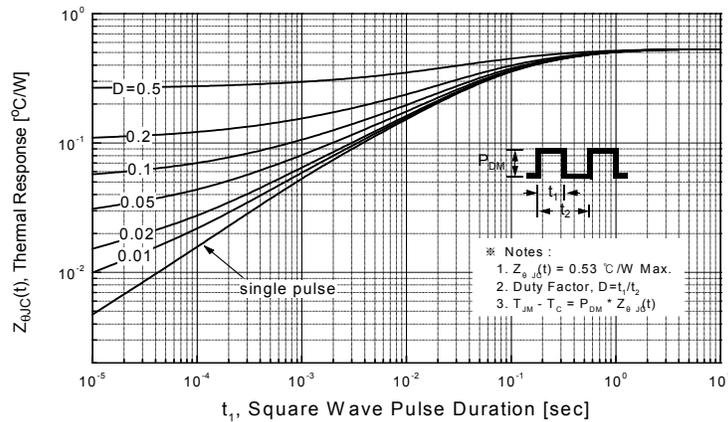
**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Drain Current vs. Case Temperature**



**Figure 11. Transient Thermal Response Curve**



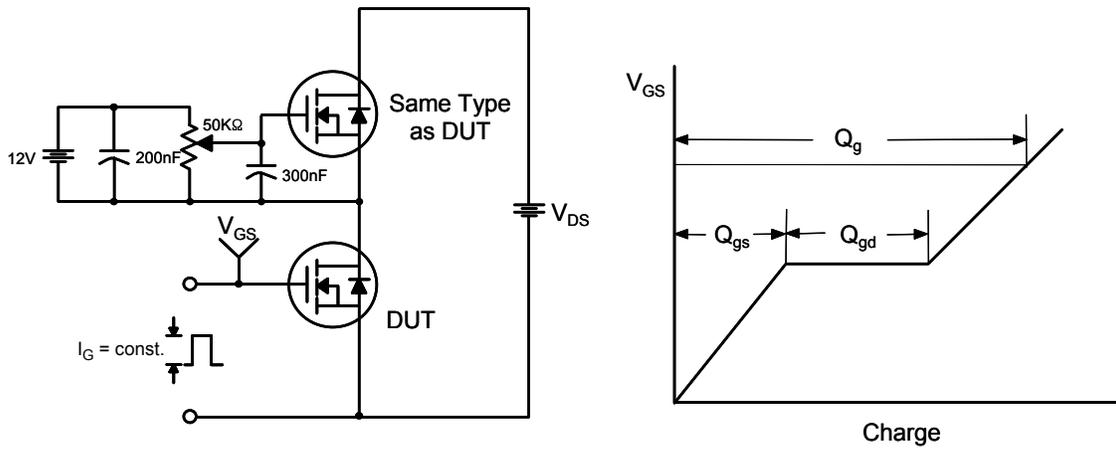


Figure 12. Gate Charge Test Circuit & Waveform

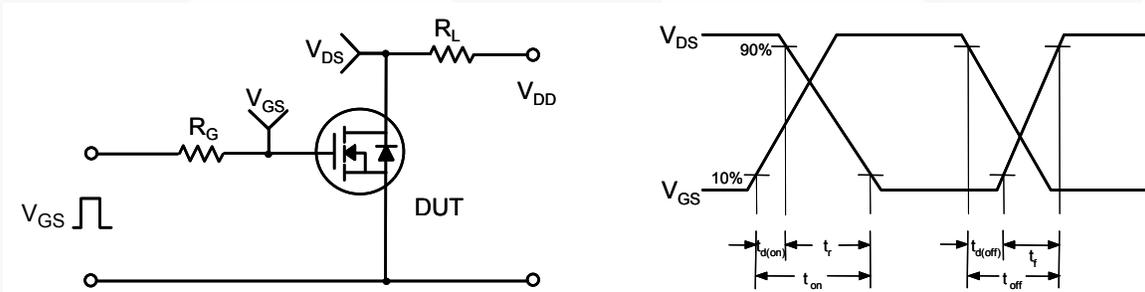


Figure 13. Resistive Switching Test Circuit & Waveforms

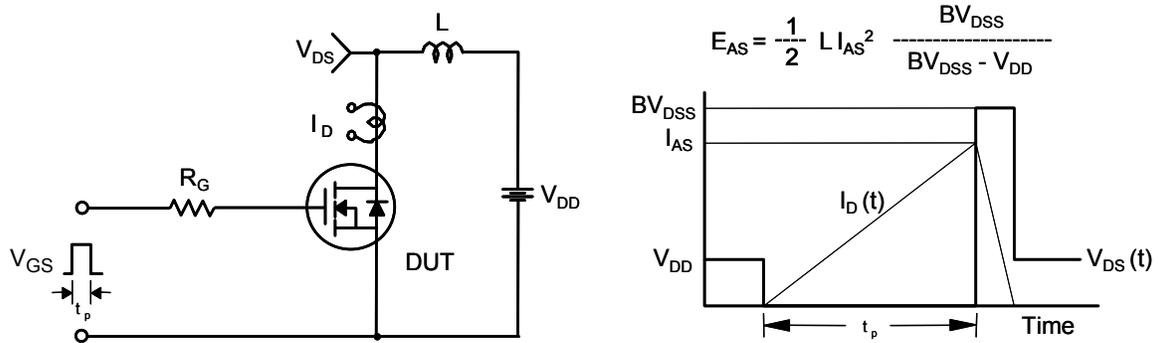


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

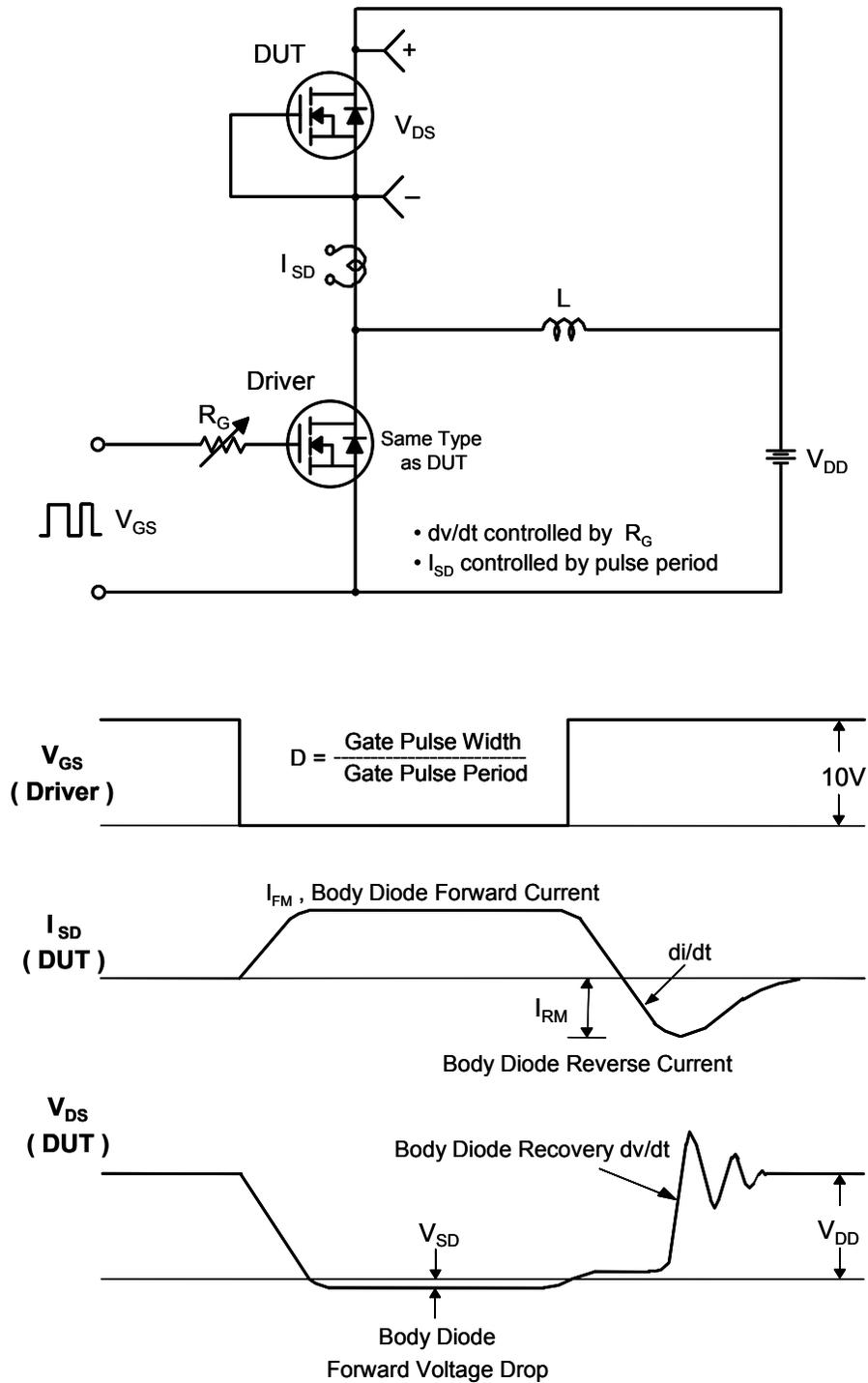
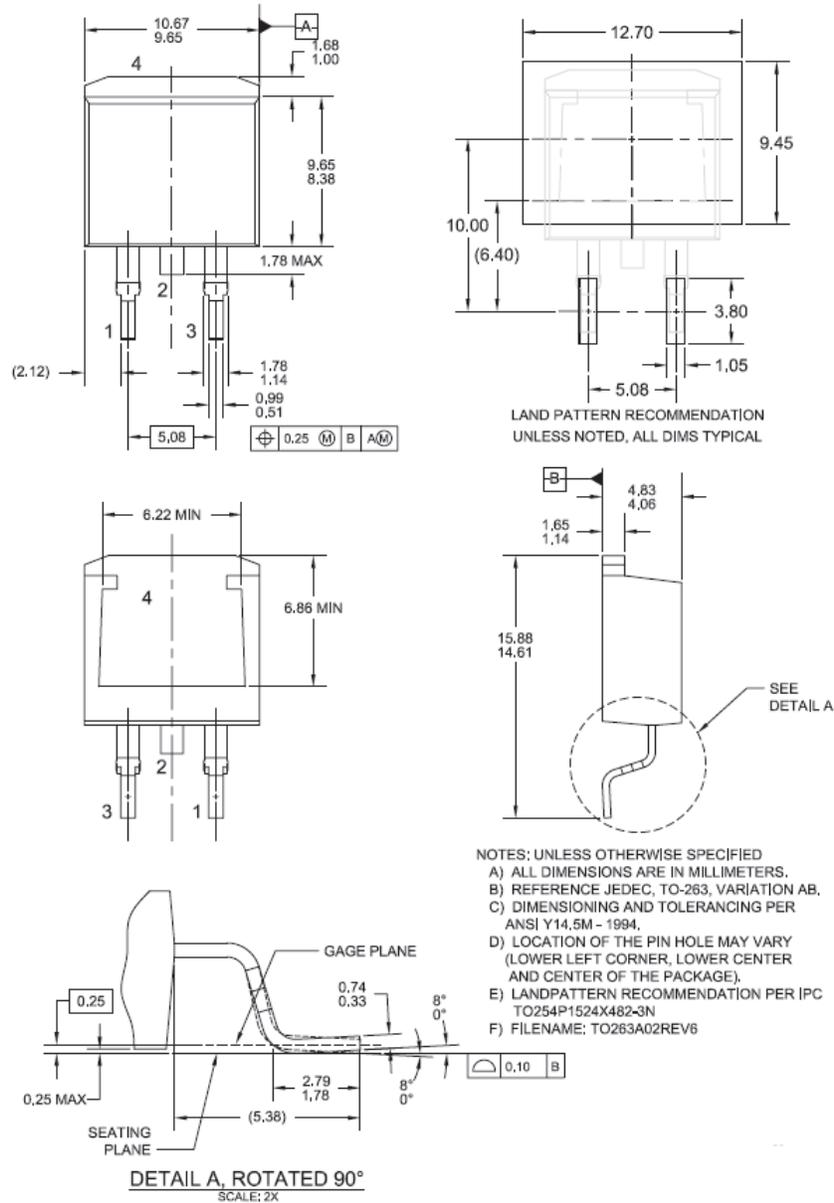


Figure 15. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms

## Mechanical Dimensions



**Figure 16. TO263 (D<sup>2</sup>PAK), Molded, 2-Lead, Surface Mount**

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Rev. I66

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