# **Dual General Purpose Transistors**

## **PNP Duals**

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-363/SC-88 which is designed for low power surface mount applications.

#### **Features**

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage BC856, SBC856 BC857, SBC857 BC858	V <sub>CEO</sub>	-65 -45 -30	V
Collector – Base Voltage BC856, SBC856 BC857, SBC857 BC858	V <sub>CBO</sub>	-80 -50 -30	V
Emitter - Base Voltage	V <sub>EBO</sub>	-5.0	V
Collector Current -Continuous	I <sub>C</sub>	-100	mAdc
Collector Current - Peak	I <sub>C</sub>	-200	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation Per Device FR-5 Board (Note 1) T <sub>A</sub> = 25°C Derate Above 25°C	P <sub>D</sub>	380 250 3.0	mW mW/°C mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	328	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

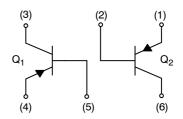


## ON Semiconductor®

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SOT-363/SC-88 CASE 419B STYLE 1



## **MARKING DIAGRAM**



3x = Specific Device Code x = B, F, G, or L

(See Ordering Information)

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

<sup>1.</sup>  $FR-5 = 1.0 \times 0.75 \times 0.062$  in

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Symbol	Min	Тур	Max	Unit
V <sub>(BR)CEO</sub>	-65 -45 -30	- - -	- - -	V
V <sub>(BR)CES</sub>	-80 -50 -30	- - -	- - -	V
V <sub>(BR)CBO</sub>	-80 -50 -30	- - -	- - -	V
V <sub>(BR)EBO</sub>	-5.0 -5.0 -5.0	- - -	- - -	V
I <sub>CBO</sub>	- -	1 1	-15 -4.0	nA μA
h <sub>FE</sub>	- - 220 420	150 270 290 520	- - 475 800	-
V <sub>CE(sat)</sub>			-0.3 -0.65	V
V <sub>BE(sat)</sub>	- -	-0.7 -0.9	- -	V
V <sub>BE(on)</sub>	-0.6 -	1 1	-0.75 -0.82	V
•				
f <sub>T</sub>	100	-	_	MHz
C <sub>ob</sub>	-	-	4.5	pF
NF				dB
	V(BR)CEO  V(BR)CES  V(BR)CBO  V(BR)EBO  ICBO  VEE(sat)  VBE(on)  fT  Cob	V(BR)CEO  -65 -45 -45 -30  V(BR)CES  -80 -50 -50 -30  V(BR)CBO  -50 -50 -50 -50 -50 -50 -50 -50 -50 -5	V(BR)CEO  -65 -45 -45 -30  -80 -50 -50 -30 -  V(BR)CBO  -80 -50 -50 -50 -50 -50 -50 -50 -50 -50 -5	V(BR)CEO  -65 -45 -45 -30 -50 -50 -30 -7  V(BR)CBO  -80 -50 -30 -7  V(BR)EBO  -50 -50 -50 -50 -7  -7  -50 -7  -7  -7  -7  -7  -7  -7  -7  -7  -7

## TYPICAL CHARACTERISTICS - BC856/SBC856

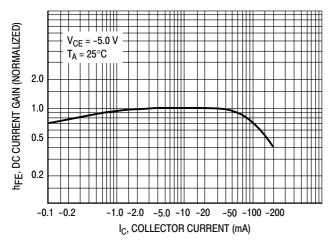


Figure 1. DC Current Gain

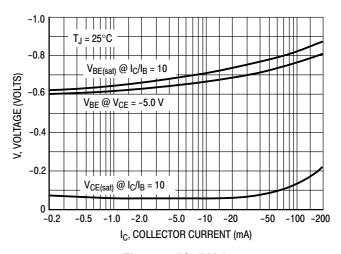


Figure 2. "On" Voltage

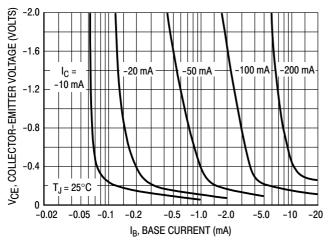


Figure 3. Collector Saturation Region

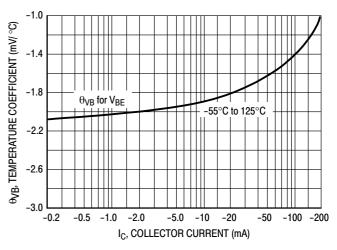


Figure 4. Base-Emitter Temperature Coefficient

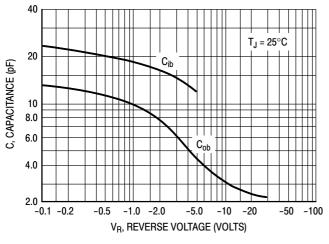


Figure 5. Capacitance

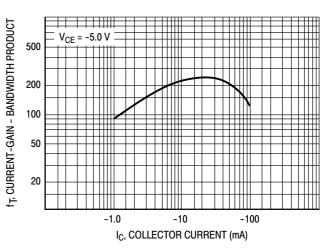
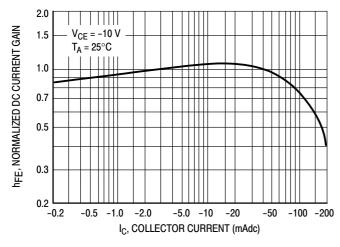


Figure 6. Current-Gain - Bandwidth Product

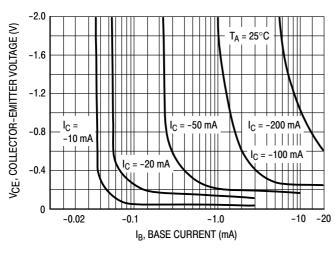
## TYPICAL CHARACTERISTICS - BC857/SBC857/BC858



-1.0  $T_A = 25^{\circ}C$ -0.9  $V_{BE(sat)} @ I_C/I_B = 10$ -0.8 -0.7V, VOLTAGE (VOLTS) -0.6  $V_{BE(on)} @ V_{CE} = -10 V$ -0.5 -0.4 -0.3 -0.2  $V_{CE(sat)} @ I_C/I_B = 10$ -0.1 -0.2 -0.1 -2.0 -5.0 -50 -100 IC, COLLECTOR CURRENT (mAdc)

Figure 7. Normalized DC Current Gain

Figure 8. "Saturation" and "On" Voltages



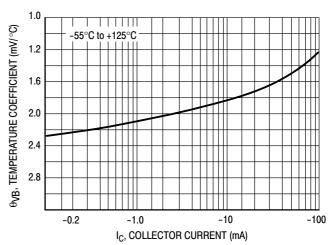
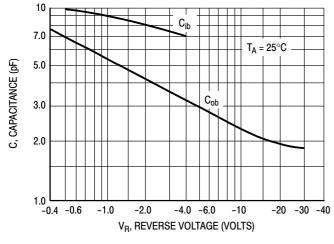


Figure 9. Collector Saturation Region

Figure 10. Base–Emitter Temperature Coefficient



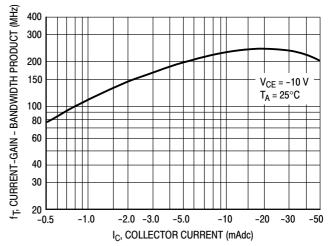


Figure 11. Capacitances

Figure 12. Current-Gain - Bandwidth Product

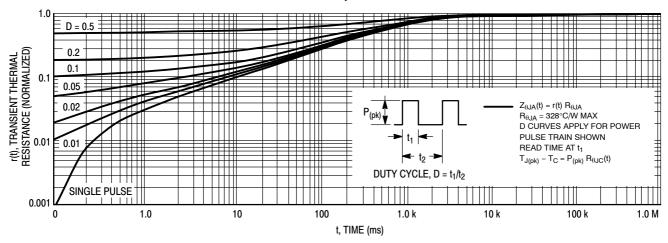


Figure 13. Thermal Response

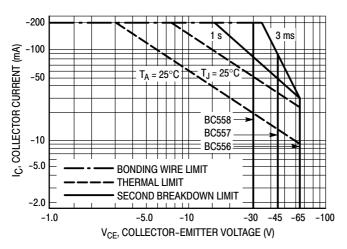


Figure 14. Active Region Safe Operating Area

The safe operating area curves indicate  $I_C$ – $V_{CE}$  limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 14 is based upon  $T_{J(pk)} = 150^{\circ}\text{C}$ ;  $T_{C}$  or  $T_{A}$  is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided  $T_{J(pk)} \leq 150^{\circ}\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

## **ORDERING INFORMATION**

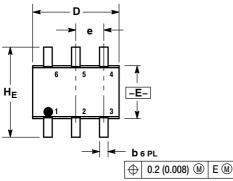
Device	Device Marking	Package	Shipping <sup>†</sup>
BC856BDW1T1G	3B	SOT-363 (Pb-Free)	3,000 / Tape & Reel
SBC856BDW1T1G	3B	SOT-363 (Pb-Free)	3,000 / Tape & Reel
BC856BDW1T3G	3B	SOT-363 (Pb-Free)	10,000 / Tape & Reel
SBC856BDW1T3G	3B	SOT-363 (Pb-Free)	10,000 / Tape & Reel
BC857BDW1T1G	3F	SOT-363 (Pb-Free)	3,000 / Tape & Reel
SBC857BDW1T1G	3F	SOT-363 (Pb-Free)	3,000 / Tape & Reel
BC857CDW1T1G	3G	SOT-363 (Pb-Free)	3,000 / Tape & Reel
SBC857CDW1T1G	3G	SOT-363 (Pb-Free)	3,000 / Tape & Reel
BC858CDW1T1G	3L	SOT-363 (Pb-Free)	3,000 / Tape & Reel

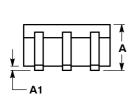
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

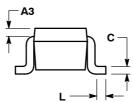
#### PACKAGE DIMENSIONS

## SC-88/SC70-6/SOT-363

CASE 419B-02 **ISSUE W** 







#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02

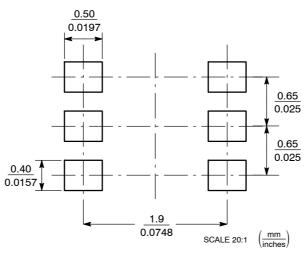
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012
С	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
E	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC			0	.026 BS	С
L	0.10	0.20	0.30	0.004	0.008	0.012
He	2.00	2.10	2.20	0.078	0.082	0.086

#### STYLE 1:

PIN 1. EMITTER 2

- BASE 2 2.
- 3. COLLECTOR 1 4. EMITTER 1
- 5. BASE 1 6. COLLECTOR 2

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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