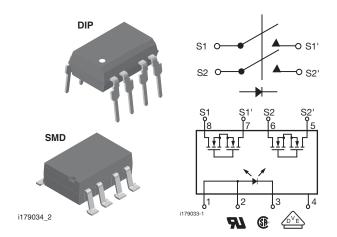
LH1503AAC, LH1503AACTR, LH1503AB

Vishay Semiconductors

2 Form A Solid-State Relay



DESCRIPTION

The LH1503 relays are DPST normally open switches (2 form A) that can replace electromechanical relays in many applications. The relays are constructed using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high voltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuity, and DMOS switches. In addition, these relays employ current limiting circuity, enabling them to pass lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

FEATURES

- · Current limit protection
- Isolation test voltage 5300 V_{RMS}
- Typical R_{ON} 20 Ω
- Load voltage 350 V
- Load current 110 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- · High reliability monolithic receptor
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- · General telecom switching
 - On/off hook control
- Ring delay
- Dial pulse
- Ground start
- Ground fault protection
- Instrumentation
- · Industrial controls

AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection

CSA: certification no. 093751

DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending),

available with option 1

ORDERING INFORMATION				
L H 1 5 0 3 A PART NUMBER ELECTE VARIATIO	7.00			
PACKAGE	UL, CSA			
SMD-8, tubes	LH1503AAC			
SMD-8, tape and reel	LH1503AACTR			
DIP-8, tubes	LH1503AB			

LH1503AAC, LH1503AACTR, LH1503AB

Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
LED continuous forward current		l _F	50	mA		
LED reverse voltage	I _R ≤ 10 μA	V _R	8	V		
OUTPUT						
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	V_{L}	350	V		
Continuous DC load current one pole operating		IL	150	mA		
Continuous DC load current two poles operating		ΙL	110	mA		
Peak load current (single shot)	t = 100 ms	l _P	(1)			
SSR						
Ambient temperature range		T _{amb}	- 40 to + 85	°C		
Storage temperature range		T _{stg}	- 40 to + 150	°C		
Pin soldering temperature (2)	t = 10 s max.	T _{sld}	260	°C		
Input to output isolation voltage		V _{ISO}	5300	V _{RMS}		
Pole-to-pole isolation voltage (S1 to S2)			500	V		
Output power dissipation (continuous)		P _{diss}	600	mW		

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to current limit performance application note for a discussion on relay operation during transient currents.
- (2) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED forward current, switch turn-on	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I_{Fon}		2	3	mA
LED forward current, switch turn-off	$V_{L} = \pm 300 \text{ V}$	I _{Foff}	0.2	0.8		mA
LED forward voltage	I _F = 10 mA	V_{F}	1.15	1.26	1.45	V
OUTPUT						
On-resistance	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	R _{ON}	12	20	25	Ω
Pole-to-pole on-resistance matching (S1 to S2)	I _F = 5 mA, I _L = 50 mA			0.2	2	ΔΩ
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.5	5000		GΩ
Current limit	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	I _{LMT}	230	270	370	mA
Off state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	I _O		0.02	200	nA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 350 \text{ V}$	Ιο			1	μA
Output appositores	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}$	Co		55		pF
Output capacitance	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co		10		pF
Pole-to-pole capacitance (S1 to S2)	$I_F = 0 \text{ mA}$			3		pF
	I _F = 5 mA			4		pF
Switch offset	I _F = 5 mA	Vos		0.15		μV
TRANSFER						
Capacitance (input to output)	V _{ISO}	C _{ISO}		1.1		pF

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 10 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}		1.6	2.5	ms
Turn-off time	$I_F = 10 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}		0.65	2.5	ms

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

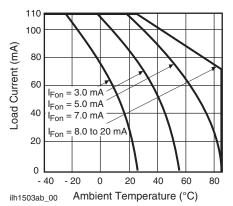


Fig. 1 - Recommended Operating Conditions

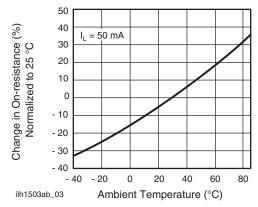


Fig. 4 - On-Resistance vs. Temperature

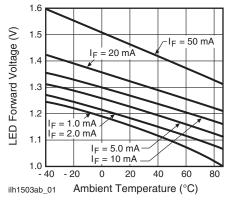


Fig. 2 - LED Voltage vs. Temperature

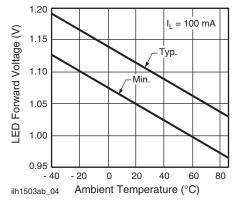


Fig. 5 - LED Dropout Voltage vs. Temperature

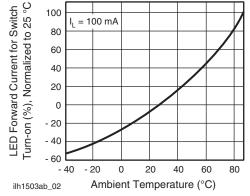


Fig. 3 - LED Current for Switch Turn-on vs. Temperature

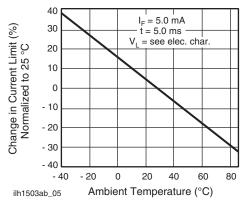


Fig. 6 - Current Limit vs. Temperature

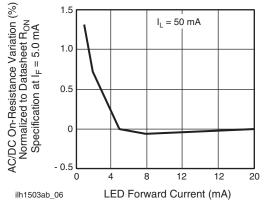


Fig. 7 - Variation in On-Resistance vs. LED Current

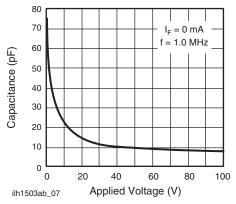


Fig. 8 - Switch Capacitance vs. Applied Voltage

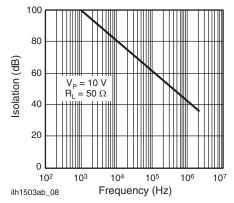


Fig. 9 - Output Isolation

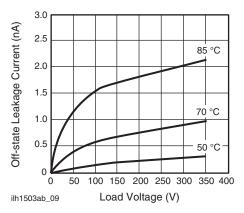


Fig. 10 - Leakage Current vs. Applied Voltage at Elevated Temperatures

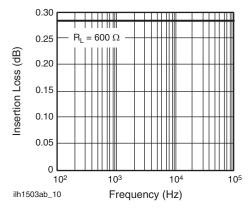


Fig. 11 - Insertion Loss vs. Frequency

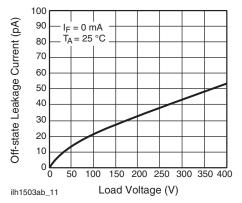


Fig. 12 - Leakage Current vs. Applied Voltage

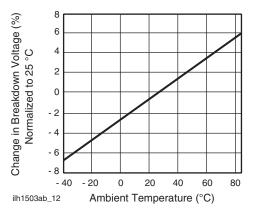


Fig. 13 - Switch Breakdown Voltage vs. Temperature

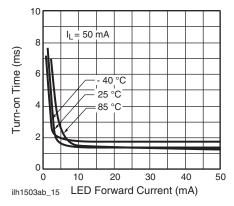


Fig. 16 - Turn-on Time vs. LED Current

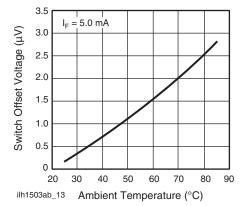


Fig. 14 - Switch Offset Voltage vs. Temperature

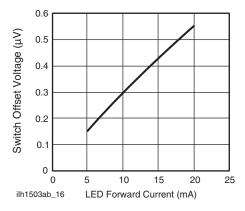


Fig. 17 - Switch Offset Voltage vs. LED Current

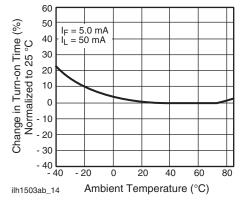


Fig. 15 - Turn-on Time vs. Temperature

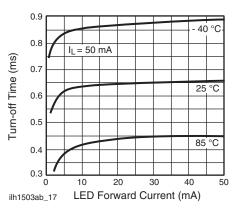
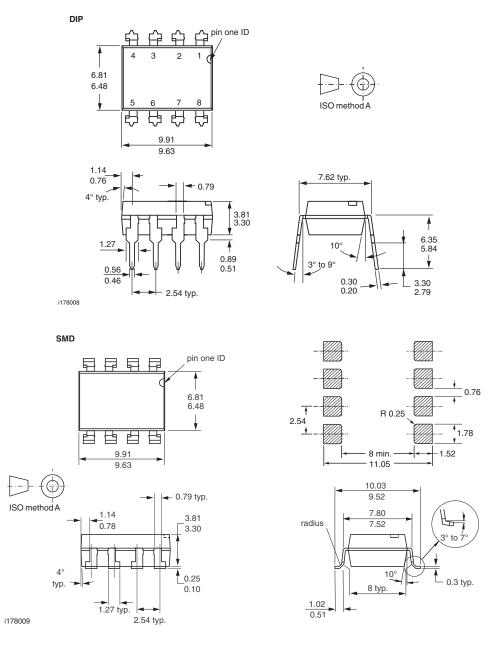


Fig. 18 - Turn-off Time vs. Temperature



PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING (example)



Note

• Tape and reel suffix (TR) is not part of the package marking.



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Vishay

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