



STGB10NC60KD, STGD10NC60KD STGF10NC60KD, STGP10NC60KD

10 A, 600 V short-circuit rugged IGBT

Features

- Lower on voltage drop ($V_{CE(sat)}$)
- Lower C_{RES} / C_{IES} ratio (no cross-conduction susceptibility)
- Very soft ultra fast recovery antiparallel diode
- Short-circuit withstand time 10 μ s

Description

This IGBT utilizes the advanced PowerMESH™ process resulting in an excellent trade-off between switching performance and low on-state behavior.

Applications

- High frequency motor controls
- SMPS and PFC in both hard switch and resonant topologies
- Motor drives

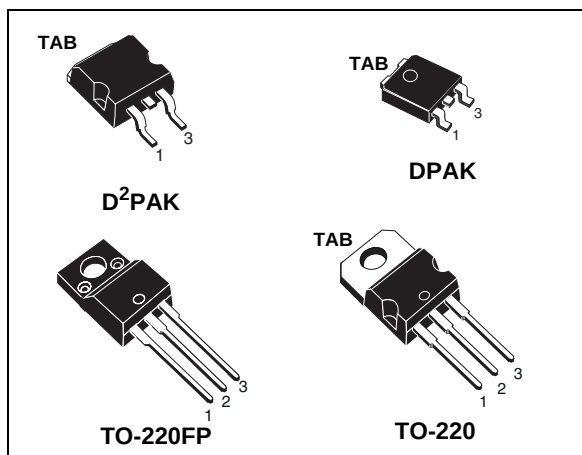


Figure 1. Internal schematic diagram

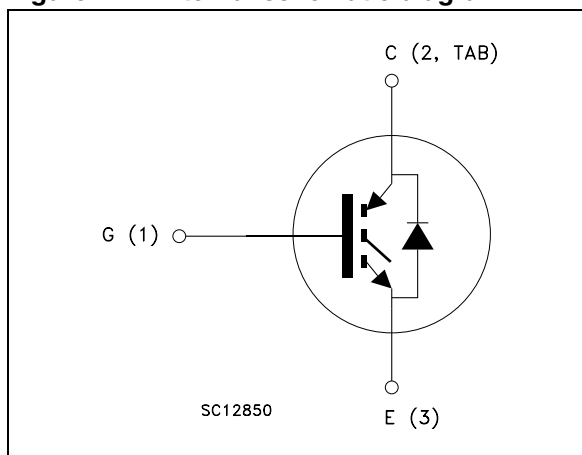


Table 1. Device summary

Order codes	Marking	Packages	Packaging
STGB10NC60KDT4	GB10NC60KD	D ² PAK	Tape and reel
STGD10NC60KDT4	GD10NC60KD	DPAK	
STGF10NC60KD	GF10NC60KD	TO-220FP	Tube
STGP10NC60KD	GP10NC60KD	TO-220	

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1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value			Unit
		D ² PAK TO-220	DPAK	TO-220FP	
V_{CES}	Collector-emitter voltage ($V_{GE} = 0$)	600			V
$I_C^{(1)}$	Continuous collector current at $T_C = 25^\circ\text{C}$	20		9	A
$I_C^{(1)}$	Continuous collector current at $T_C = 100^\circ\text{C}$	10		6	A
$I_{CL}^{(2)}$	Turn-off latching current	30			A
$I_{CP}^{(3)}$	Pulsed collector current	30			A
V_{GE}	Gate-emitter voltage	± 20			V
I_F	Diode RMS forward current at $T_C = 25^\circ\text{C}$	10			A
I_{FSM}	Surge non repetitive forward current $T_p = 10$ ms sinusoidal	20			A
P_{TOT}	Total dissipation at $T_C = 25^\circ\text{C}$	65	62	25	W
V_{ISO}	Insulation withstand voltage (RMS) from all three leads to external heat sink ($t=1$ s; $T_C=25^\circ\text{C}$)	--			2500 V
t_{scw}	Short-circuit withstand time $V_{CE} = 0.5 V_{CES}$, $T_J = 125^\circ\text{C}$, $R_G = 10 \Omega$, $V_{GE} = 12$ V	10			μs
T_{stg}	Storage temperature	- 55 to 150			$^\circ\text{C}$
T_J	Operating junction temperature				

1. Calculated according to the iterative formula

$$I_C(T_C) = \frac{T_{J(max)} - T_C}{R_{thj-c} \times V_{CE(sat)(max)}(T_{J(max)}, I_C(T_C))}$$

2. $V_{clamp} = 80 \% V_{CES}$, $V_{GE} = 15$ V, $R_G = 10 \Omega$, $T_J = 150^\circ\text{C}$

3. Pulse width limited by maximum junction temperature and turn-off within RBSOA

Table 3. Thermal data

Symbol	Parameter	Value			Unit
		TO-220 D ² PAK	DPAK	TO-220FP	
R _{thj-case}	Thermal resistance junction-case IGBT	1.9	2	5	°C/W
R _{thj-case}	Thermal resistance junction-case diode	4	4.5	7	
R _{thj-amb}	Thermal resistance junction-ambient	62.5	100	62.5	

2 Electrical characteristics

($T_j = 25^\circ\text{C}$ unless otherwise specified)

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage ($V_{GE} = 0$)	$I_C = 1\text{mA}$	600			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{V}$, $I_C = 5\text{A}$ $V_{GE} = 15\text{V}$, $I_C = 5\text{A}$, $T_j = 125^\circ\text{C}$		2.2 1.8	2.5	V V
$V_{GE(th)}$	Gate threshold voltage	$V_{CE} = V_{GE}$, $I_C = 250\mu\text{A}$	4.5		6.5	V
I_{CES}	Collector cut-off current ($V_{GE} = 0$)	$V_{CE} = 600\text{V}$ $V_{CE} = 600\text{V}$, $T_j = 125^\circ\text{C}$			150 1	μA mA
I_{GES}	Gate-emitter leakage current ($V_{CE} = 0$)	$V_{GE} = \pm 20\text{V}$			± 100	nA
$g_{fs}^{(1)}$	Forward transconductance	$V_{CE} = 15\text{V}$, $I_C = 5\text{A}$		15		S

1. Pulse test: pulse duration < 300 μs , duty cycle < 2 %

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies}	Input capacitance	$V_{CE} = 25\text{V}$, $f = 1\text{MHz}$, $V_{GE} = 0$	-	380	-	pF
C_{oes}	Output capacitance			46		pF
C_{res}	Reverse transfer capacitance			8.5		pF
Q_g	Total gate charge	$V_{CE} = 390\text{V}$, $I_C = 5\text{A}$,	-	19	-	nC
Q_{ge}	Gate-emitter charge	$V_{GE} = 15\text{V}$,		5		nC
Q_{gc}	Gate-collector charge	(see Figure 19)		9		nC

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 390V, I_C = 5A$		17		ns
t_r	Current rise time	$R_G = 10\Omega, V_{GE} = 15V$	-	6	-	ns
$(di/dt)_{on}$	Turn-on current slope	(see Figure 20)		655		A/ μs
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 390V, I_C = 5A$		16.5		ns
t_r	Current rise time	$R_G = 10\Omega, V_{GE} = 15V,$ $T_J = 125^\circ C$	-	6.5	-	ns
$(di/dt)_{on}$	Turn-on current slope	(see Figure 20)		575		A/ μs
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 390V, I_C = 5A,$		33		ns
$t_{d(off)}$	Turn-off delay time	$R_{GE} = 10\Omega, V_{GE} = 15V$	-	72	-	ns
t_f	Current fall time	(see Figure 20)		82		ns
$t_r(V_{off})$	Off voltage rise time	$V_{CC} = 390V, I_C = 5A,$		60		ns
$t_{d(off)}$	Turn-off delay time	$R_{GE} = 10\Omega, V_{GE} = 15V,$ $T_J = 125^\circ C$	-	106	-	ns
t_f	Current fall time	(see Figure 20)		136		ns

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 390V, I_C = 5A$		55		μJ
$E_{off}^{(2)}$	Turn-off switching losses	$R_G = 10\Omega, V_{GE} = 15V$	-	85	-	μJ
E_{ts}	Total switching losses	(see Figure 20)		140		μJ
$E_{on}^{(1)}$	Turn-on switching losses	$V_{CC} = 390V, I_C = 5A$		87		μJ
$E_{off}^{(2)}$	Turn-off switching losses	$R_G = 10\Omega, V_{GE} = 15V,$ $T_J = 125^\circ C$	-	162	-	μJ
E_{ts}	Total switching losses	(see Figure 20)		249		μJ

1. E_{on} is the turn-on losses when a typical diode is used in the test circuit in figure 2. If the IGBT is offered in a package with a co-pak diode, the co-pak diode is used as external diode. IGBTs & Diode are at the same temperature (25°C and 125°C)
2. Turn-off losses include also the tail of the collector current

Table 8. Collector-emitter diode

Symbol	Parameter	Test conditions	Min	Typ.	Max.	Unit
V_F	Forward on-voltage	$I_F=5\text{ A}$ $I_F=5\text{ A}$, $T_J=125\text{ °C}$	-	2 1.6	-	V V
t_{rr} Q_{rr} I_{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F=5\text{ A}$, $V_R=40\text{ V}$, $di/dt=100\text{ A}/\mu\text{s}$ (see Figure 5)	-	22 14 1.3	-	ns nC A
t_{rr} Q_{rr} I_{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	$I_F=5\text{ A}$, $V_R=40\text{ V}$, $T_J=125\text{ °C}$, $di/dt=100\text{ A}/\mu\text{s}$ (see Figure 5)	-	35 40 2.2	-	ns nC A

2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

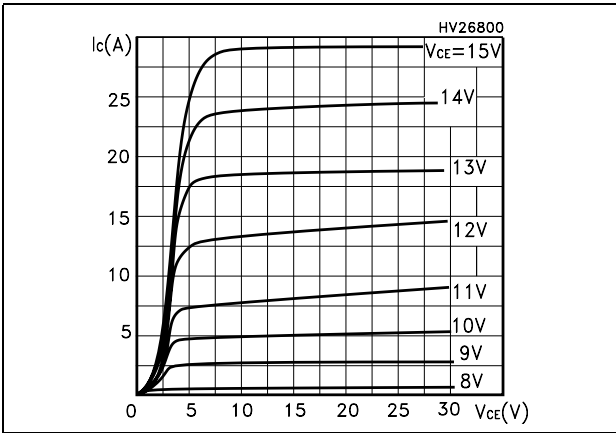


Figure 3. Transfer characteristics

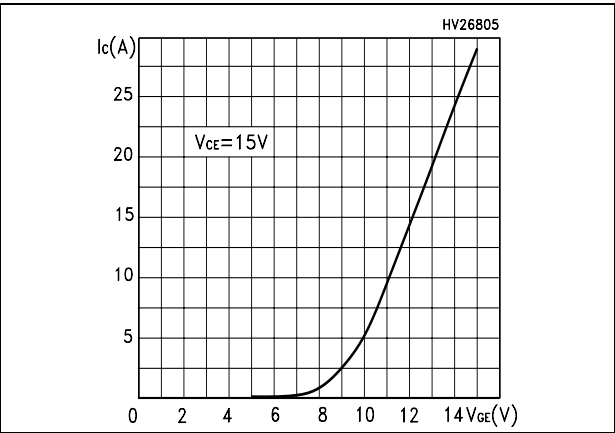


Figure 4. Transconductance

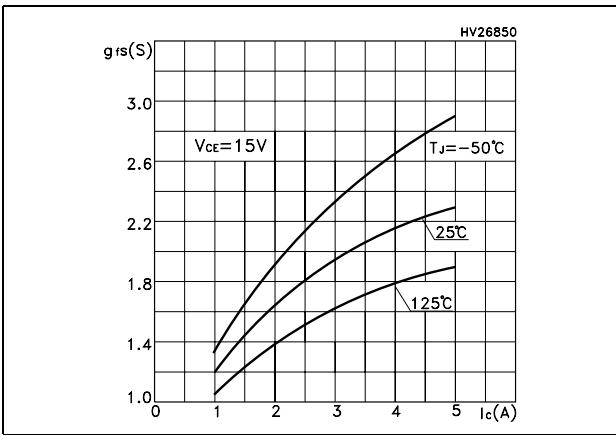


Figure 5. Collector-emitter on voltage vs temperature

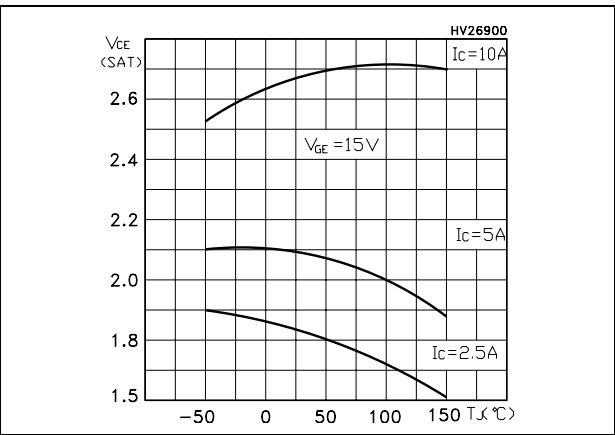


Figure 6. Gate charge vs gate-source voltage

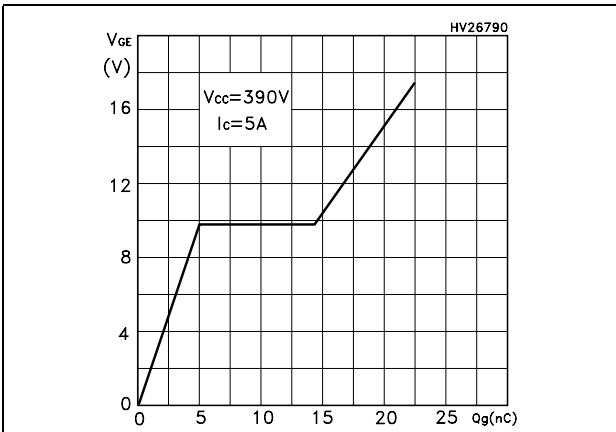


Figure 7. Capacitance variations

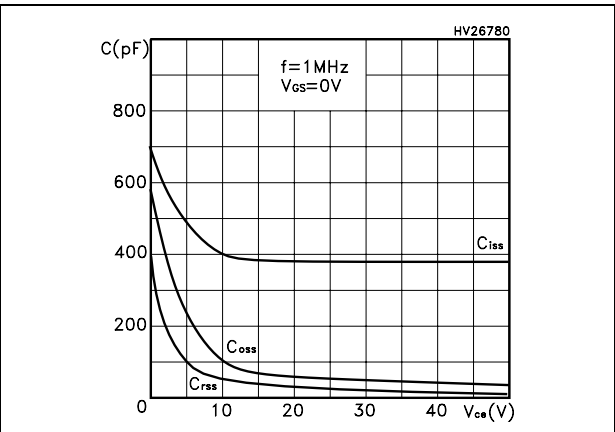


Figure 8. Normalized gate threshold voltage vs temperature

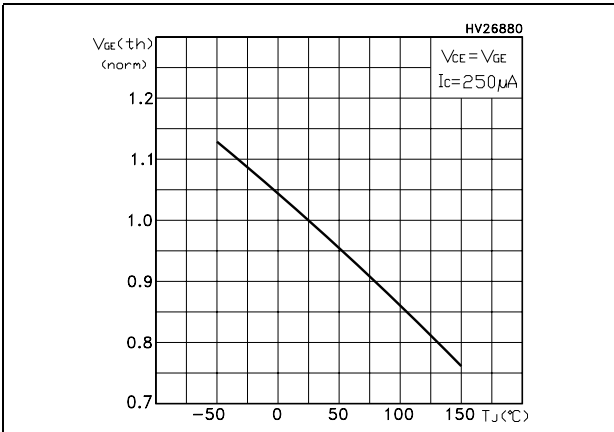


Figure 9. Collector-emitter on voltage vs collector current

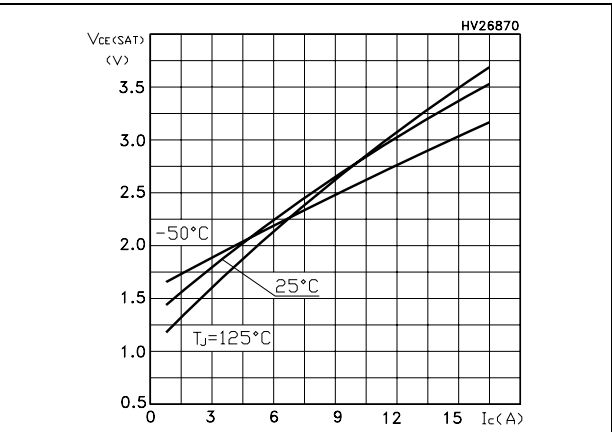


Figure 10. Normalized breakdown voltage vs temperature

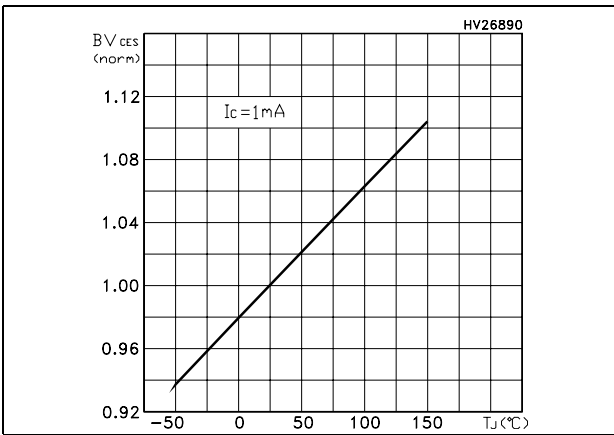


Figure 11. Switching losses vs temperature

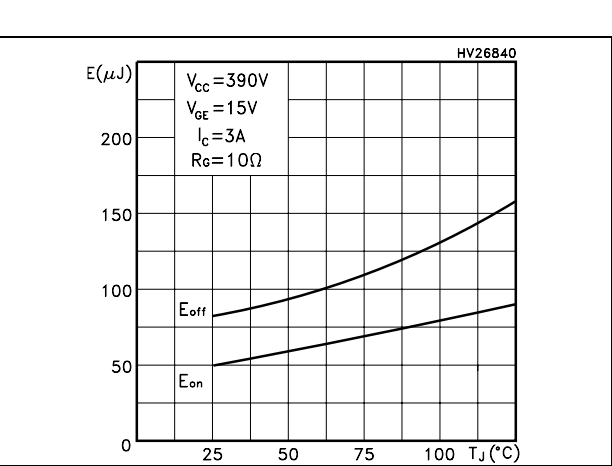


Figure 12. Switching losses vs gate resistance

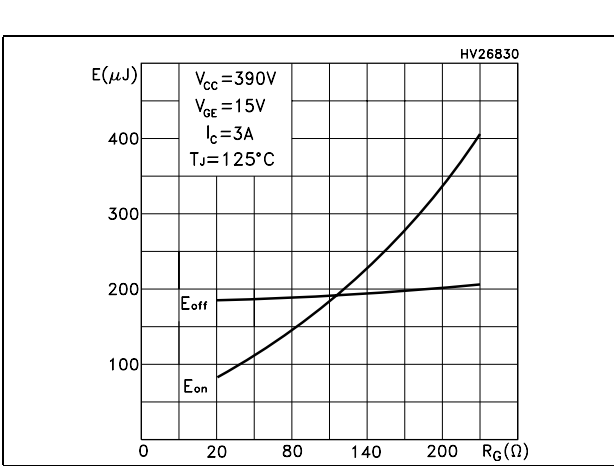


Figure 13. Switching losses vs collector current

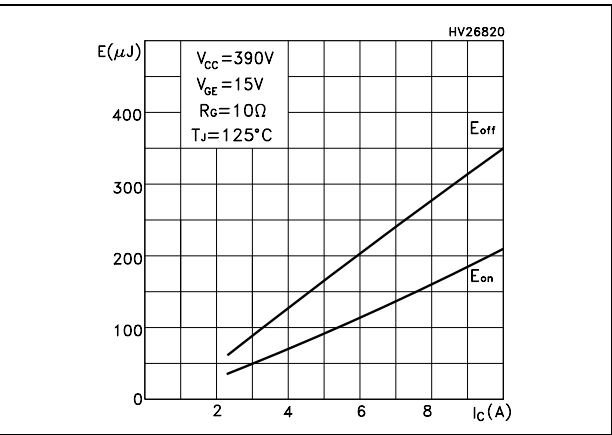


Figure 14. Thermal impedance for D²PAK, DPAK and TO-220

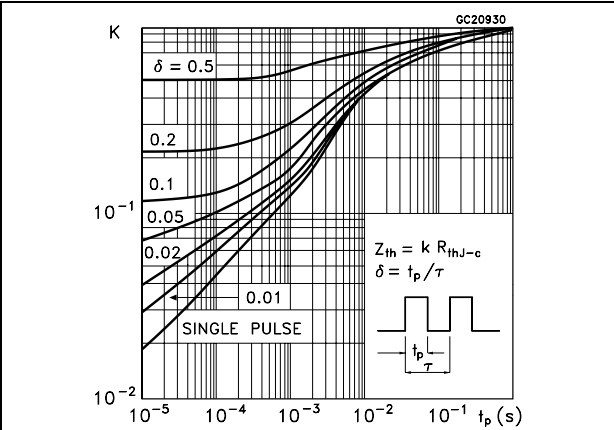


Figure 15. Turn-off SOA

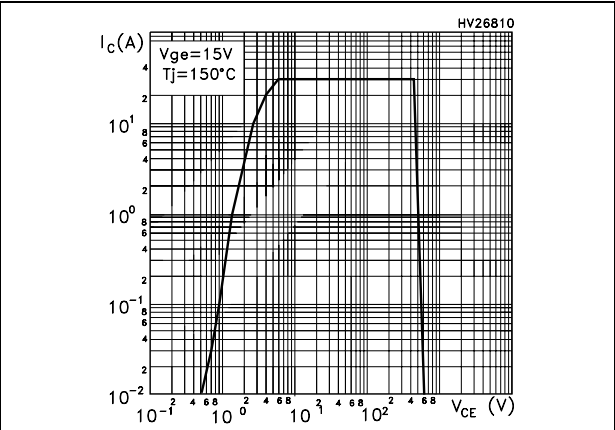


Figure 16. Emitter-collector diode characteristics

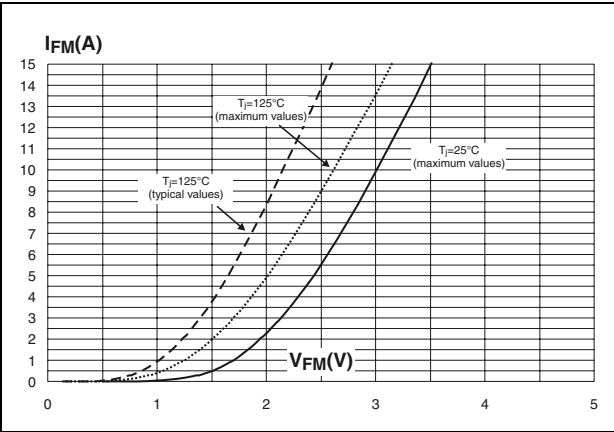
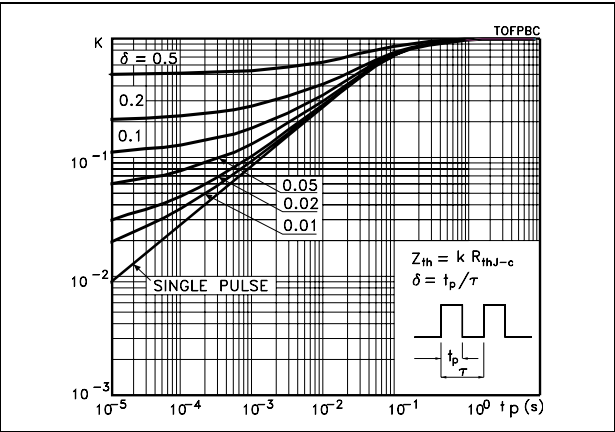


Figure 17. Thermal impedance for TO-220FP



3 Test circuits

Figure 18. Test circuit for inductive load switching

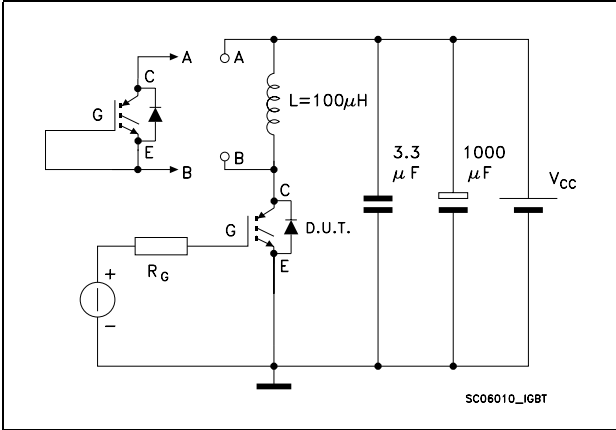


Figure 19. Gate charge test circuit

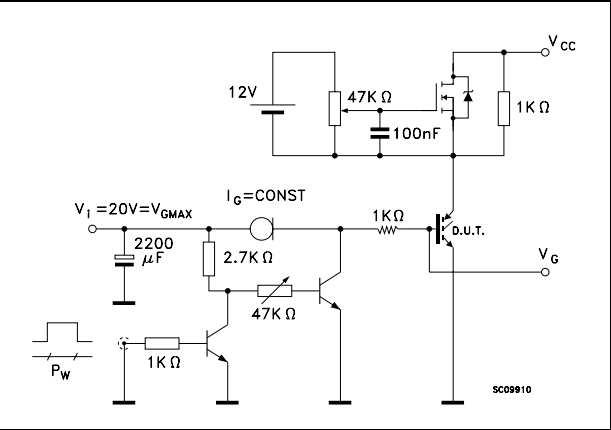


Figure 20. Switching waveform

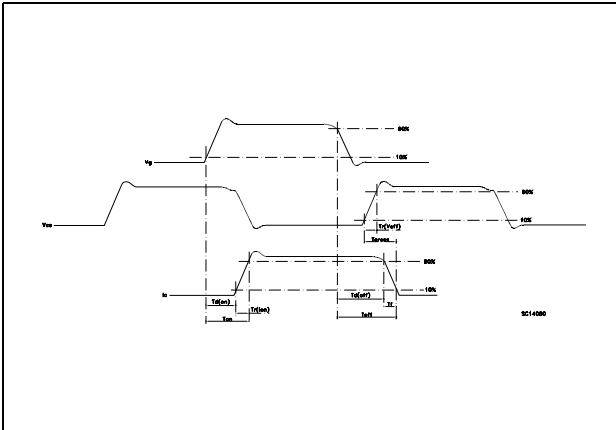
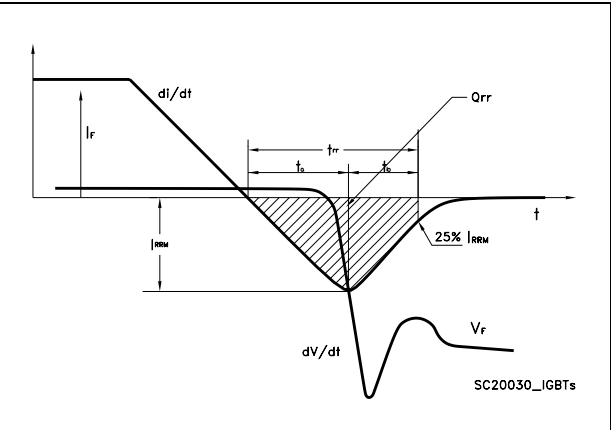


Figure 21. Diode recovery time waveform



4 **Package mechanical data**

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

Table 9. D²PAK package mechanical data

Dim.	mm			inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
A1	0.03		0.23	0.001		0.009
b	0.70		0.93	0.027		0.037
b2	1.14		1.70	0.045		0.067
c	0.45		0.60	0.017		0.024
c2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1	7.50			0.295		
E	10		10.40	0.394		0.409
E1	8.50			0.334		
e		2.54			0.1	
e1	4.88		5.28	0.192		0.208
H	15		15.85	0.590		0.624
J1	2.49		2.69	0.099		0.106
L	2.29		2.79	0.090		0.110
L1	1.27		1.40	0.05		0.055
L2	1.30		1.75	0.051		0.069
R		0.4			0.016	
V2	0°		8°	0°		8°

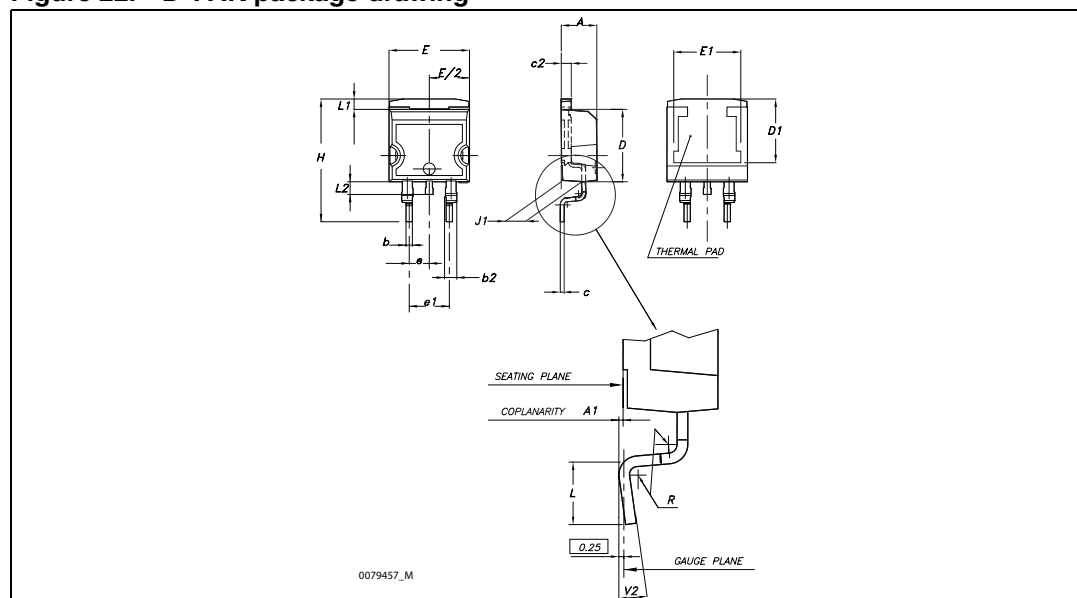
Figure 22. D²PAK package drawing

Table 10. DPAK package mechanical data

Dim.	mm		
	Min.	Typ	Max.
A	2.20		2.40
A1	0.90		1.10
A2	0.03		0.23
b	0.64		0.90
b4	5.20		5.40
c	0.45		0.60
c2	0.48		0.60
D	6.00		6.20
D1		5.10	
E	6.40		6.60
E1		4.70	
e		2.28	
e1	4.40		4.60
H	9.35		10.10
L	1		
L1		2.80	
L2		0.80	
L4	0.60		1
R		0.20	
V2	0 °		8 °

Figure 23. DPAK package drawing

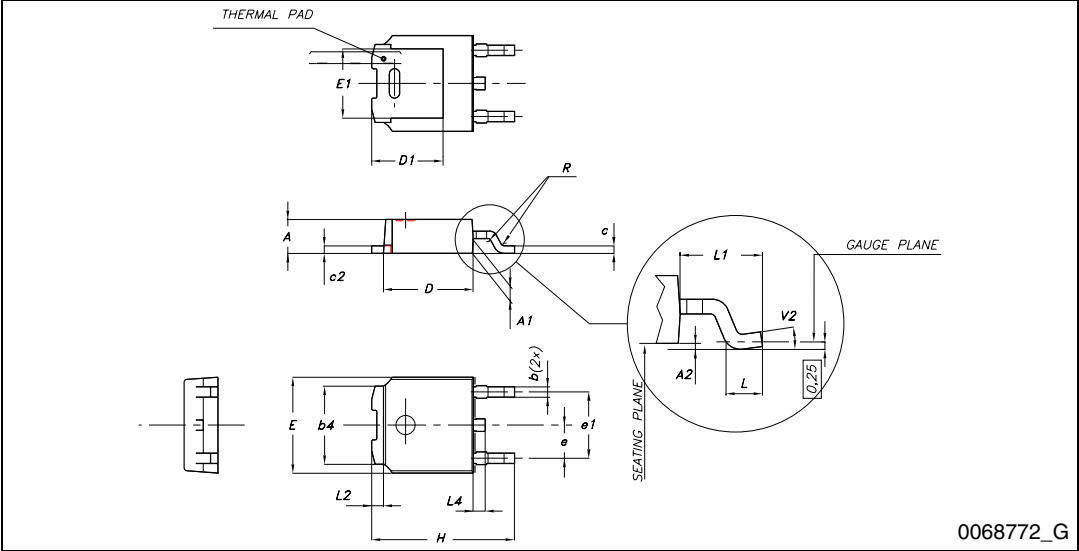


Table 11. TO-220FP package mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.4		4.6
B	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
H	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

Figure 24. TO-220FP package drawing

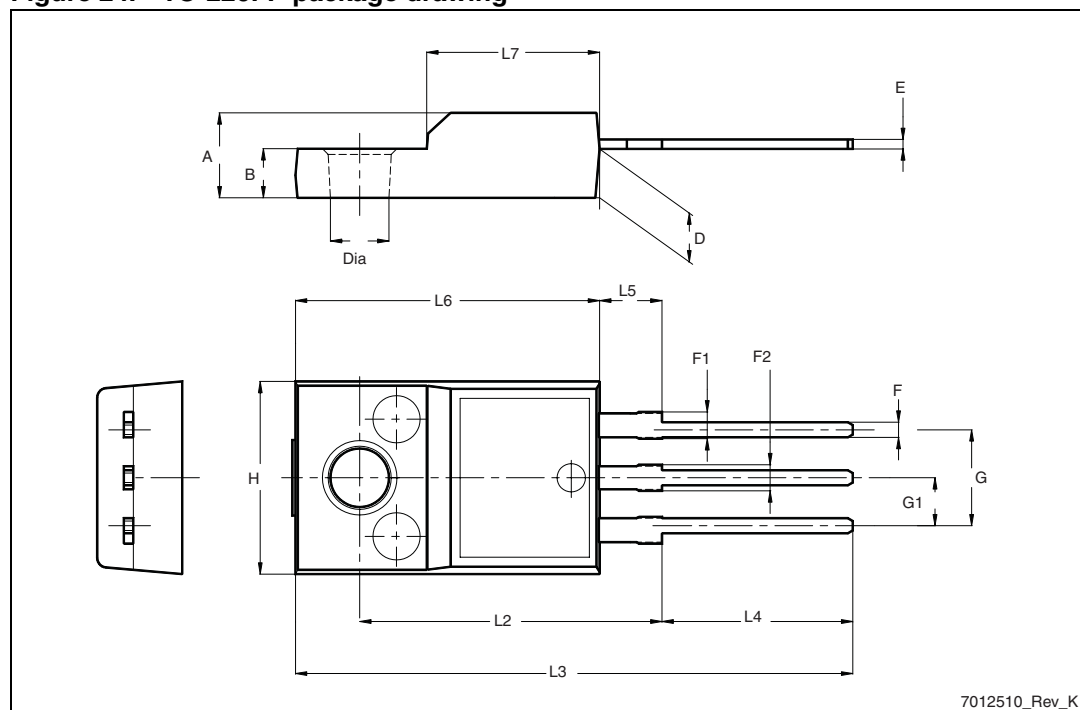
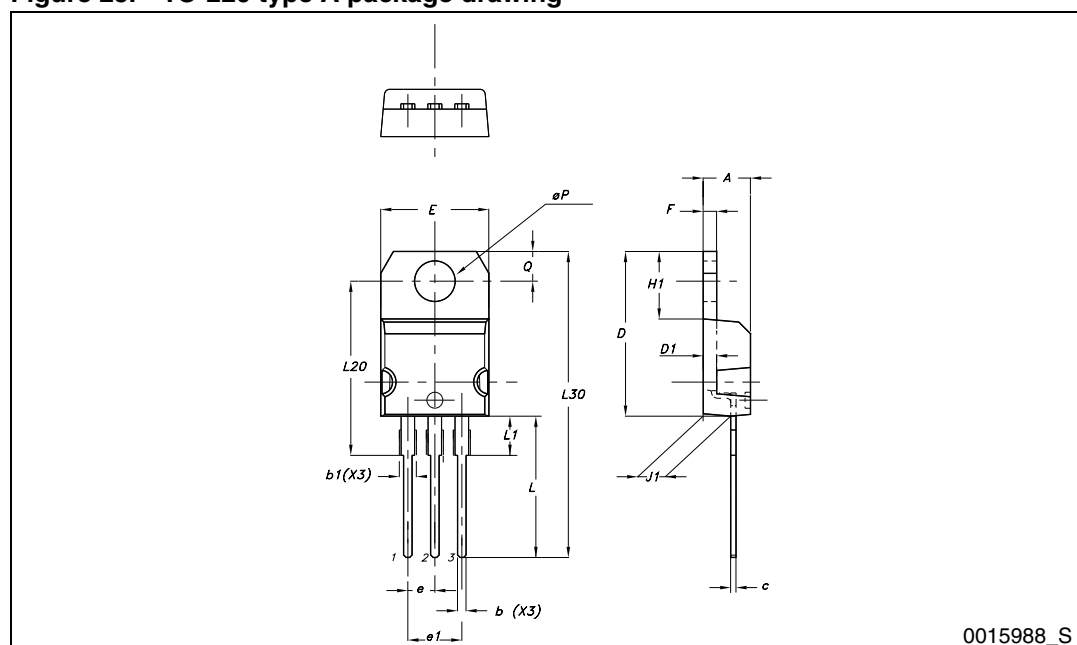


Table 12. TO-220 type A mechanical data

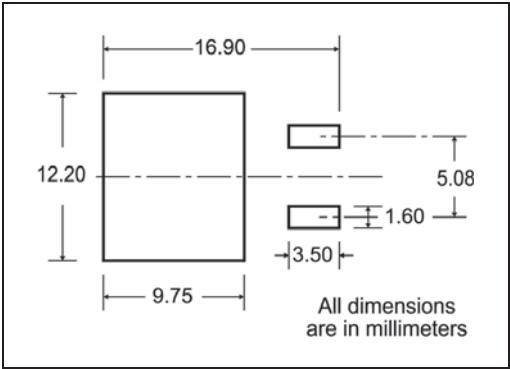
Dim.	mm		
	Min.	Typ.	Max.
A	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
c	0.48		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
e	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
ØP	3.75		3.85
Q	2.65		2.95

Figure 25. TO-220 type A package drawing

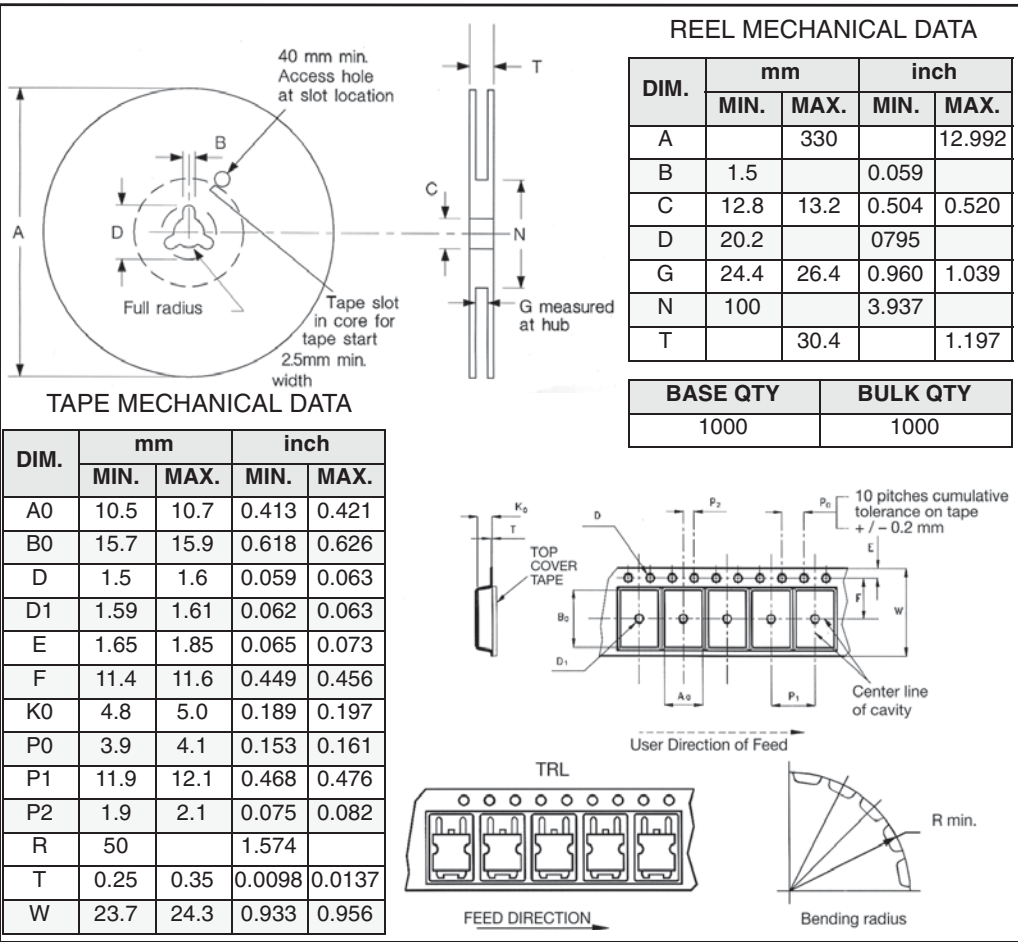


5 Packaging mechanical data

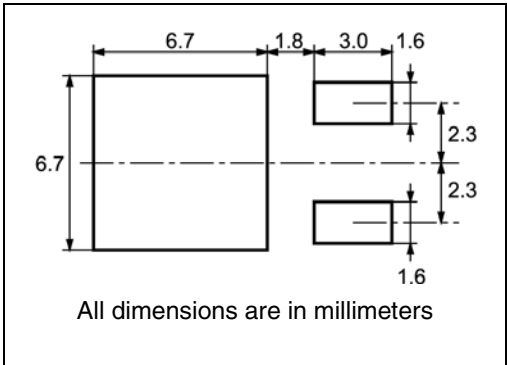
D²PAK FOOTPRINT



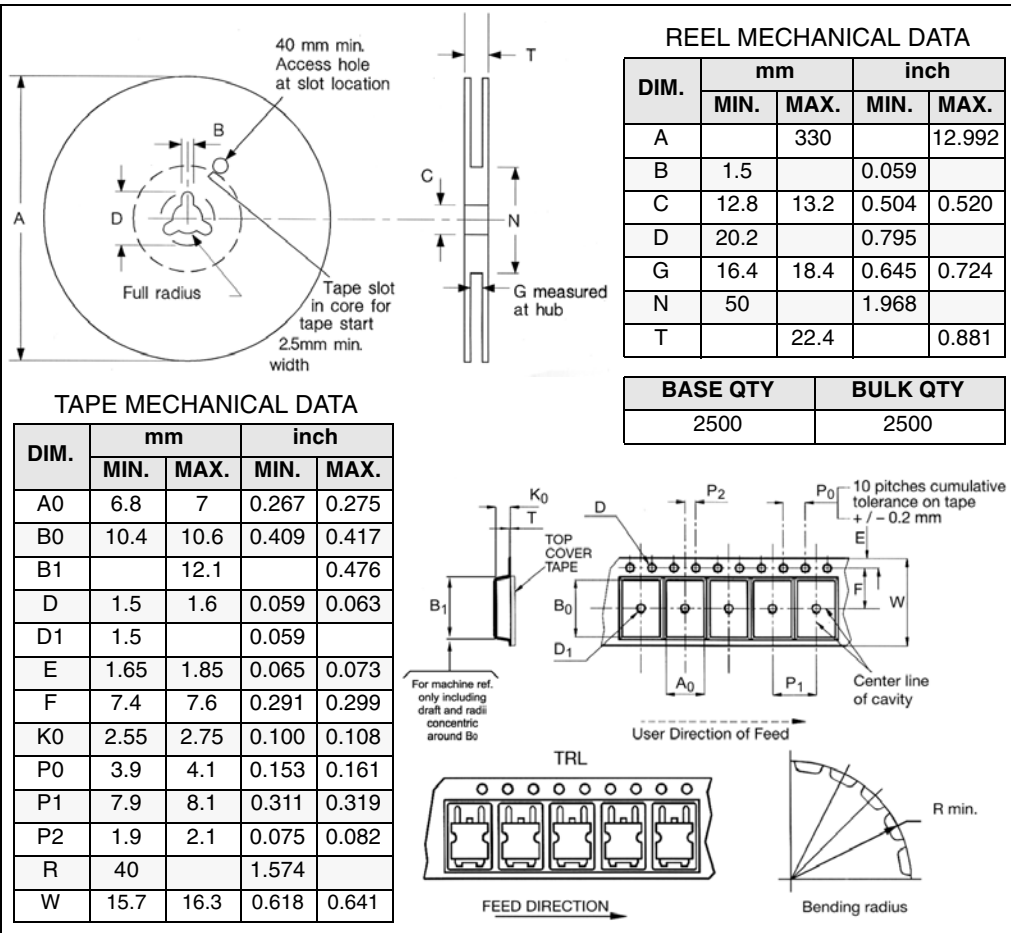
TAPE AND REEL SHIPMENT



DPAK FOOTPRINT



TAPE AND REEL SHIPMENT



6 Revision history

Table 13. Document revision history

Date	Revision	Changes
14-Jun-2005	1	New release.
19-Jul-2005	2	Complete version.
27-Jan-2006	3	Inserted ecopack indication.
01-Mar-2006	4	The document has been reformatted.
08-Feb-2007	5	Modified value on Table 6.: Switching on/off (inductive load) .
24-Nov-2009	6	Inserted DPAK package option.

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Tel +86 (21) 64016692-8333

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