NPN Silicon General Purpose Amplifier Transistor

This NPN transistor is designed for general purpose amplifier applications. This device is housed in the SOT-723 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Reduces Board Space
- High h_{FE}, 210-460 (typical)
- Low $V_{CE(sat)}$, < 0.5 V
- ESD Performance: Human Body Model; > 2000 V,
 - Machine Model; > 200 V
- Available in 8 mm, 7-inch/3000 Unit Tape and Reel
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free Devices

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{(BR)CBO}	50	Vdc
Collector-Emitter Voltage	V _{(BR)CEO}	50	Vdc
Emitter-Base Voltage	V _{(BR)EBO}	7.0	Vdc
Collector Current – Continuous	I _C	150	mAdc

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Power Dissipation (Note 1)	P_{D}	260	mW
Junction Temperature	T_J	150	°C
Storage Temperature Range	T _{stg}	−55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.

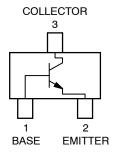
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ON Semiconductor®

http://onsemi.com

NPN GENERAL PURPOSE AMPLIFIER TRANSISTORS SURFACE MOUNT



MARKING DIAGRAM



SOT-723



XX = Specific Device Code (B9 = 2SC5658M3T5G RM = 2SC5658RM3T5G) M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
2SC5658M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel
2SC5658RM3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel
NSV2SC5658M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

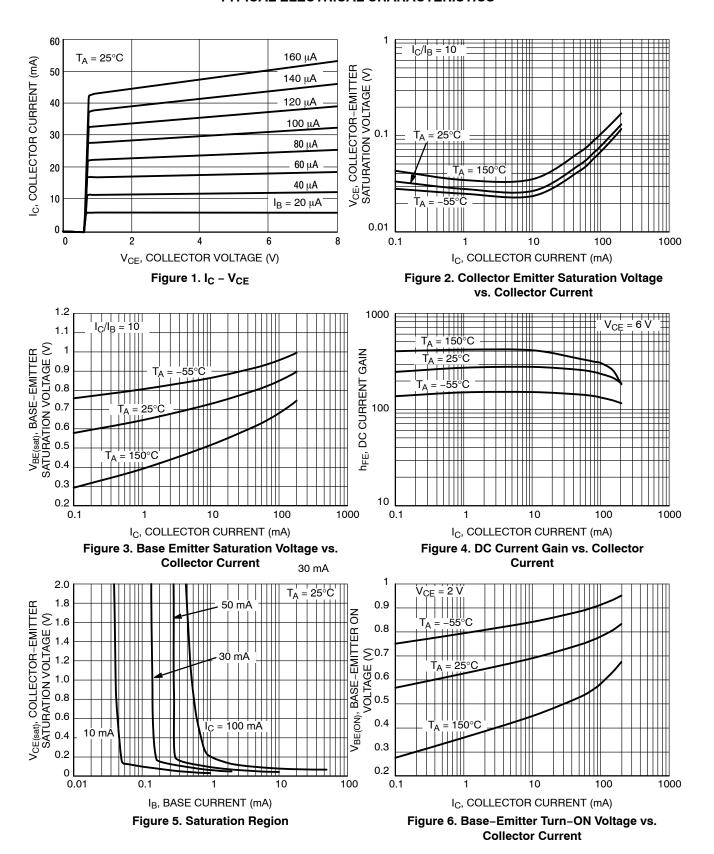
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

Characteristic	Symbol	Min	Тур	Max	Unit
Collector-Base Breakdown Voltage (I _C = 50 μAdc, I _E = 0)	V _{(BR)CBO}	50	-	-	Vdc
Collector-Emitter Breakdown Voltage (I _C = 1.0 mAdc, I _B = 0)	V _{(BR)CEO}	50	-	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 50 \mu Adc, I_E = 0$)	V _{(BR)EBO}	7.0	-	-	Vdc
Collector-Base Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	I _{CBO}	-	-	0.5	μΑ
Emitter-Base Cutoff Current (V _{EB} = 4.0 Vdc, I _B = 0)	I _{EBO}	-	-	0.5	μΑ
Collector-Emitter Saturation Voltage (Note 2) (I _C = 50 mAdc, I _B = 5.0 mAdc)	V _{CE(sat)}	-	-	0.4	Vdc
	h _{FE}	120 215	- -	560 375	-
Transition Frequency (V _{CE} = 12 Vdc, I _C = 2.0 mAdc, f = 30 MHz)	f _T	-	180	-	MHz
Output Capacitance (V _{CB} = 12 Vdc, I _C = 0 Adc, f = 1.0 MHz)	C _{OB}	_	2.0	_	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. *Include NSV-prefix devices where applicable.

2. Pulse Test: Pulse Width ≤ 300 µs, D.C. ≤ 2%.

TYPICAL ELECTRICAL CHARACTERISTICS



TYPICAL ELECTRICAL CHARACTERISTICS

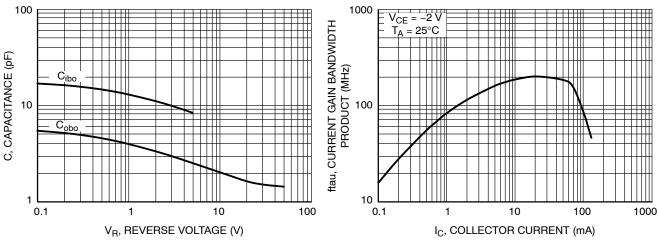


Figure 7. Capacitance

Figure 8. Current Gain Bandwidth Product vs.
Collector Current

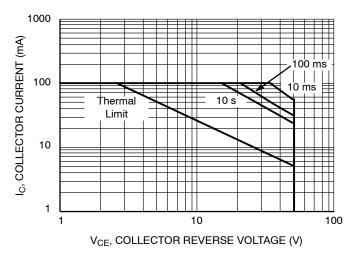
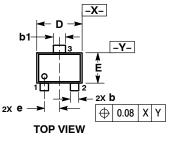
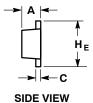


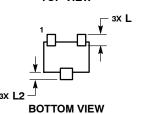
Figure 9. Safe Operating Area

PACKAGE DIMENSIONS

SOT-723 CASE 631AA ISSUE D







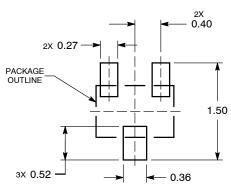


NOTES:

- DIMENSIONING AND TOLERANCING PER ASME
- Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS OR GATE BURRS

	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.45	0.50	0.55	
b	0.15	0.21	0.27	
b1	0.25	0.31	0.37	
С	0.07	0.12	0.17	
D	1.15	1.20	1.25	
E	0.75	0.80	0.85	
е	0.40 BSC			
ΗE	1.15	1.20	1.25	
L	0.29 REF			
L2	0.15	0.20	0.25	

RECOMMENDED **SOLDERING FOOTPRINT***



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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