



| Parameter           | Ratings | Units                                |
|---------------------|---------|--------------------------------------|
| Blocking Voltage    | 350     | V <sub>p</sub>                       |
| Load Current        | 120     | mA <sub>rms</sub> / mA <sub>DC</sub> |
| On-Resistance (max) | 35      | Ω                                    |

### Features

- 3750V<sub>rms</sub> Input/Output Isolation
- 100% Solid State
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

### Applications

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

### Description

LBA110 comprises two independent 350V, 120mA, 35Ω solid state relays: one single-pole, normally open (1-Form-A) relay and one single-pole, normally closed (1-Form-B) relay.

LBA110 is designed to provide an ideal solution where a complementary Form-A/Form-B relay pair is required.

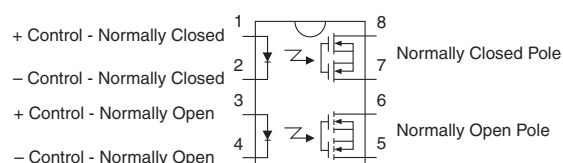
### Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component  
TUV Certificate B 09 07 49410 004

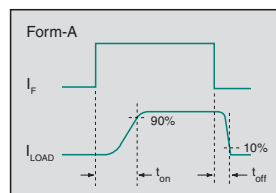
### Ordering Information

| Part #    | Description                      |
|-----------|----------------------------------|
| LBA110    | 8-Pin DIP (50/Tube)              |
| LBA110S   | 8-Pin Surface Mount (50/Tube)    |
| LBA110STR | 8-Pin Surface Mount (1,000/Reel) |
| LBA110P   | 8-Pin Flat Pack (50/Tube)        |
| LBA110PTR | 8-Pin Flat Pack (1,000/Reel)     |

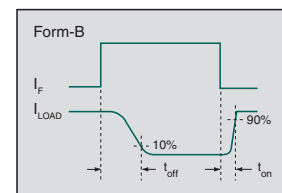
### Pin Configuration



### Switching Characteristics of Normally Open Devices



### Switching Characteristics of Normally Closed Devices



## Absolute Maximum Ratings @ 25°C

| Parameter                            | Ratings     | Units            |
|--------------------------------------|-------------|------------------|
| Blocking Voltage                     | 350         | V <sub>P</sub>   |
| Reverse Input Voltage                | 5           | V                |
| Input Control Current                | 50          | mA               |
| Peak (10ms)                          | 1           | A                |
| Input Power Dissipation <sup>1</sup> | 150         | mW               |
| Total Power Dissipation <sup>2</sup> | 800         | mW               |
| Isolation Voltage, Input to Output   | 3750        | V <sub>rms</sub> |
| Operational Temperature              | -40 to +85  | °C               |
| Storage Temperature                  | -40 to +125 | °C               |

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

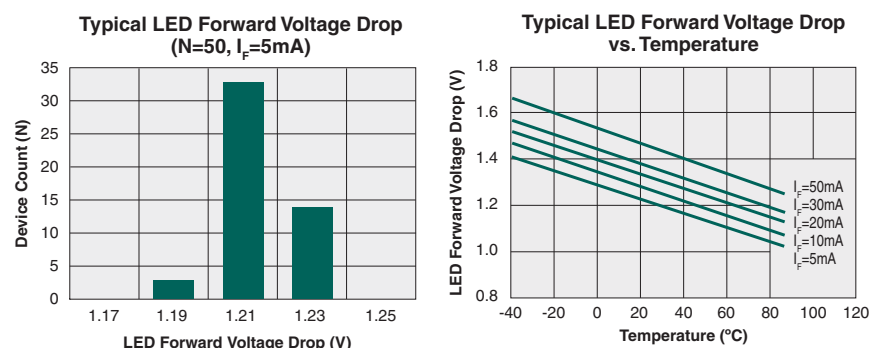
*Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.*

## Electrical Characteristics @ 25°C

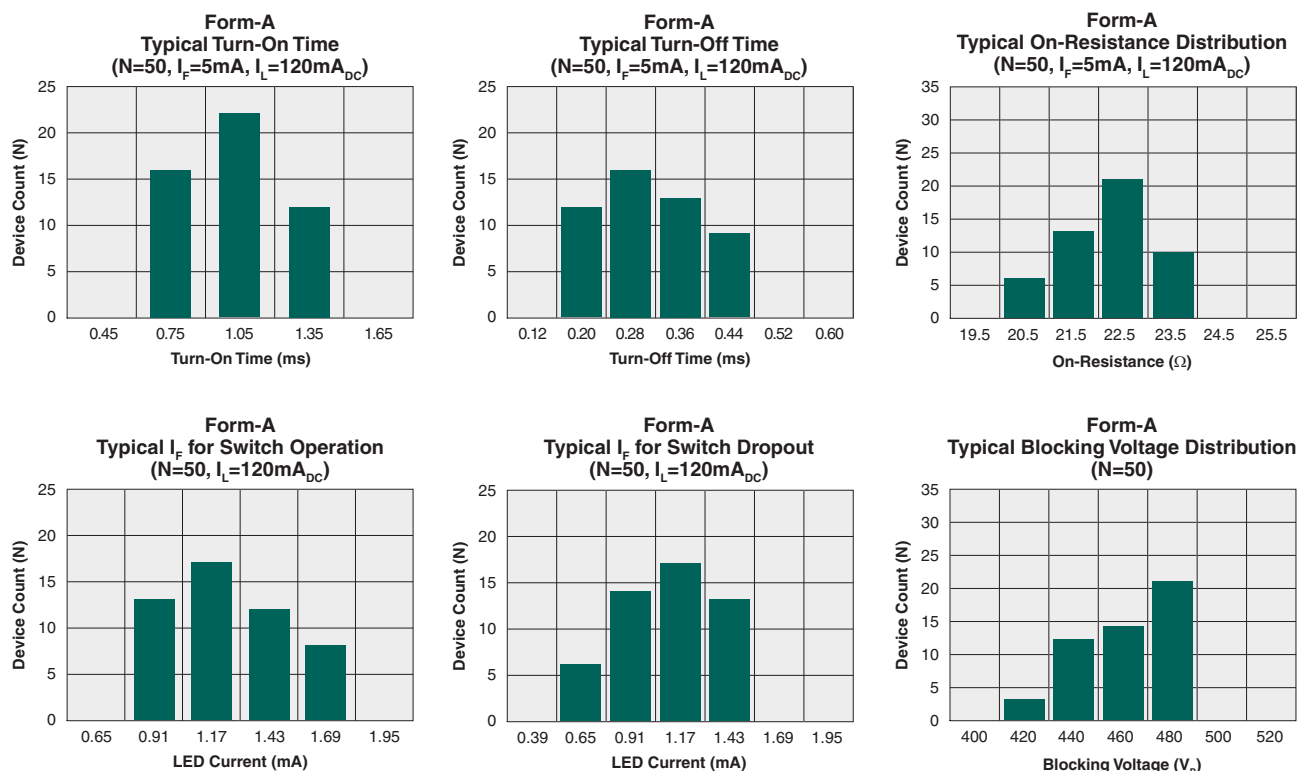
| Parameter                           | Conditions                               | Symbol            | Min | Typ | Max  | Units                                |
|-------------------------------------|------------------------------------------|-------------------|-----|-----|------|--------------------------------------|
| <b>Output Characteristics</b>       |                                          |                   |     |     |      |                                      |
| Load Current                        |                                          |                   |     |     |      |                                      |
| Continuous <sup>1</sup>             | -                                        | I <sub>L</sub>    | -   | -   | 120  | mA <sub>rms</sub> / mA <sub>DC</sub> |
| Peak                                | t = 10ms                                 | I <sub>LPK</sub>  | -   | -   | ±350 | mA <sub>P</sub>                      |
| On-resistance                       | I <sub>L</sub> =120mA                    | R <sub>ON</sub>   | -   | 25  | 35   | Ω                                    |
| Off-State Leakage Current           | V <sub>L</sub> =350V <sub>P</sub>        | I <sub>LEAK</sub> | -   | -   | 1    | μA                                   |
| Switching Speeds                    |                                          |                   |     |     |      |                                      |
| Turn-On                             | I <sub>F</sub> =5mA, V <sub>L</sub> =10V | t <sub>on</sub>   | -   | -   | 3    | ms                                   |
| Turn-Off                            |                                          | t <sub>off</sub>  | -   | -   | 3    |                                      |
| Output Capacitance                  | V <sub>L</sub> =50V, f=1MHz              | C <sub>OUT</sub>  | -   | 25  | -    | pF                                   |
| <b>Input Characteristics</b>        |                                          |                   |     |     |      |                                      |
| Input Control Current to Activate   | I <sub>L</sub> =120mA                    | I <sub>F</sub>    | -   | -   | 2    | mA                                   |
| Input Control Current to Deactivate | -                                        | I <sub>F</sub>    | 0.4 | 0.7 | -    | mA                                   |
| Input Voltage Drop                  | I <sub>F</sub> =5mA                      | V <sub>F</sub>    | 0.9 | 1.2 | 1.4  | V                                    |
| Reverse Input Current               | V <sub>R</sub> =5V                       | I <sub>R</sub>    | -   | -   | 10   | μA                                   |
| <b>Common Characteristics</b>       |                                          |                   |     |     |      |                                      |
| Input to Output Capacitance         | -                                        | C <sub>I/O</sub>  | -   | 3   | -    | pF                                   |

<sup>1</sup> If both poles operate the load current must be derated so as not to exceed the package power dissipation value.

## Form-A/Form-B PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*

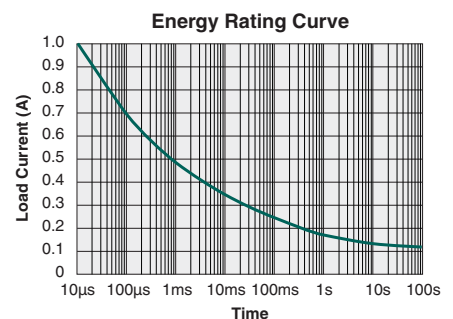
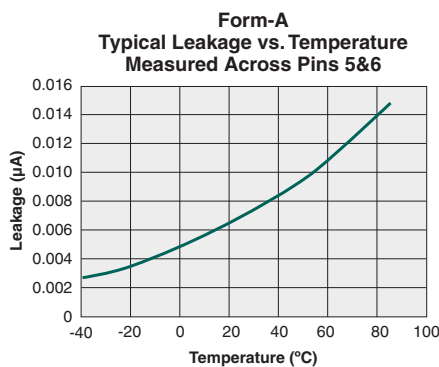
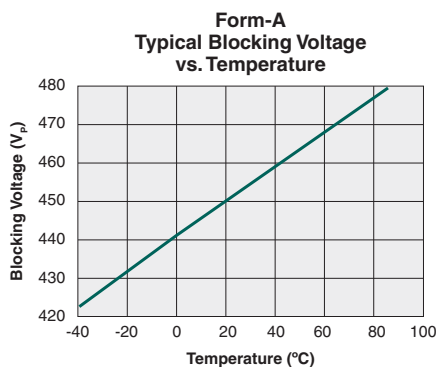
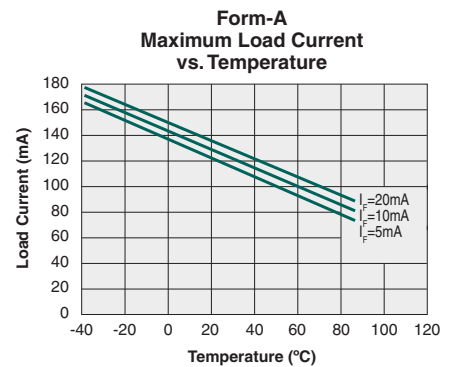
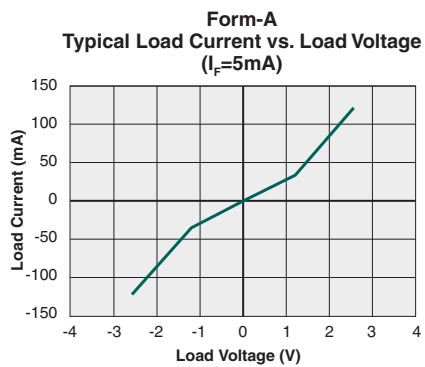
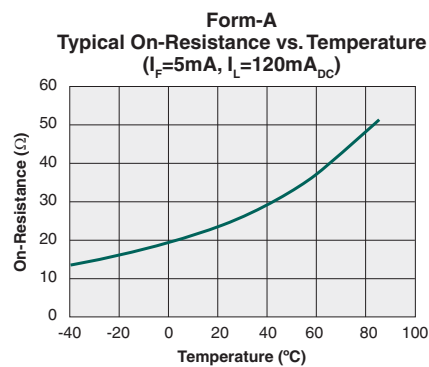
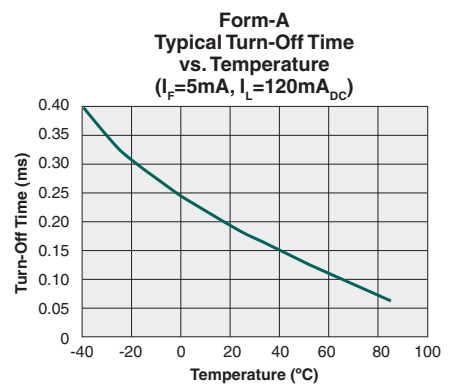
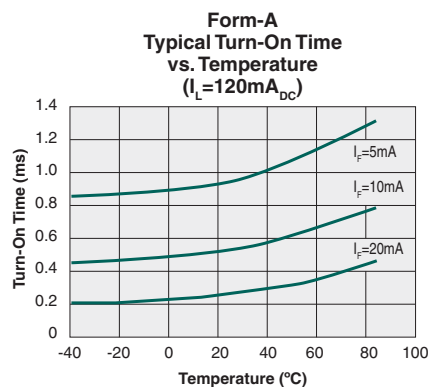
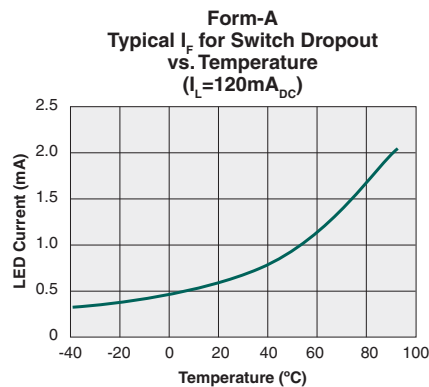
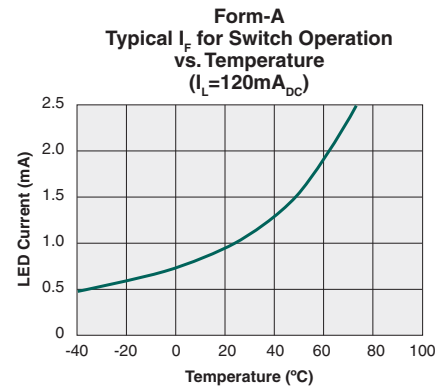
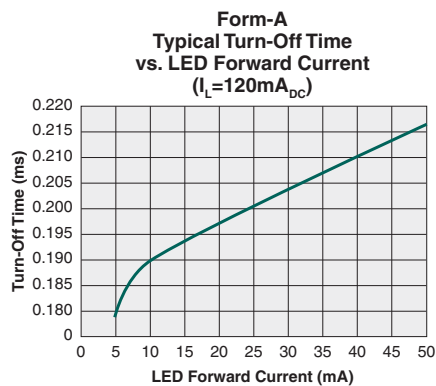
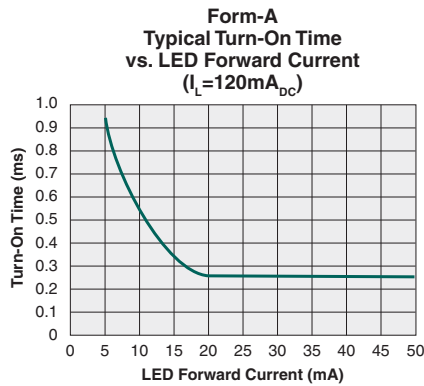


## Form-A PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*



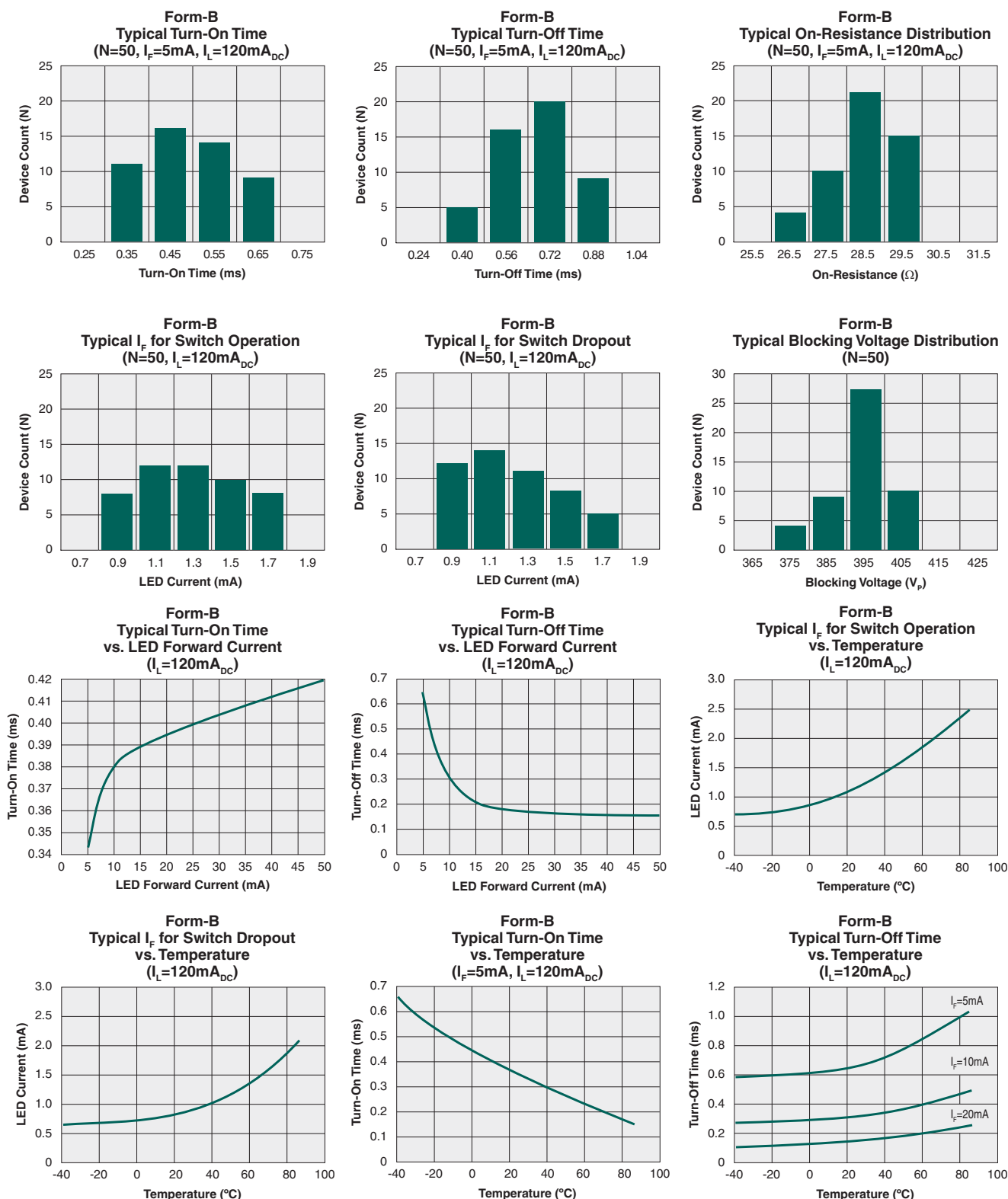
\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

# Form-A PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*



\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

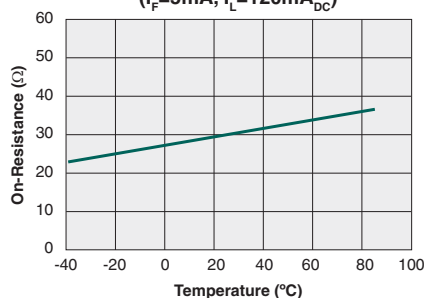
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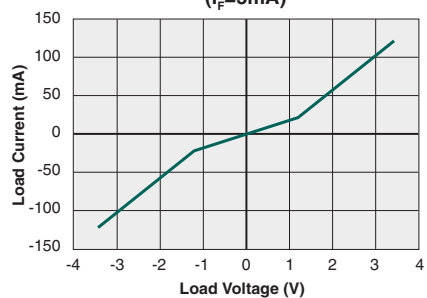
\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Form-B PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*

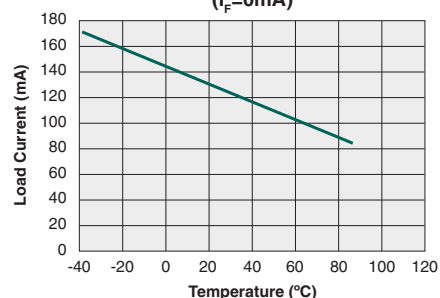
**Form-B**  
**Typical On-Resistance vs. Temperature**  
( $I_F=5\text{mA}$ ,  $I_L=120\text{mA}_{DC}$ )



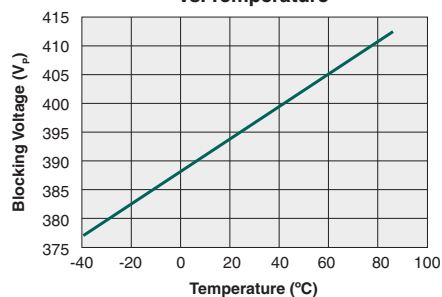
**Form-B**  
**Typical Load Current vs. Load Voltage**  
( $I_F=5\text{mA}$ )



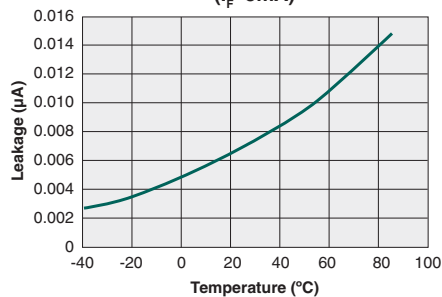
**Form-B**  
**Maximum Load Current vs. Temperature**  
( $I_F=0\text{mA}$ )



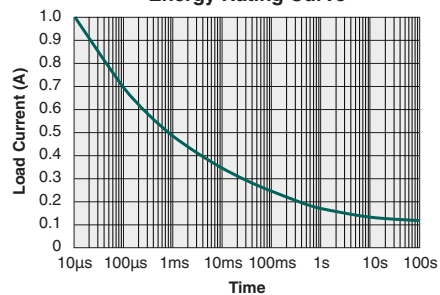
**Form-B**  
**Typical Blocking Voltage vs. Temperature**



**Form-B**  
**Typical Leakage vs. Temperature**  
**Measured Across Pins 5&6**  
( $I_F=5\text{mA}$ )



**Energy Rating Curve**



\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

| Device                     | Moisture Sensitivity Level (MSL) Rating |
|----------------------------|-----------------------------------------|
| LBA110 / LBA110S / LBA110P | MSL 1                                   |

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

| Device           | Maximum Temperature x Time |
|------------------|----------------------------|
| LBA110 / LBA110S | 250°C for 30 seconds       |
| LBA110P          | 260°C for 30 seconds       |

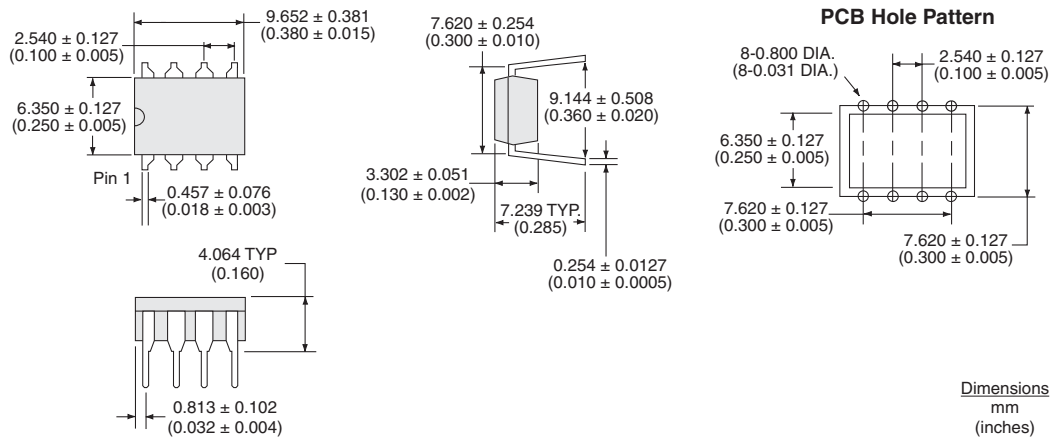
### Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

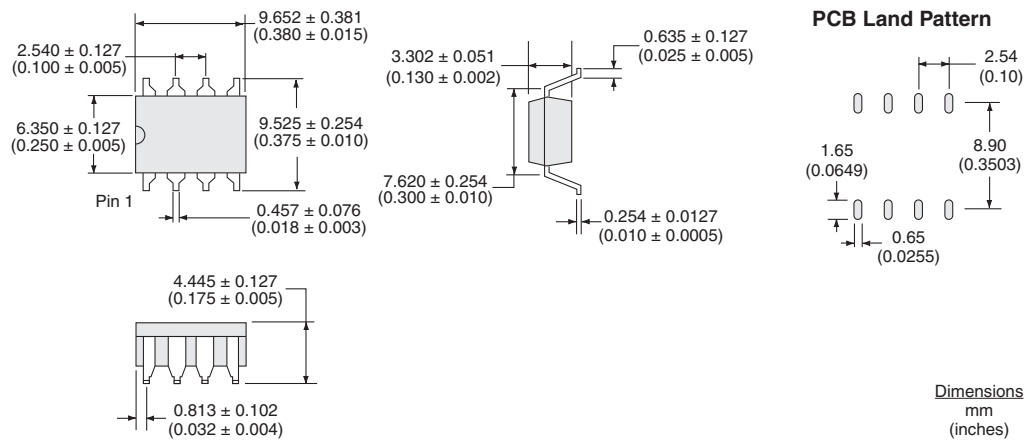


# MECHANICAL DIMENSIONS

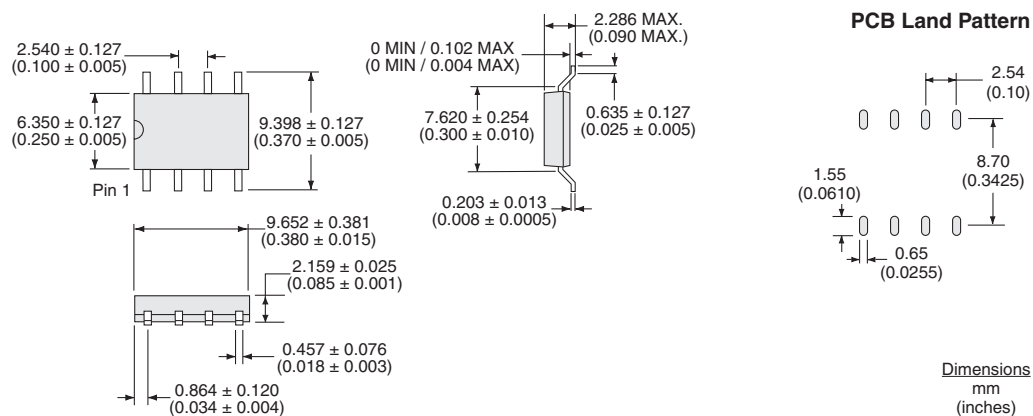
## LBA110



## LBA110S

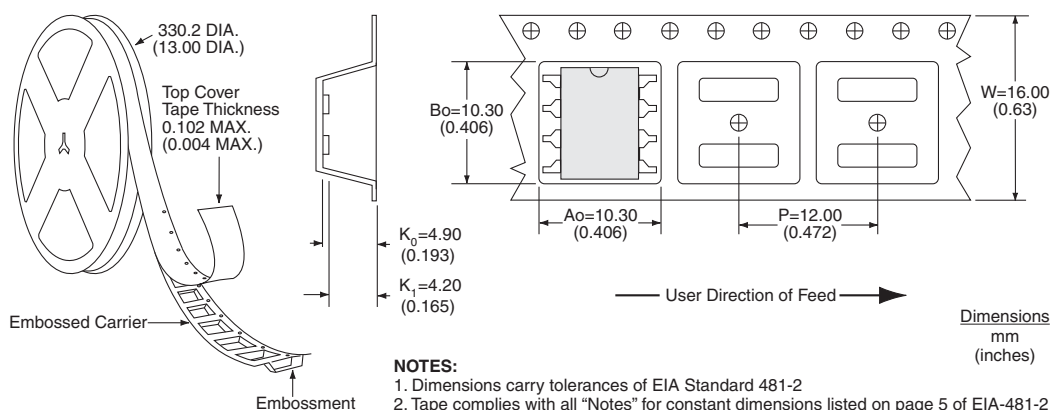


## LBA110P

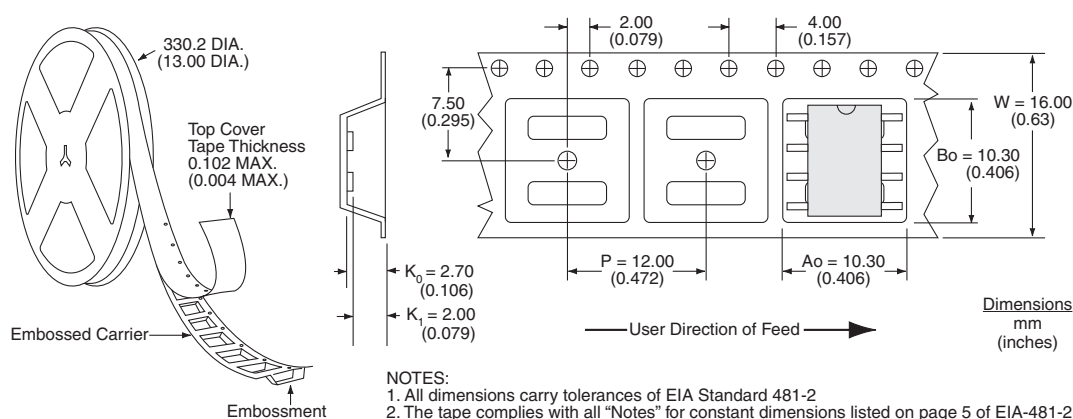


## MECHANICAL DIMENSIONS

### LBA110STR Tape & Reel



### LBA110PTR Tape & Reel



For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)

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Minhang District, Shanghai , China

➤ Sales :

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Email       amall@ameya360.com  
  
QQ         800077892  
  
Skype       ameyasales1 ameyasales2

➤ Customer Service :

Email       service@ameya360.com

➤ Partnership :

Tel         +86 (21) 64016692-8333  
  
Email       mkt@ameya360.com