

## Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$ max	$I_D$ max $T_C = +25^\circ\text{C}$
60V	7.5m $\Omega$ @ $V_{GS} = 10\text{V}$	30A
	11.5m $\Omega$ @ $V_{GS} = 4.5\text{V}$	25A

## Features and Benefits

- Low  $R_{DS(ON)}$  – ensures on state losses are minimized.
- Excellent  $Q_{gd} \times R_{DS(ON)}$  Product (FOM)
- Advanced Technology for DC-DC Converters
- Small form factor thermally efficient package enables higher density end products.
- Occupies just 33% of the board area occupied by SO-8, enabling smaller end product.
- 100% UIS (Avalanche) Rated
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

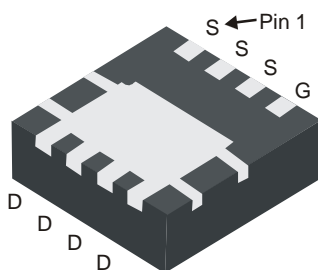
## Description and Applications

This MOSFET is designed to minimize the on-state resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

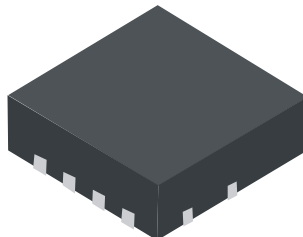
- Synchronous Rectifier
- Backlighting
- Power Management Functions
- DC-DC Converters

## Mechanical Data

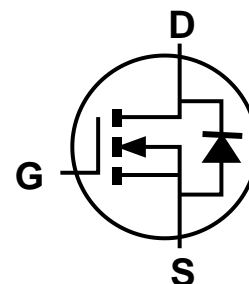
- Case: POWERDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminal Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (Approximate) (G3)



Bottom View



Top View



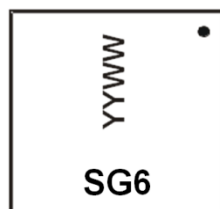
Equivalent Circuit

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6010LFG-7	POWERDI <sup>®</sup> 3333-8	2,000/Tape & Reel
DMT6010LFG-13	POWERDI <sup>®</sup> 3333-8	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



SG6 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last digit of year (ex: 13 = 2013)  
 WW = Week code (01 ~ 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	I <sub>D</sub>	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	A
		13 11	
	I <sub>D</sub>	T <sub>C</sub> = +25°C T <sub>C</sub> = +70°C	A
		30 24	
Maximum Continuous Body Diode Forward Current (Note 5)	I <sub>S</sub>	3	A
Pulsed Drain Current (10μs pulse, duty cycle = 1%)	I <sub>DM</sub>	80	A
Avalanche Current, L=0.1mH	I <sub>AS</sub>	20	A
Avalanche Energy, L=0.1mH	E <sub>AS</sub>	64	mJ

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	T <sub>A</sub> = +25°C T <sub>C</sub> = +25°C	W
		2.2 41	
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	Steady State t<10s	°C/W
		55 35	
Thermal Resistance, Junction to Case (Note 5)	R <sub>θJC</sub>	3	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 6)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 6)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.8	—	2	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	6	7.5	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A
		—	7.8	11.5		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 20A
Diode Forward Voltage	V <sub>SD</sub>	—	0.9	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	C <sub>iss</sub>	—	2090	—	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	746	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	38.5	—		
Gate resistance	R <sub>g</sub>	—	0.59	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	19.3	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 20A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	41.3	—		
Gate-Source Charge	Q <sub>gs</sub>	—	6.0	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	8.8	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	5.7	—	nS	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A, R <sub>G</sub> = 3Ω,
Turn-On Rise Time	t <sub>r</sub>	—	4.3	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	23.4	—		
Turn-Off Fall Time	t <sub>f</sub>	—	9.7	—		

- Notes:
5. R<sub>θJA</sub> is determined with the device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate. R<sub>θJC</sub> is guaranteed by design while R<sub>θJA</sub> is determined by the user's board design.
  6. Short duration pulse test used to minimize self-heating effect.
  7. Guaranteed by design. Not subject to product testing.

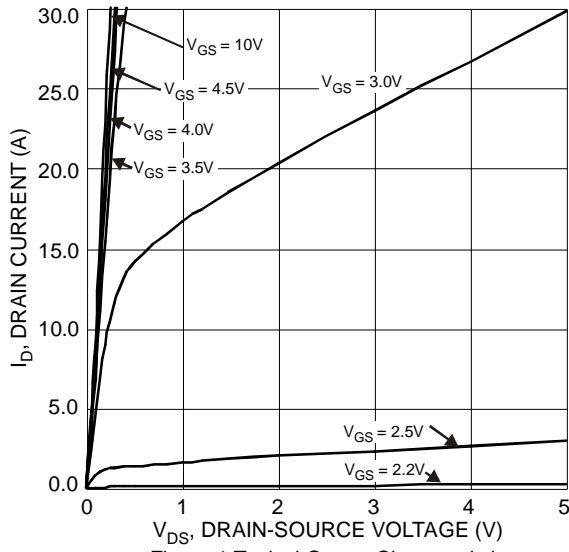


Figure 1 Typical Output Characteristic

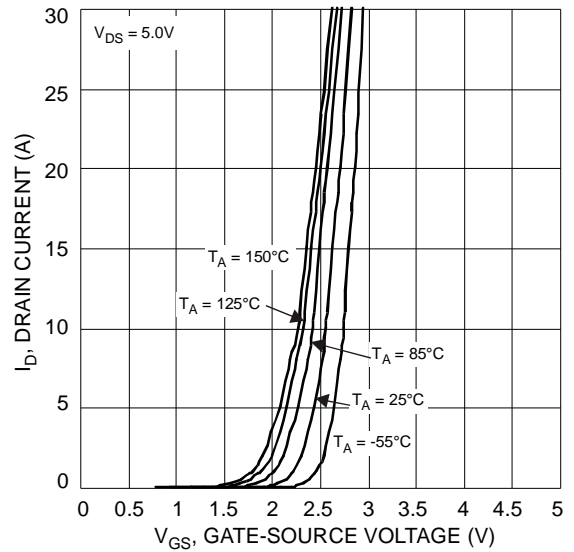


Figure 2 Typical Transfer Characteristics

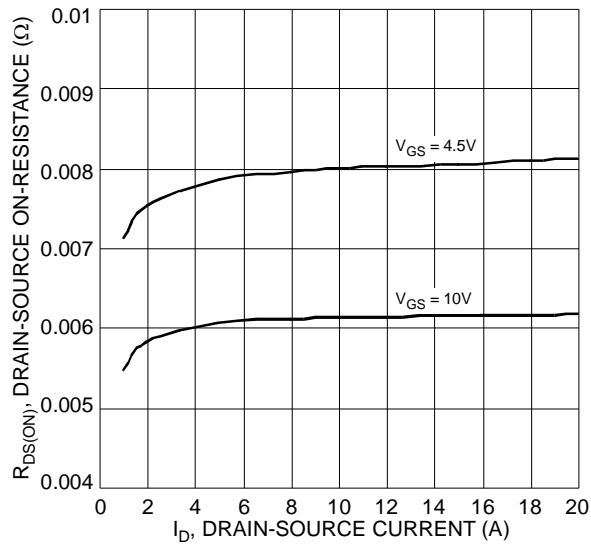


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

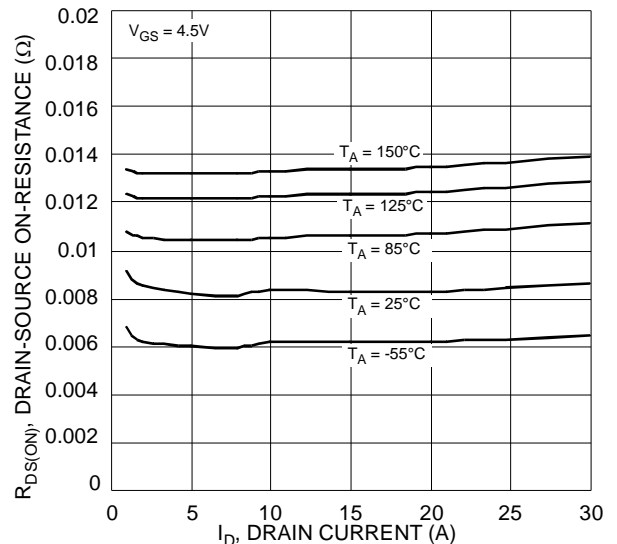


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

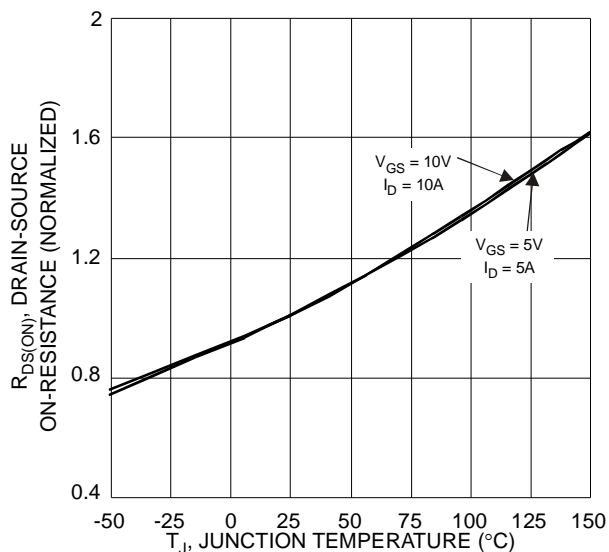


Figure 5 On-Resistance Variation with Temperature

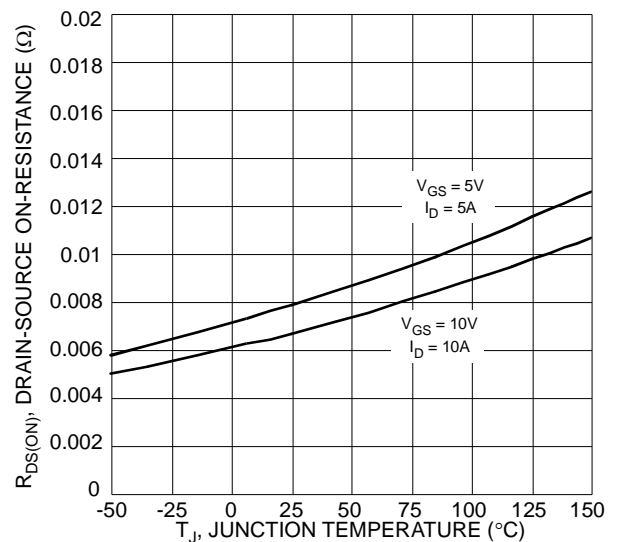


Figure 6 On-Resistance Variation with Temperature

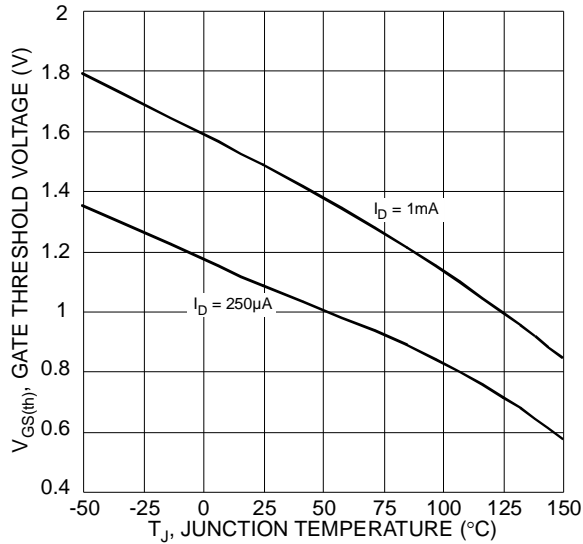


Figure 7 Gate Threshold Variation vs. Ambient Temperature

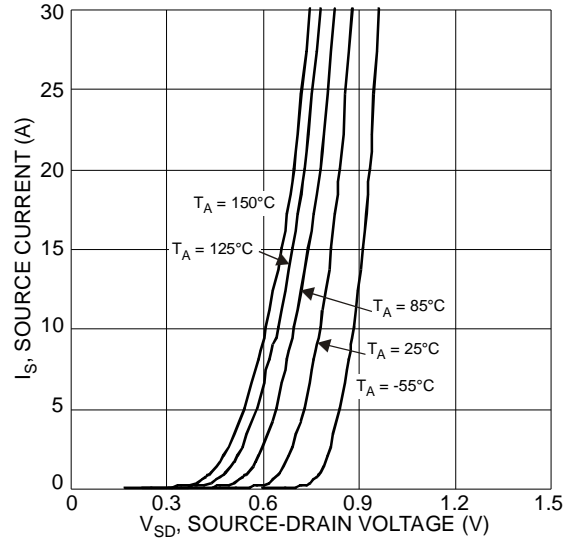


Figure 8 Diode Forward Voltage vs. Current

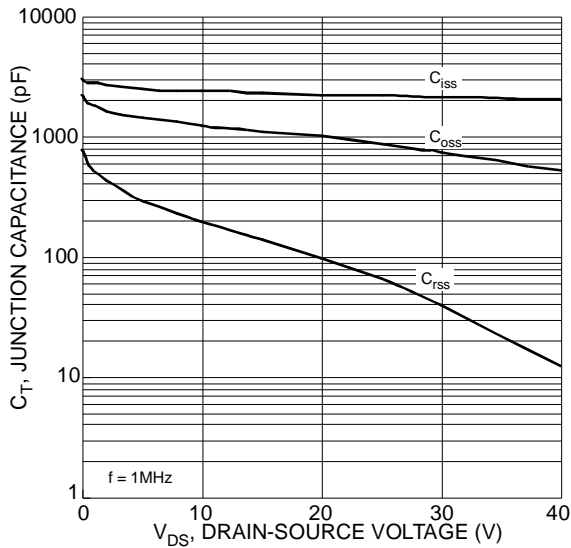


Figure 9 Typical Junction Capacitance

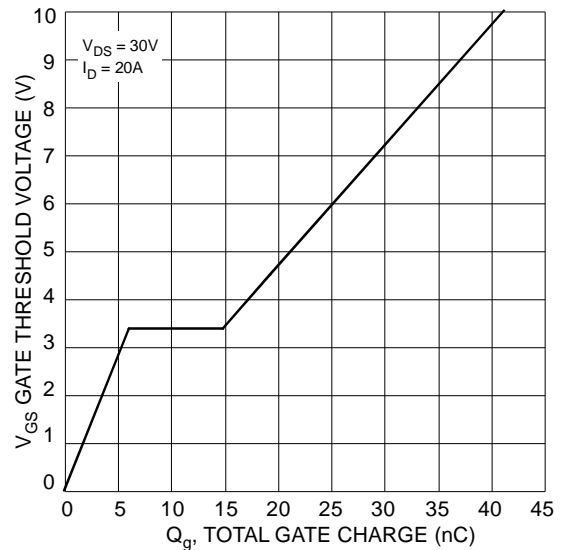


Figure 10 Gate Charge

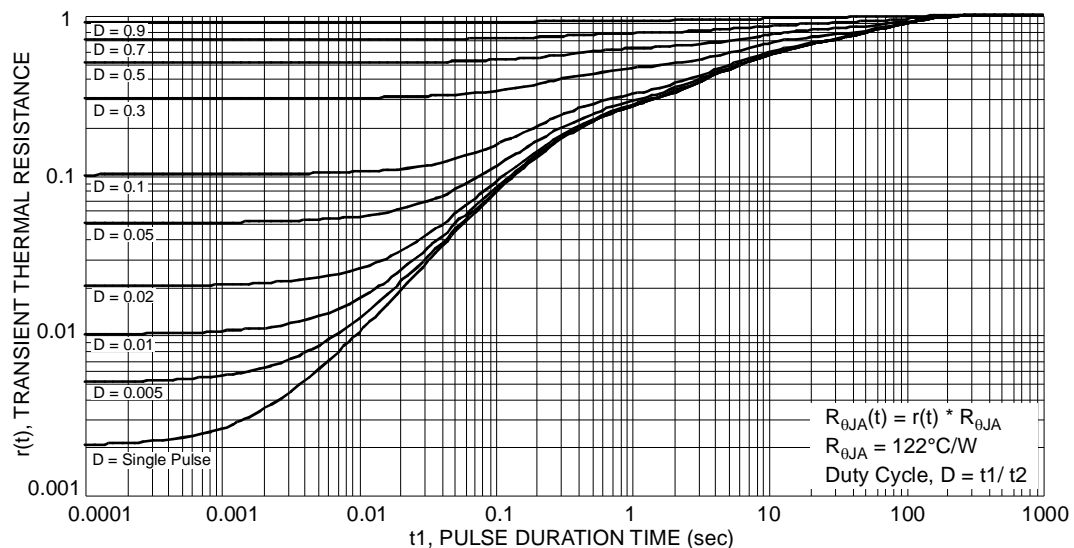
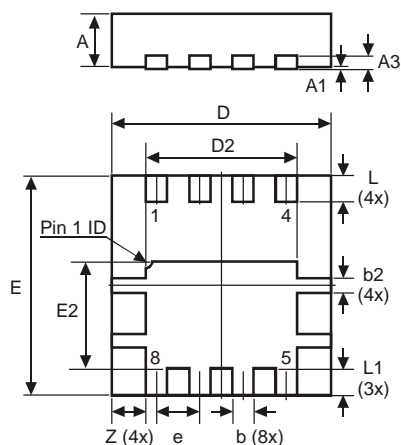


Figure 11 Transient Thermal Resistance

## Package Outline Dimensions

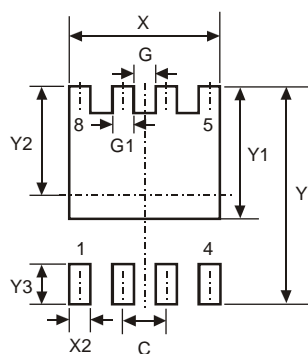
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



POWERDI®3333-8			
Dim	Min	Max	Typ
D	3.25	3.35	3.30
E	3.25	3.35	3.30
D2	2.22	2.32	2.27
E2	1.56	1.66	1.61
A	0.75	0.85	0.80
A1	0	0.05	0.02
A3	—	—	0.203
b	0.27	0.37	0.32
b2	—	—	0.20
L	0.35	0.45	0.40
L1	—	—	0.39
e	—	—	0.65
Z	—	—	0.515
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.650
G	0.230
G1	0.420
Y	3.700
Y1	2.250
Y2	1.850
Y3	0.700
X	2.370
X2	0.420

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