





30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C		
-30V	$7.5 \text{m}\Omega$ @ V_{GS} = -10V	-12A		
-30 V	10.2mΩ @ V _{GS} = -4.5V	-10A		

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

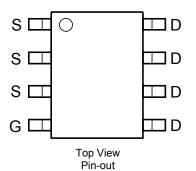
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

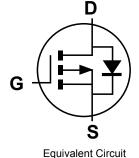
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram below
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208 (23)
- Weight: 0.074 grams (approximate)









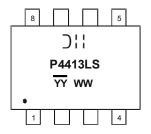
Ordering Information (Note 4)

ĺ	Part Number	Case	Packaging
	DMG4413LSS-13	SO-8	2500/Tape & Reel

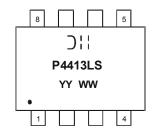
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Chengdu A/T Site



Shanghai A/T Site

O!! = Manufacturer's Marking
P4413LS = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 13 = 2013)
WW = Week (01 - 53)

YY = Date Code Marking for SAT (Shanghai Assembly/ Test site)

YY = Date Code Marking for CAT (Chengdu Assembly/ Test site)



Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Dusin Courset (Nata C) // - 40 //	Steady State	T _A = +25°C T _A = +70°C	I _D	-12 -10	А
Continuous Drain Current (Note 6) V _{GS} = -10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-22 -17	Α
Continuous Drain Current (Note 6) \ - 4 5\	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-10 -8	Α
Continuous Drain Current (Note 6) V _{GS} = -4.5V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-18 -14	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1	I _{DM}	-100	Α		
Maximum Body Diode continuous Current	Is	-4	Α		

Thermal Characteristics

Characteristic	Symbol	Value	Units		
Total Dowar Dissination (Note 5)	T _A = +25°C	Б	1.7	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P _D	1.1	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0,JA}	74	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	MθJA	22	C/VV	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	P _D	2.2	W	
Total Fower Dissipation (Note 0)	$T_A = +70^{\circ}C$	FD	1.4	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	56		
Thermal Resistance, Junction to Ambient (Note 0)	t<10s	MθJA	17	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{ heta JC}$	2.5		
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to 150	°C	

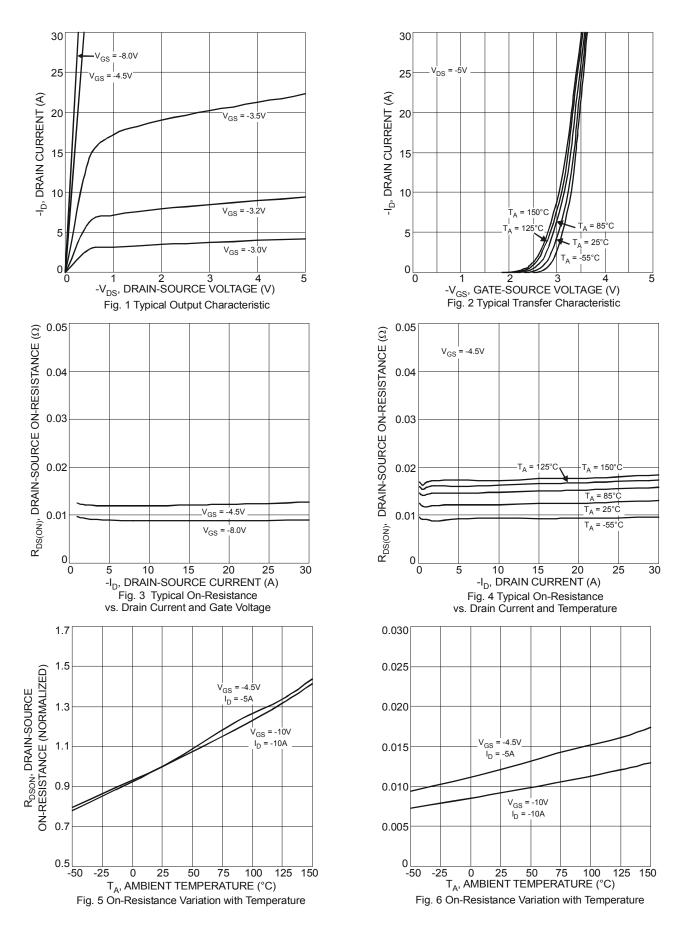
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30		_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_		-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±1	μА	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-1.1	1.6	-2.1	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance			6.3	7.5	mΩ	$V_{GS} = -10V, I_D = -13A$
Static Diain-Source On-Resistance	R _{DS(ON)}	_	7.9	10.2	1112.2	$V_{GS} = -4.5V$, $I_{D} = -10A$
Forward Transconductance	9 _{fs}	_	26	_	S	$V_{DS} = -15V$, $I_D = -13A$
Diode Forward Voltage	V_{SD}	_	-0.7	-1.0	V	$V_{GS} = 0V, I_S = -2.7A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	4965	_	pF	V - 45V V - 0V
Output Capacitance	Coss	_	1487	_	pF	V _{DS} = -15V, V _{GS} = 0V -f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	711	_	pF	1 - 1.0101112
Gate Resistance	R _G	_	7.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$ f = 1.0MHz
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Q_{G}	_	46	_		V - 45V V - 5V
Gate-Source Charge	Q_{GS}	_	17	_	nC	$V_{DS} = -15V, V_{GS} = -5V$ $I_{D} = -13A$
Gate-Drain Charge	Q_{GD}	_	16	_		ID = -13A
Turn-On Delay Time	t _{d(on)}	_	15	_		
Rise Time	t _r	_	9	_	$V_{DS} = -15V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{d(off)}	_	160	_	ns	$I_D = -1A, R_G = 6.0\Omega$
Fall Time	t _f	_	66			

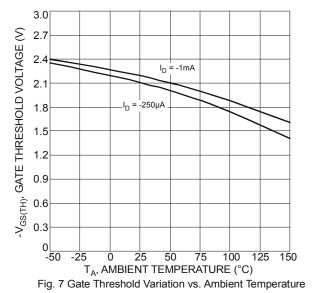
Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

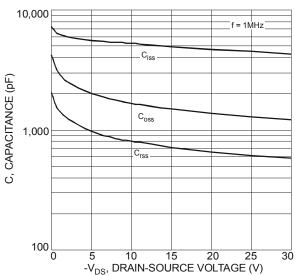








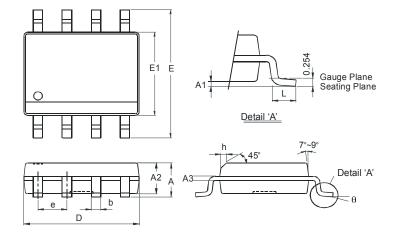
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Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Fig. 9 Typical Total Capacitance

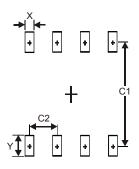


SO-8						
Dim	Min	Max				
Α	1	1.75				
A1	0.10	0.20				
A2	1.30	1.50				
A3	0.15	0.25				
b	0.3	0.5				
D	4.85	4.95				
Е	5.90	6.10				
E1	3.85 3.95					
е	e 1.27 Typ					
h	1	0.35				
١	0.62	0.82				
θ	0°	8°				
All Dimensions in mm						



Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27

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