

U232

N-Channel Dual Silicon Junction Field-Effect Transistor

- Differential Amplifier
- Low & Maximum Frequency Amplifier

Absolute maximum ratings at $T_A = 25^\circ\text{C}$

Reverse Gate Source & Gate Drain Voltage	-50V
Continuous Forward Gate Current	50 mA
Continuous Device Power Dissipation	300 mW
Power Derating	1.7 mW/ $^\circ\text{C}$
Storage Temperature Range	-65 $^\circ\text{C}$ to +150 $^\circ\text{C}$

At 25 $^\circ\text{C}$ free air temperature

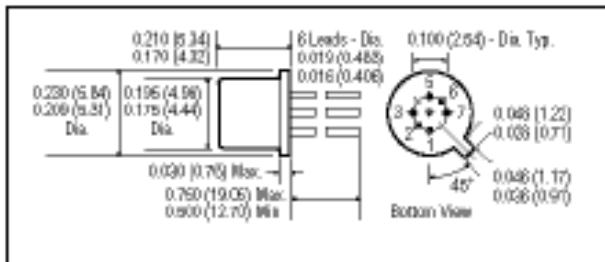
Static Electrical Characteristics

		U232			Process NJ16		
		Min	Typ	Max	Unit	Test Conditions	
Gate Source Breakdown Voltage	$V_{(BR)GSS}$	-50			V	$I_G = -1 \mu\text{A}$, $V_{DS} = 0 \text{ V}$	
Gate Reverse Current	I_{GSS}			-0.1	nA	$V_{GS} = -10 \text{ V}$, $V_{DS} = 0 \text{ V}$	
Gate Source Cutoff Voltage	$V_{GS(OFF)}$	-0.5		-4.5	V	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0 \text{ V}$	
Drain Saturation Current (pulsed)	I_{DSS}	0.5		5	mA	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0 \text{ V}$	

Dynamic Electrical Characteristics

Common-Source Forward Transconductance	g_{fs}	1		3	mS	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0 \text{ V}$	$f = 1 \text{ kHz}$
Common-Source Input Capacitance	C_{iss}			6	pF	$V_{DS} = 10 \text{ V}$, $I_D = 5 \text{ mA}$	$f = 1 \text{ MHz}$
Common-Source Reverse Transfer Capacitance	C_{rss}			2	pF	$V_{DS} = 10 \text{ V}$, $I_D = 5 \text{ mA}$	$f = 1 \text{ MHz}$
Equivalent Short Circuit Input Noise Voltage	$\sim e_N$			80	nV/ $\sqrt{\text{Hz}}$	$V_{DS} = 10 \text{ V}$, $I_D = 5 \text{ mA}$	$f = 100 \text{ Hz}$

Matching Characteristics		Min	Max	Units	Test Conditions
Differential Gate-Source Voltage	$(V_{GS1} - V_{GS2})$		10	mV	$V_{DS} = 10 \text{ V}$, $I_D = -10 \text{ mA}$
Differential Gate Source Voltage with Temperature	$\frac{\Delta(V_{GS1} - V_{GS2})}{\Delta T}$		25	$\mu\text{V}/^\circ\text{C}$	$V_{DS} = 10 \text{ V}$, $I_D = 30 \mu\text{A}$



TO-71 Package
 Dimensions in inches (mm)
Pin Configuration
 1 Source, 2 Drain, 3 Gate,
 5 & 6 Drain, 6 Drain, 7 Gate

Surface Mount Version:
 SMPU232



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