TOSHIBA Photocoupler GaAlAs IRED + Photo IC

TLP350

Industrial Inverter
Inverter for Air Conditioner
IGBT/Power MOSFET Gate Drive
IH (Induction Heating)

The TOSHIBA TLP350 consists of a GaAlAs light-emitting diode and an integrated photodetector.

This unit is an 8-lead DIP package.

The TLP350 is suitable for gate driving IGBTs or power MOSFETs.

• Peak output current : $I_0 = \pm 2.5 A \text{ (max)}$

• Guaranteed performance over temperature: -40 to 100°C

• Supply current : $I_{CC} = 2 \text{ mA (max)}$

• Power supply voltage: Vcc = 15 to 30 V

Threshold input current: IFLH = 5 mA (max)

• Switching time (t_{pLH}/t_{pHL}): 500 ns (max)

• Common mode transient immunity: 15 kV/µs

• Isolation voltage: 3750 Vrms

• UL Recognized: UL1577, File No. E67349

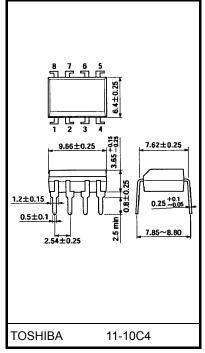
• Option(D4)

VDE Approved: DIN EN 60747-5-2

 $\begin{aligned} & \text{Maximum Operating Insulation Voltage:} & 890 V_{PK} \\ & \text{Highest Permissible Over Voltage} & \vdots & 6000 V_{PK} \end{aligned}$

(Note): When a EN 60747-5-2 approved type is needed, Please designate "Option(D4)"

Unit: mm

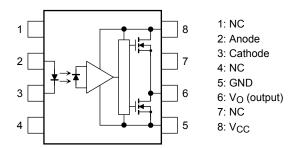


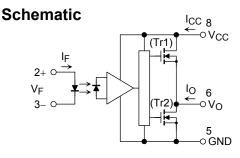
Weight: 0.54 g (typ.)

Truth Table

Input	LED	Tr1	Tr2	Output
Н	ON	ON	OFF	Н
L	OFF	OFF	ON	L

Pin Configuration (top view)





A 0.1 μF bypass capacitor must be connected between pins 8 and 5. (See Note 6)

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit		
	Forward current	lF	20	mA		
	Forward current derating (Ta ≥ 8	∆l _F /∆Ta	l _F /∆Ta –0.54			
	Peak transient forward current	(Note 1)	I _{FPT}	I _{FPT} 1		
	Reverse voltage		V_{R}	5	V	
	Junction temperature		Tj	125	°C	
	"H" peak output current	Ta = -40 to 100°C	I _{OPH}	-2.5	Α	
ō	"L" peak output current	(Note 2)	I _{OPL}	2.5	Α	
Detector	Supply voltage	Ta < 95 °C	V _{CC}	35	V	
۵	Supply voltage Derating	Ta ≥ 95 °C	$\Delta V_{CC}/\Delta Ta$	-1.0	V /°C	
	Junction temperature		Tj	125	°C	
Oper	rating frequency	f	50	kHz		
Stora	age temperature range	T _{stg}	-55 to 125	°C		
Oper	rating temperature range	T _{opr}	-40 to 100	°C		
Lead	soldering temperature (10 s)	T _{sol}	260	°C		
Isola	tion voltage (AC, 1 minute, R.H. ≤	60%) (Note 5)	BVS	3750	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Pulse width $P_W \le 1 \mu s$, 300 pps
- Note 2: Exponential waveform pulse width $P_W \le 0.3 \mu s$, $f \le 15 \text{ kHz}$
- Note 3: Exponential waveform $I_{OPH} \ge -2.0A$ ($\le 0.3\mu s$), $I_{OPL} \le 2.0A$ ($\le 0.3\mu s$)
- Note 4: At 2 mm or more from the lead root.
- Note 5: This device is regarded as a two terminal device: pins 1, 2, 3 and 4 are shorted together, as are pins 5, 6, 7 and 8.
- Note 6: A ceramic capacitor $(0.1 \, \mu F)$ should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypass may impair the switching property. The total lead length between capacitor and coupler should not exceed 1 cm.

Recommended Operating Conditions

Characteristic		Symbol	Min	Тур.	Max	Unit
Input current, ON	(Note 7)	I _{F (ON)}	7.5	_	10	mA
Input voltage, OFF		V _F (OFF)	0	_	0.8	V
Supply voltage		V _{CC}	15	_	30	V
Peak output current		I _{OPH} /I _{OPL}	_	_	±2.0	Α
Operating temperature		T _{opr}	-40	_	100	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 7: Input signal rise time (fall time) $< 0.5 \mu s$.

Note 8: If the rising slope of the supply voltage (V_{CC}) for the detector is steep, stable operation of the internal circuits cannot be guaranteed.

Be sure to set $3.0V/\mu s$ or less for a rising slope of the V_{CC} .

Electrical Characteristics (Ta = -40 to 100°C, unless otherwise specified)

Characteristic		Symbol	Test Circuit	Test C	onditions	Min	Тур.*	Max	Unit
Forward voltage		V _F		$I_F = 10 \text{ mA}, \text{ Ta} = 25^{\circ}\text{C}$		_	1.6	1.8	V
Temperature coefficient of forward voltage		∆V _F /∆Ta	_	I _F = 10 mA		_	-2.0	_	mV/°C
Input reverse current		I _R	_	V _R = 5 V, Ta = 25°C		_	_	10	μА
Input capacitance	_	C _T	_	V = 0 , f = 1 MHz, Ta = 25°C		_	45	250	pF
Output current (Note 9)	"H" Level	Іорн	1	$V_{CC} = 30 \text{ V,I}_F = 5 \text{ mA}$ $V_{8-6} = 3.5 \text{ V}$		_	-1.6	-1.0	
	II Level	ЮРН	'	$V_{CC} = 15 \text{ V}, I_F = 5 \text{ mA}$ $V_{8-6} = 7.0 \text{ V}$		_	_	-2.0	А
	"L" Level lop	2	$V_{CC} = 30 \text{ V,I}_F = 0 \text{ mA}$ $V_{6-5} = 2.5 \text{V}$		1.0	1.6	_	۸	
	L Levei	Level I _{OPL}	2	$V_{CC} = 15 \text{ V,I}_F = 0 \text{ mA}$ $V_{6-5} = 7.0 \text{V}$		2.0	_	_	
Output voltage	"H" Level	V _{OH}	3	V _{CC 1=} +15 V V _{EE 1=} +15 V	I _F = 5 mA	11	13.7	_	V
Output voltage	"L" Level	V _{OL}	4	$R_L = 200 \Omega$	V _F = 0.8 V	_	-14.9	-12.5	V
Cumply ourrant	"H" Level	Icch	5	V _{CC} = 30 V	I _F = 10 mA	_	1.3	2.0	mA
Supply current	"L" Level	ICCL	6	V _O open	$I_F = 0 \text{ mA}$	_	1.3	2.0	IIIA
Threshold input current	$L \rightarrow H$	I _{FLH}	_	V _{CC} = 15V , V _O > 1V , I _O = 0mA		_	1.8	5	mA
Threshold input voltage	$H \rightarrow L$	V _{FHL}	_	V _{CC} = 15V , V _O < 1V , I _O = 0mA		0.8	_	_	V
Supply voltage		V _{CC}	_	_		15	_	30	V
UVLO threshhold		V _{UVLO+}	_	V _O > 2.5 V , I _F = 5 mA		11.0	12.5	13.5	V
		V _{UVLO-}	_			9.5	11.0	12.0	V
UVLO hysteresis		UVLO _{HYS}	_			_	1.5	_	V

^{*:} All typical values are at Ta = 25°C

Note 9: Duration of I_O : $\leq 50 \mu s$ (1 PULSE)

Note 10: This product is more sensitive to static electricity (ESD) than the conventional product because of its minimal power consumption design.

General static electricity precautions are necessary for handling this component.

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V = 0,f = 1MHz (Note5)		1.0	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60% (Note5)	1×10 ¹²	10 ¹⁴	_	Ω
	BV_S	AC,1 minute	3750	_	_	V _{rms}
Isolation voltage		AC,1 second,in oil	_	10000	_	v rms
		DC,1 minute,in oil	_	10000	-	Vdc

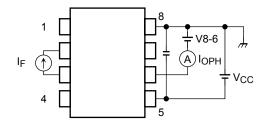
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Switching Characteristics (Ta = -40 to 100° C, unless otherwise specified)

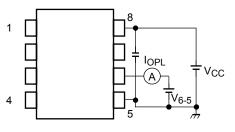
Characteristic		Symbol	Test Circuit	Test Conditions		Min	Typ.*	Max	Unit	
Propagation delay time	$L \rightarrow H$	t _{pLH}			$I_F = 0 \rightarrow 5 \text{ mA}$	50	260	500		
Tropagation delay time	$H \rightarrow L$	t _{pHL}		$R_g = 20 \Omega$ $C_g = 10 \text{ nF}$	$I_F = 5 \rightarrow 0 \text{ mA}$	50	260	500		
Switching Time Dispersion between ON and OFF		t _{рНL} -t _{рLН}	7	$V_{CC} = 30 \text{ V}$ $R_g = 20 \Omega$, $C_g = 10 \text{ nF}$		_	_	350	ns	
Output rise time (10-90%)		t _r		Vcc	V _{CC} = 30 V	$I_F = 0 \rightarrow 5 \text{ mA}$	_	15	_	
Output fall time (90-10%)		t _f		$R_g = 20 \Omega$ $C_g = 10 \text{ nF}$	$I_F = 5 \rightarrow 0 \text{ mA}$	_ 8	8	_		
Common mode transient immunity at high level output		CMH		8	$I_F = 5 \text{ mA}$ $V_{O \text{ (min)}} = 26 \text{V}$	-15000	_	_	\//a	
Common mode transient immunity at low level output		CML	ð		I _F = 0 mA V _{O (max)} =1V	15000	_	_	V/μs	

^{*:} All typical values are at Ta = 25°C

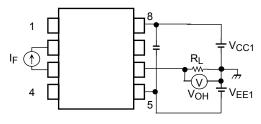
Test Circuit 1: IOPH



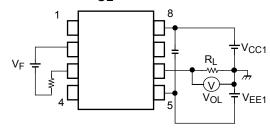
Test Circuit 2: I_{OPL}



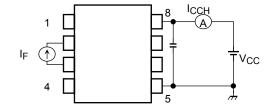
Test Circuit 3: V_{OH}



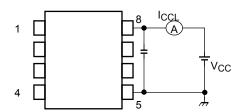
Test Circuit 4: V_{OL}



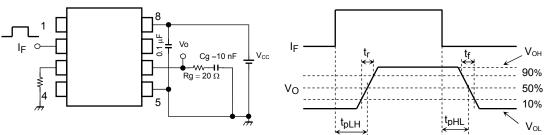
Test Circuit 5: I_{CCH}



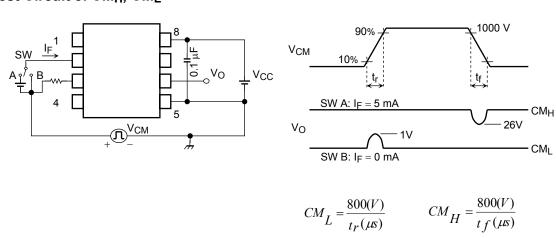
Test Circuit 6: I_{CCL}



Test Circuit 7: t_{pLH} , t_{pHL} , t_{r} , t_{f} , PDD

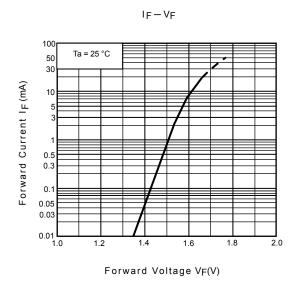


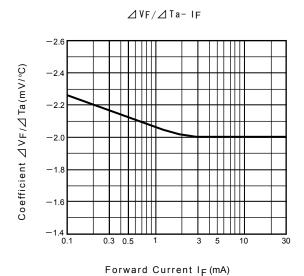
Test Circuit 8: CMH, CML

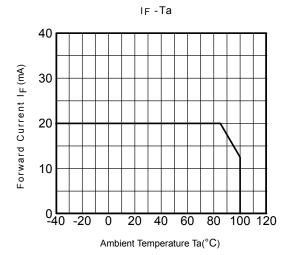


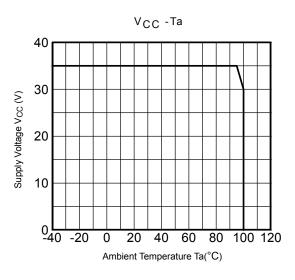
CM_L (CM_H) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

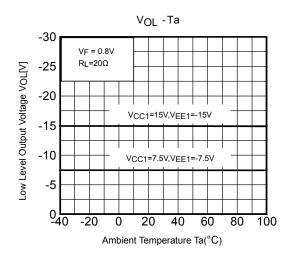
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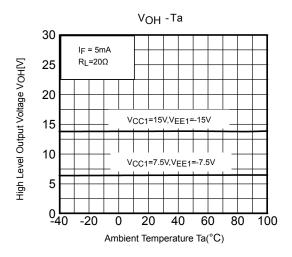




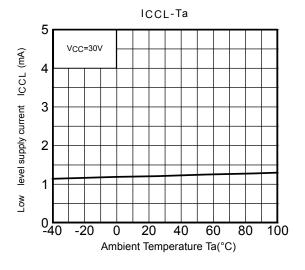


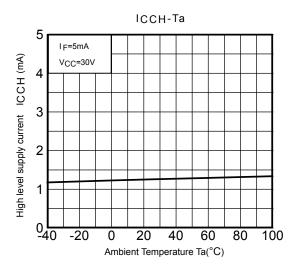


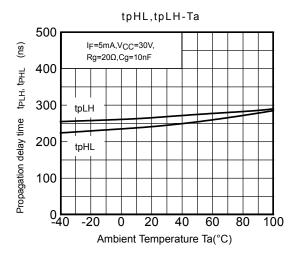


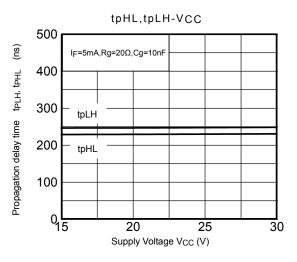


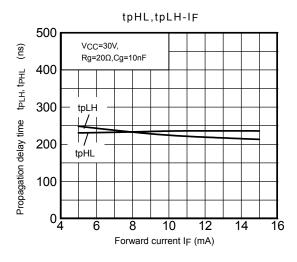
^{*:} The above graphs show typical characteristics.

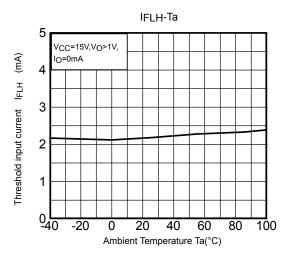




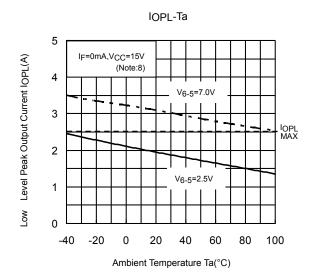


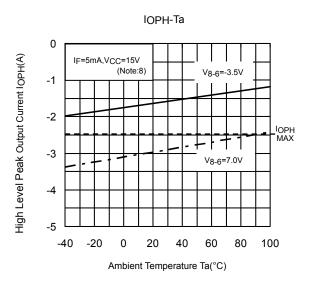


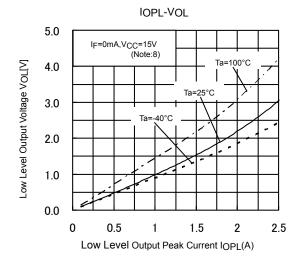


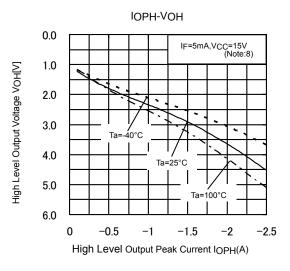


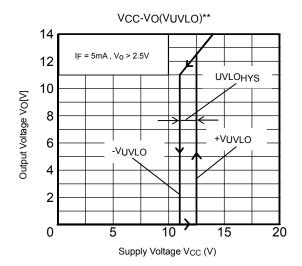
^{*:} The above graphs show typical characteristics.

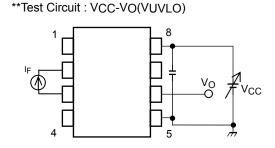












^{*:} The above graphs show typical characteristics.

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