



DMN3033LSN

#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Features**

- Low Gate Charge
- Low R<sub>DS(ON)</sub>:
  - 30 mΩ @V<sub>GS</sub> = 10V
- 40 m $\Omega$  @V<sub>GS</sub> = 4.5V
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- "Green" Device (Note 4)

### **Mechanical Data**

- Case: SC-59
- Case Material Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.014 grams (approximate)

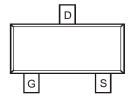


Top View



Drain

Equivalent Circuit



Pin Configuration

#### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Drain Current (Note 1) Continuous	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	Ι <sub>D</sub>	6 5	А
Pulsed Drain Current (Note 2)		I <sub>DM</sub>	24	A
Body-Diode Continuous Current (Note 1)		Is	2.25	A

## Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 1)	PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 1) t ≤10s	R <sub>θJA</sub>	90	°C /W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 1. Device mounted on 1"x1", FR-4 PC board with 2 oz. Copper and test pulse width t  $\leq$ 10s.

2. Repetitive Rating, pulse width limited by junction temperature.

3. No purposefully added lead.

4. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

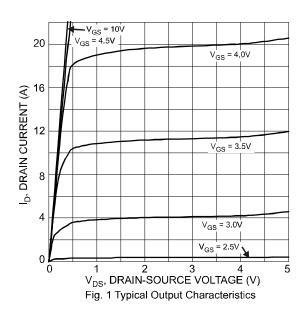


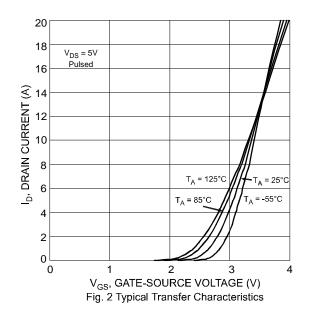
## **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Мах	Unit	Test Condition
STATIC PARAMETERS		Cymbol		- IJP	max	Unit	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	—	V	$I_{D} = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	T <sub>J</sub> = 25°C T <sub>J</sub> = 55°C	I <sub>DSS</sub>	_	_	1 5	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage Current		I <sub>GSS</sub>	_		±100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.0		2.1	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance (Note 5)	R <sub>DS (ON)</sub>	_	25 36	30 40	mΩ	$V_{GS} = 10V, I_D = 6A$ $V_{GS} = 4.5V, I_D = 5A$	
Forward Transconductance (Note 5)			_	5	_	S	$V_{DS} = 10V, I_D = 8A$
Diode Forward Voltage (Note 5)			_	0.7	1.1	V	I <sub>S</sub> = 2.25A, V <sub>GS</sub> = 0V
DYNAMIC PARAMETERS (Note 6)							
Total Gate Charge		Qg	_	10.5		nC	$V_{GS} = 5V, V_{DS} = 15V, I_D = 6A$
Gate-Source Charge		Q <sub>gs</sub>	_	3.8		nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$
Gate-Drain Charge		Q <sub>gd</sub>	_	2.9		nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$
Turn-On Delay Time		t <sub>D(on)</sub>	_	11	—	ns	
Turn-On Rise Time		tr	_	7	_	ns	V <sub>DD</sub> = 15V, V <sub>GS</sub> = 10V,
Turn-Off Delay Time		t <sub>D(off)</sub>	_	63		ns	$R_D = 1.8\Omega, R_G = 6\Omega$
Turn-Off Fall Time		t <sub>f</sub>		30		ns	
Input Capacitance		Ciss	_	755	_	pF	
Output Capacitance		Coss	_	136		pF	−V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V −f = 1.0MHz
Reverse Transfer Capacitance		C <sub>rss</sub>	_	108		pF	

Notes: 5. Test pulse width t = 300ms.

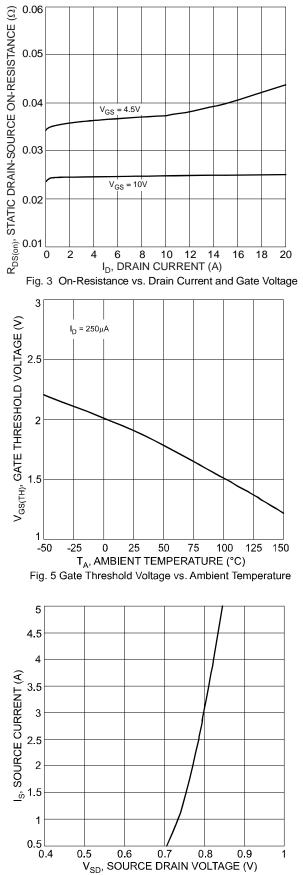
6. Guaranteed by design. Not subject to production testing.



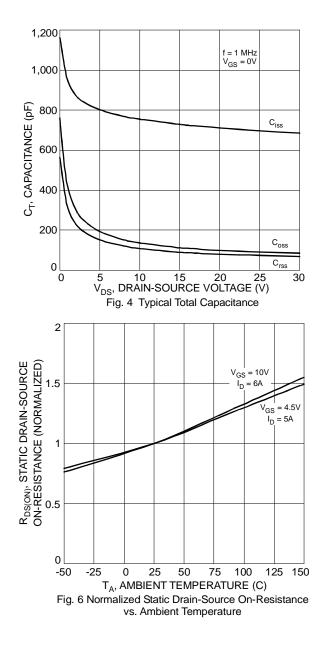












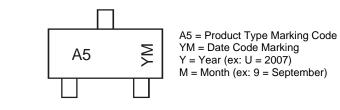


## Ordering Information (Note 7)

Part Number	Case	Packaging
DMN3033LSN-7	SC-59	3000/Tape & Reel

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

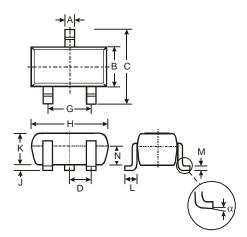
## **Marking Information**



Date	Code	Kev	

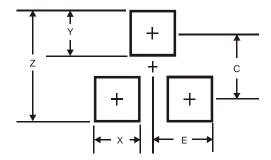
Year	2007	20	08	2009	2010	20	11	2012	2013	20	14	2015
Code	U	١	/	W	Х	``	Y	Z	А	E	3	С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

## **Package Outline Dimensions**



SC-59							
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
С	2.70	3.00	2.80				
D	-	-	0.95				
G	-	-	1.90				
н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
κ	1.00	1.30	1.10				
L 0.35		0.55	0.40				
М	0.10	0.20	0.15				
Ν	0.70	0.80	0.75				
α	0°	8°	-				
All	Dimens	ions in	mm				

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Y	1.0
С	2.4
E	1.35



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# Website :

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# Contact Us :

➤ Address :

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

- > Sales :
  - Direct +86 (21) 6401-6692
  - Email amall@ameya360.com
  - QQ 800077892
  - Skype ameyasales1 ameyasales2

# > Customer Service :

Email service@ameya360.com

# > Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com