

# MGSF2N02EL, MVSF2N02EL

## Power MOSFET

### 2.8 Amps, 20 Volts, N-Channel SOT-23

These miniature surface mount MOSFETs low  $R_{DS(on)}$  assure minimal power loss and conserve energy, making these devices ideal for use in space sensitive power management circuitry.

#### Features

- Low  $R_{DS(on)}$  Provides Higher Efficiency and Extends Battery Life
- Miniature SOT-23 Surface Mount Package Saves Board Space
- $I_{DSS}$  Specified at Elevated Temperature
- AEC Q101 Qualified and PPAP Capable – MVSF2N02EL
- These Devices are Pb-Free and are RoHS Compliant

#### Applications

- DC-DC Converters
- Power Management in Portable and Battery Powered Products, ie: Computers, Printers, PCMCIA Cards, Cellular and Cordless Telephones

#### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	20	Vdc
Gate-to-Source Voltage – Continuous	$V_{GS}$	$\pm 8.0$	Vdc
Drain Current – Continuous @ $T_A = 25^\circ\text{C}$ – Single Pulse ( $t_p = 10 \mu\text{s}$ )	$I_D$ $I_{DM}$	2.8 5.0	A
Total Power Dissipation @ $T_A = 25^\circ\text{C}$	$P_D$	1.25	W
Operating and Storage Temperature Range	$T_J, T_{stg}$	$-55$ to $150$	$^\circ\text{C}$
Thermal Resistance Junction-to-Ambient (Note 1) Thermal Resistance Junction-to-Ambient (Note 2)	$R_{\theta JA}$	100 300	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	$T_L$	260	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

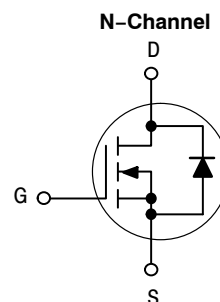
1. 1" Pad,  $t < 10$  sec.
2. Min pad, steady state.



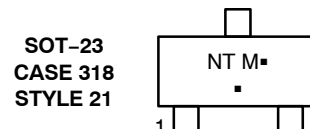
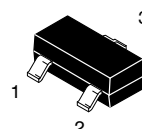
ON Semiconductor®

<http://onsemi.com>

2.8 A, 20 V  
 $R_{DS(on)} = 85 \text{ m}\Omega$  (max)

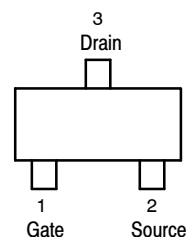


#### MARKING DIAGRAM



xxx = Specific Device Code  
M = Date Code  
■ = Pb-Free Package

#### PIN ASSIGNMENT



#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

# MGSF2N02EL, MVSF2N02EL

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage (Note 3) (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 10 μAdc) Temperature Coefficient (Positive)	V <sub>(BR)DSS</sub>	20 –	– 22	– –	Vdc mV/°C
Zero Gate Voltage Drain Current (V <sub>DS</sub> = 20 Vdc, V <sub>GS</sub> = 0 Vdc) (V <sub>DS</sub> = 20 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> = 125°C)	I <sub>DSS</sub>	– –	– –	1.0 10	μAdc
Gate-Source Leakage Current (V <sub>GS</sub> = ± 8.0 Vdc, V <sub>DS</sub> = 0 Vdc)	I <sub>GSS</sub>	–	–	± 100	nA

### ON CHARACTERISTICS (Note 3)

Gate-Source Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μAdc) Threshold Temperature Coefficient (Negative)	V <sub>GS(th)</sub>	0.5 –	– –2.3	1.0 –	Vdc mV/°C
Static Drain-to-Source On-Resistance (V <sub>GS</sub> = 4.5 Vdc, I <sub>D</sub> = 3.6 A) (V <sub>GS</sub> = 2.5 Vdc, I <sub>D</sub> = 3.1 A)	R <sub>DS(on)</sub>	– –	78 105	85 115	mΩ

### DYNAMIC CHARACTERISTICS

Input Capacitance	(V <sub>DS</sub> = 5.0 Vdc, V <sub>GS</sub> = 0 V, f = 1.0 MHz)	C <sub>iss</sub>	–	150	–	pF
Output Capacitance		C <sub>oss</sub>	–	130	–	
Transfer Capacitance		C <sub>rss</sub>	–	45	–	

### SWITCHING CHARACTERISTICS (Note 4)

Turn-On Delay Time	(V <sub>DD</sub> = 16 Vdc, I <sub>D</sub> = 2.8 Adc, V <sub>gs</sub> = 4.5 V, R <sub>G</sub> = 2.3 Ω)	t <sub>d(on)</sub>	–	6.0	–	ns
Rise Time		t <sub>r</sub>	–	95	–	
Turn-Off Delay Time		t <sub>d(off)</sub>	–	28	–	
Fall Time		t <sub>f</sub>	–	125	–	
Gate Charge	(V <sub>DS</sub> = 16 Vdc, I <sub>D</sub> = 1.75 Adc, V <sub>GS</sub> = 4.0 Vdc) (Note 3)	Q <sub>T</sub>	–	3.5	–	nC
		Q <sub>gs</sub>	–	0.6	–	
		Q <sub>gd</sub>	–	1.5	–	

### SOURCE-DRAIN DIODE CHARACTERISTICS

Forward Voltage	(I <sub>S</sub> = 1.0 Adc, V <sub>GS</sub> = 0 Vdc) (Note 3)	V <sub>SD</sub>	– –	0.76 –	1.2 –	V
Reverse Recovery Time	(I <sub>S</sub> = 1.0 Adc, V <sub>GS</sub> = 0 Vdc, dI <sub>S</sub> /dt = 100 A/μs) (Note 3)	t <sub>rr</sub>	–	104	–	ns
		t <sub>a</sub>	–	42	–	
		t <sub>b</sub>	–	62	–	
Reverse Recovery Stored Charge		Q <sub>RR</sub>	–	0.20	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

4. Switching characteristics are independent of operating junction temperature.

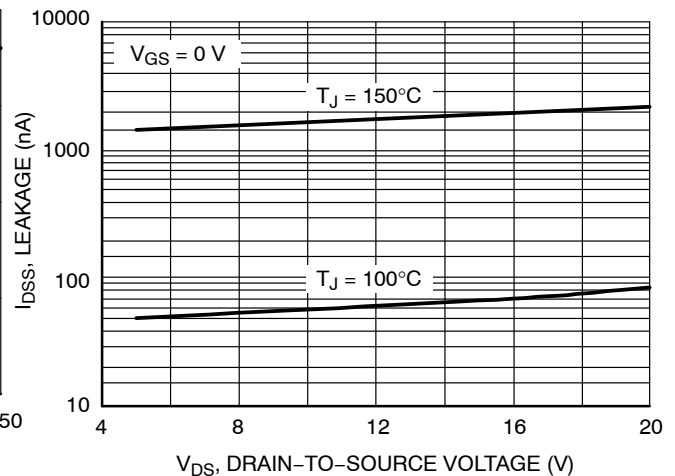
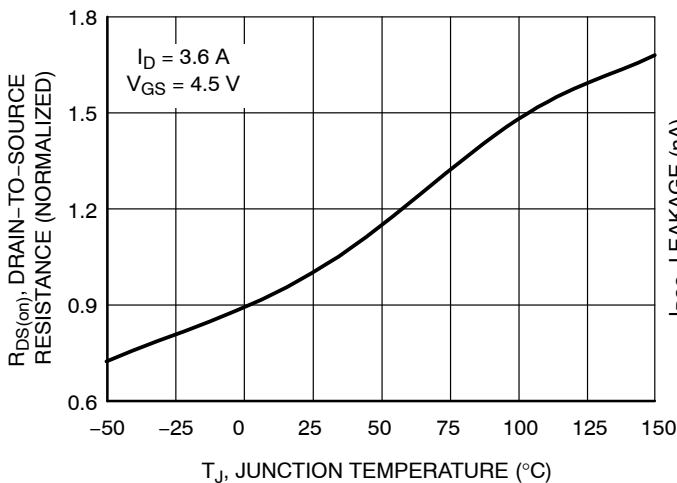
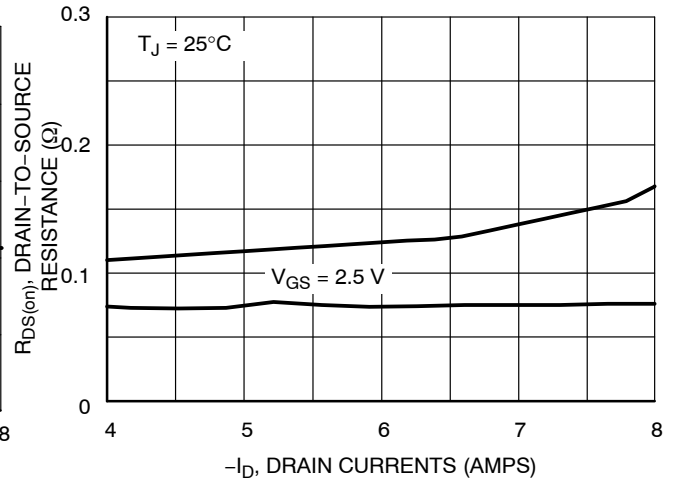
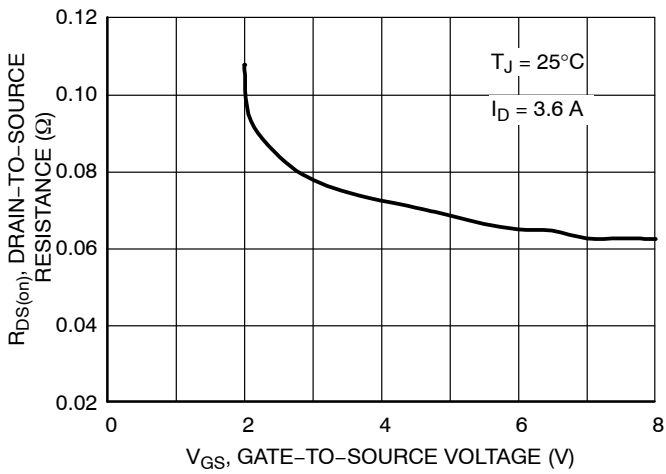
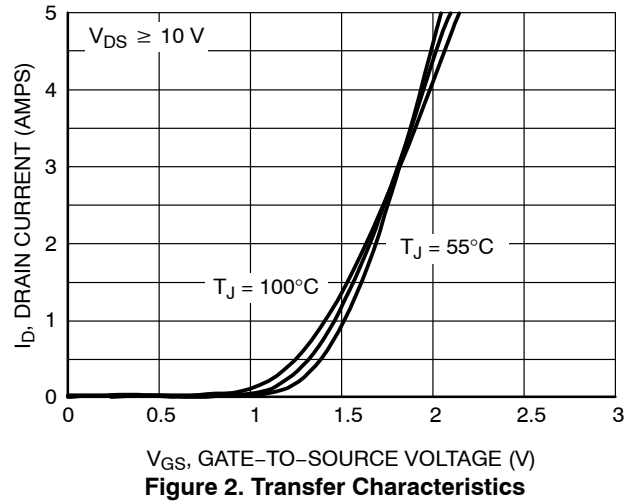
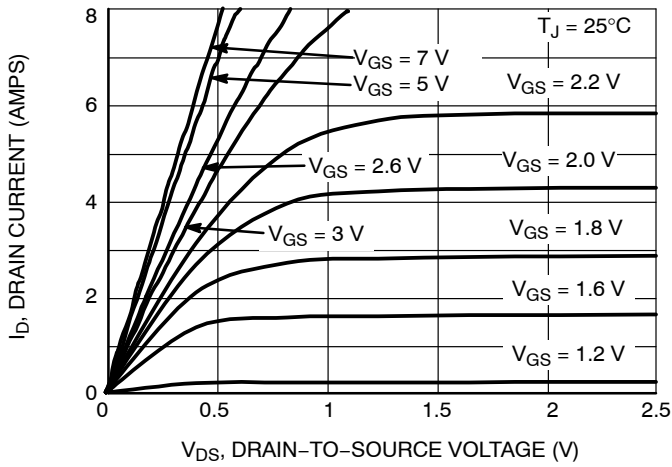
## ORDERING INFORMATION

Device	Package	Shipping†
MGSF2N02ELT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MVSF2N02ELT1G*		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*MVSF Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

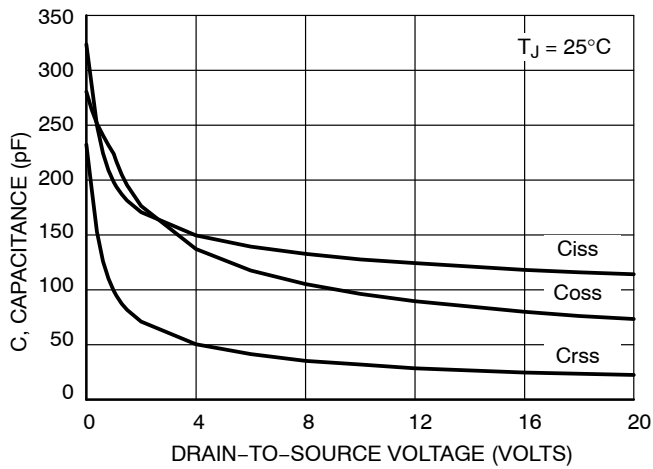


Figure 7. Capacitance Variation

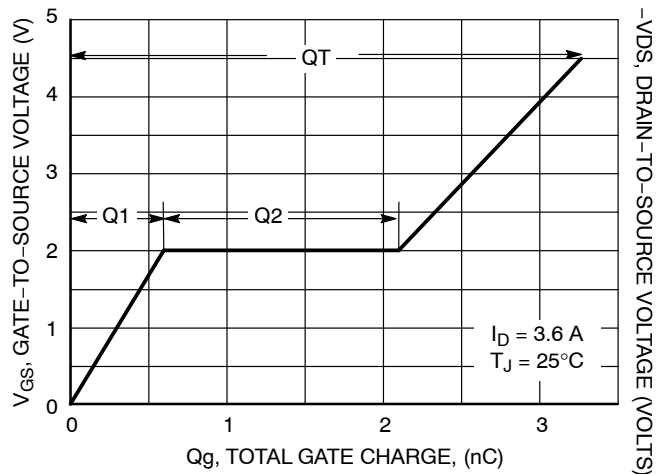


Figure 8. Gate-to-Source Voltage vs. Total Charge

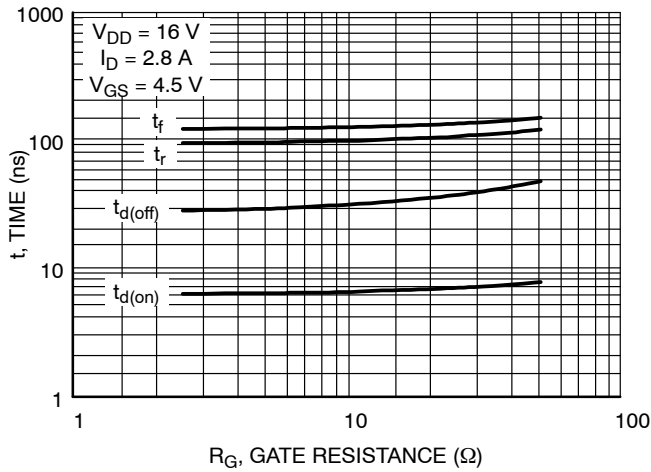


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

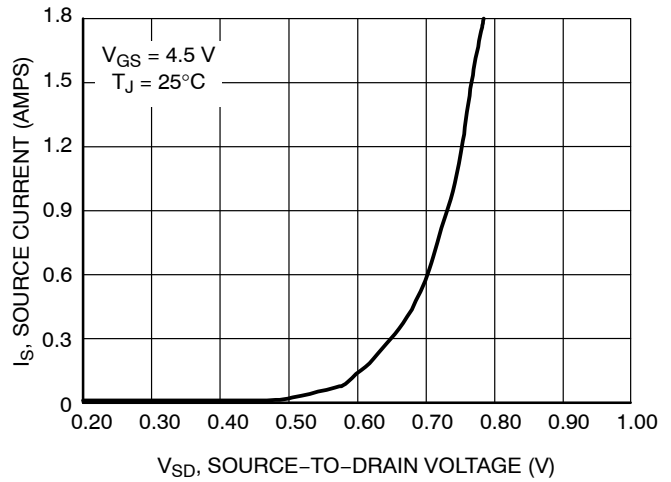


Figure 10. Diode Forward Voltage vs. Current

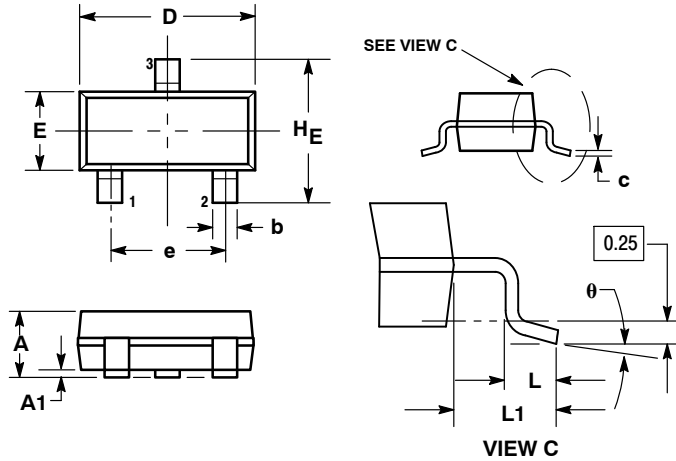
# MGSF2N02EL, MVSF2N02EL

## PACKAGE DIMENSIONS

### SOT-23 (TO-236)

CASE 318-08

ISSUE AP



#### NOTES:

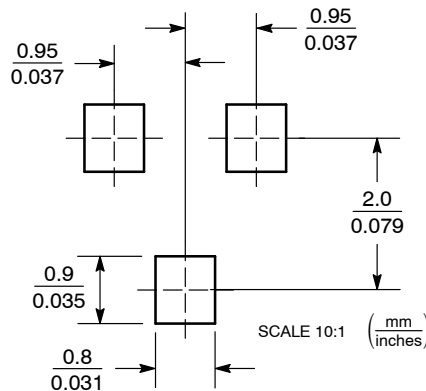
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
c	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104
θ	0°	---	10°	0°	---	10°


#### STYLE 21:

- PIN 1. GATE
- SOURCE
- DRAIN

### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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