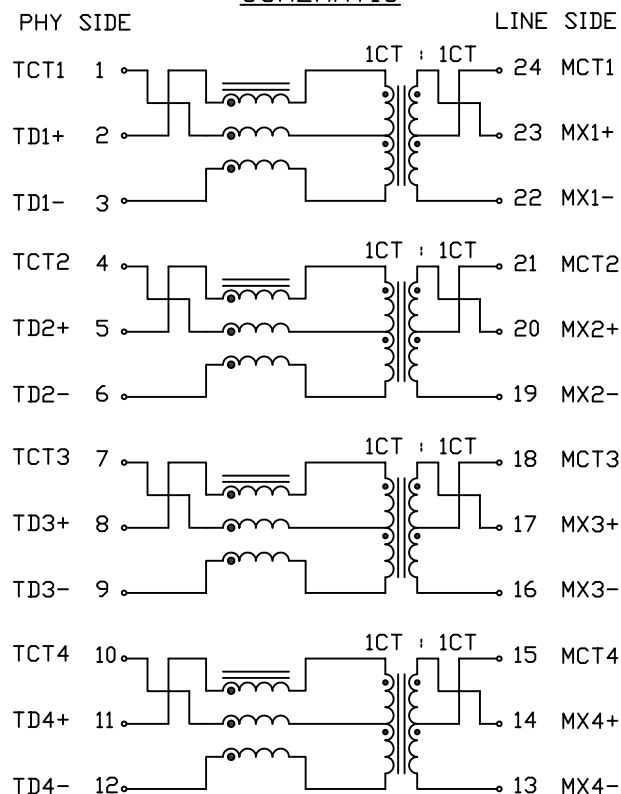


ELECTRICAL CHARACTERISTICS @25°C

RoHS



SCHEMATIC



DC RESISTANCE

(2-3), (5-6), (8-9), (11-12)	1.00 Ω M MAX
(1-3), (4-6), (7-9), (10-12)	0.75 Ω M MAX
(23-22), (20-19), (17-16), (14-13)	0.50 Ω M MAX
(24-22), (21-19), (18-16), (15-13)	0.30 Ω M MAX

URNS RATIO

(2-1-3) : (23-24-22)	1CT : 1CT $\pm 2\%$
(5-4-6) : (20-21-19)	1CT : 1CT $\pm 2\%$
(8-7-9) : (17-18-16)	1CT : 1CT $\pm 2\%$
(11-10-12) : (14-15-13)	1CT : 1CT $\pm 2\%$

POLARITY

PER DOT CONVENTION

OCL

-40°C TO +85°C
-40°C TO +85°C

100kHz, 100mV

350 μ H MIN WITH 11mA DC BIAS
120 μ H MIN WITH 19mA DC BIAS

INTERWINDING CAPACITANCE, C_{ww}

@1MHz, 20mV
30pF MAX

INSERTION LOSS

0.3MHz - 1MHz
1MHz - 65MHz
65MHz - 100MHz
100MHz - 125MHz

1.1dB MAX
0.8dB MAX
1.0dB MAX
1.2dB MAX

RETURN LOSS @100 Ω M

0.5MHz - 40MHz
40MHz - 100MHz

18dB MIN
12-20LOG(f/80MHz)dB MIN

CROSSTALK

0.3MHz - 100MHz

33-20LOG(f/100MHz)dB MIN

CM TO CM REJ

0.3MHz - 100MHz

30dB MIN

CM TO DM REJ

0.3MHz - 100MHz

35dB MIN

BALANCED DC LINE CURRENT

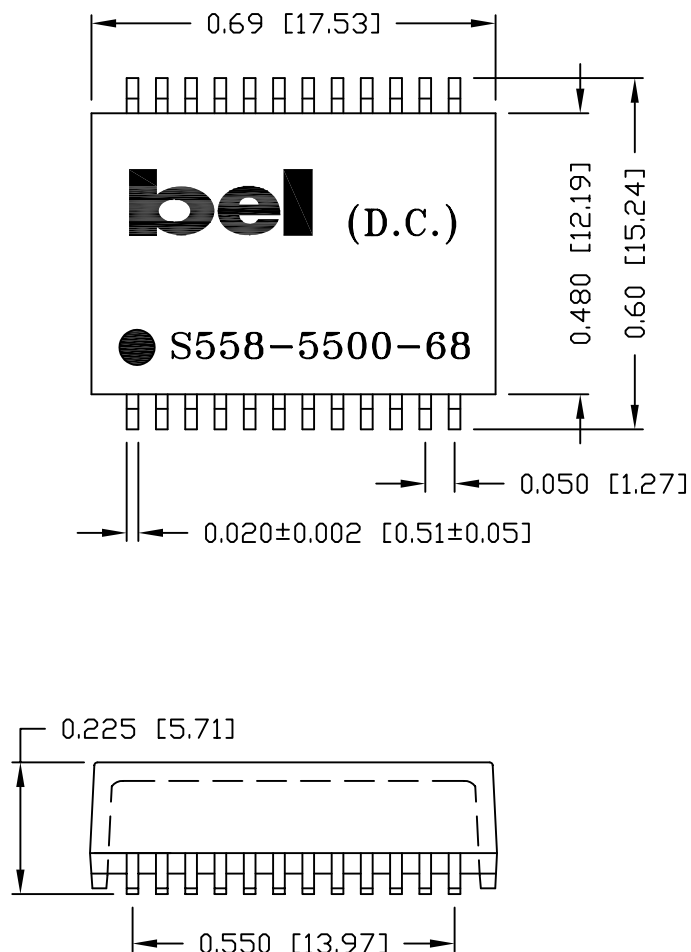
720mA MAX @57VDC CONTINUOUS
1.2A MAX @57VDC FOR 200ms

HIPOT

1500 Vrms

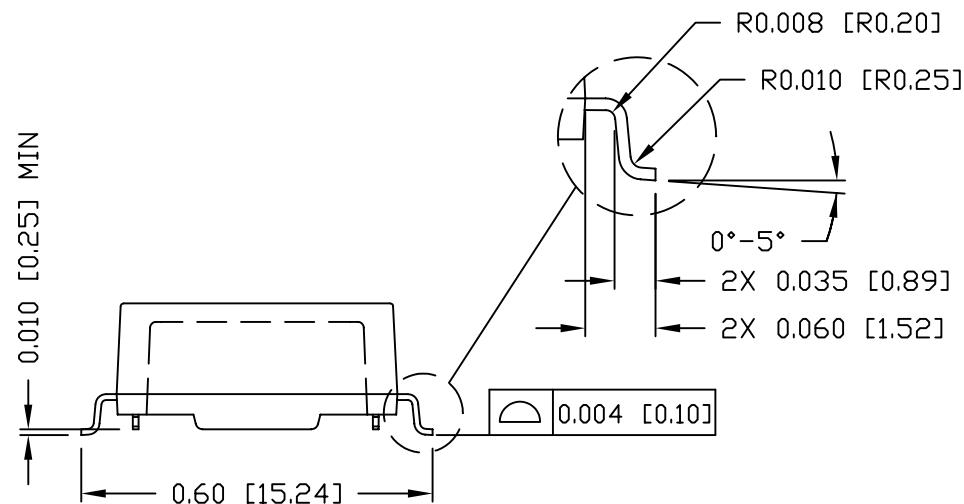
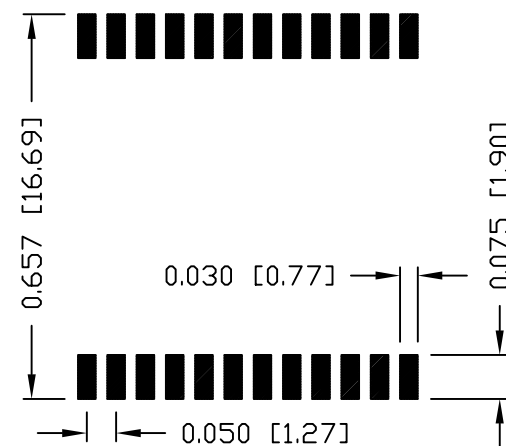
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE
CH CHAN	2012-09-06	ELECTRICAL SPECIFICATION	S558-5500-68		UNIT : INCH [mm] REV. : B
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A SIZE : A4
ZC Guo	2012-09-06		S558550068B.DWG	.XX	⊕ PAGE : 2
				.XXX	

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SUGGESTED PCB PAD LAYOUT

RoHS



NOTES:

- STANDARD MARKING REFER TO DOC. HAND-WORK-04.
- PACKAGE CODE: "RQS001".

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REFERENCE	
LAWRENCE TSANG	2012-09-06	MECHANICAL OUTLINE	S558-5500-68	TOL. IN INCH	UNIT : INCH [mm]	REV. : B
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A	SIZE : A4
ZC Guo	2012-09-06		S558550068B.DWG	.XX ±0.01		PAGE : 3
				.XXX ±0.005		



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Components Supply Platform

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