

Part Number: WP3VEYW

High Efficiency Red
Yellow

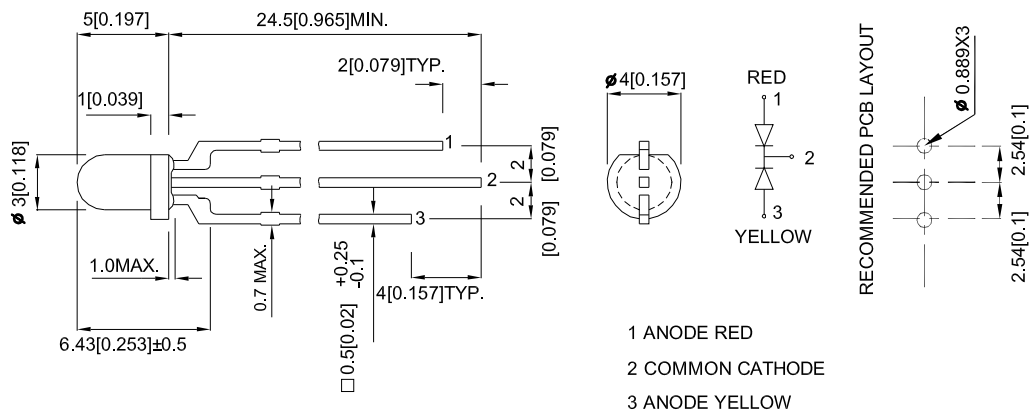
Features

- Uniform light output.
- Low power consumption.
- 3 leads with one common lead.
- Long life - solid state reliability.
- RoHS compliant.

Descriptions

- The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.
- The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode.

Package Dimensions



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is ± 0.25 (0.01") unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



Selection Guide

Part No.	Dice	Lens Type	Iv (mcd) [2] @ 20mA		Viewing Angle [1]
			Min.	Typ.	2θ1/2
WP3VEYW	High Efficiency Red (GaAsP/GaP)	White Diffused	15	40	60°
			*10	*30	
	Yellow (GaAsP/GaP)		10	15	
			*10	*15	

Notes:

1. $\theta_{1/2}$ is the angle from optical centerline where the luminous intensity is 1 / 2 of the optical peak value.

2. Luminous intensity / luminous Flux: + / -15%.

* Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Typ.	Max.	Units	Test Conditions
λ_{peak}	Peak Wavelength	High Efficiency Red Yellow	627 590		nm	I _F =20mA
λ_D [1]	Dominant Wavelength	High Efficiency Red Yellow	617 588		nm	I _F =20mA
$\Delta\lambda_{1/2}$	Spectral Line Half-width	High Efficiency Red Yellow	45 35		nm	I _F =20mA
C	Capacitance	High Efficiency Red Yellow	15 20		pF	V _F =0V;f=1MHz
V _F [2]	Forward Voltage	High Efficiency Red Yellow	2 2.1	2.5 2.5	V	I _F =20mA
I _R	Reverse Current	High Efficiency Red Yellow		10 10	uA	V _R = 5V

Notes:

1. Wavelength: + / -1nm.

2. Forward Voltage: + / -0.1V.

3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

Absolute Maximum Ratings at TA=25°C

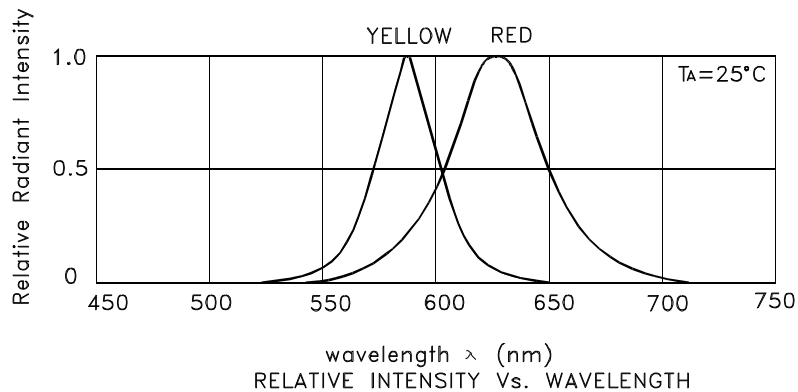
Parameter	High Efficiency Red	Yellow	Units
Power dissipation	75	75	mW
DC Forward Current	30	30	mA
Peak Forward Current [1]	160	140	mA
Reverse Voltage	5		V
Operating / Storage Temperature	-40°C To +85°C		
Lead Solder Temperature [2]	260°C For 3 Seconds		
Lead Solder Temperature [3]	260°C For 5 Seconds		

Notes:

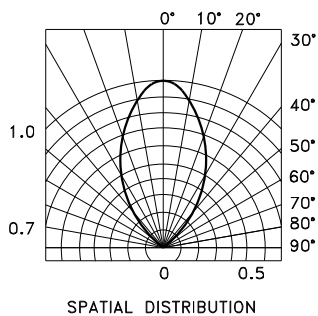
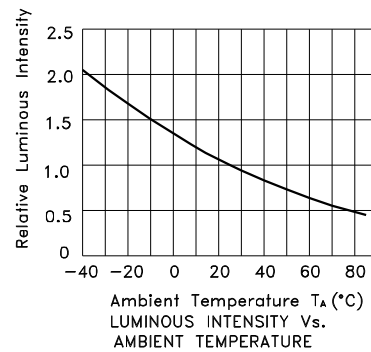
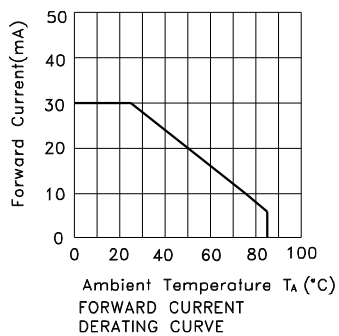
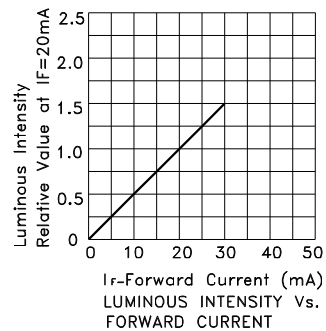
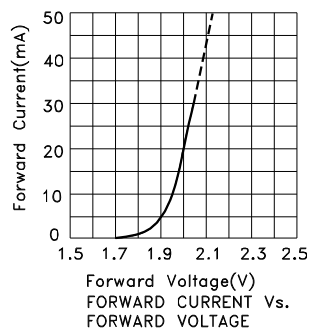
1. 1 / 10 Duty Cycle, 0.1ms Pulse Width.

2. 2mm below package base.

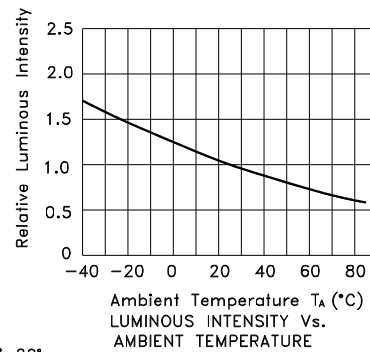
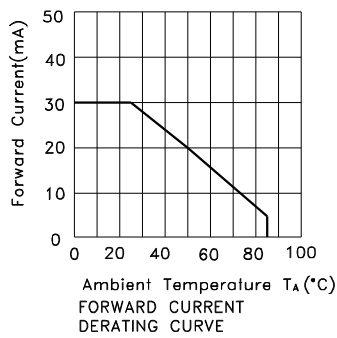
3. 5mm below package base.



WP3VEYW High Efficiency Red

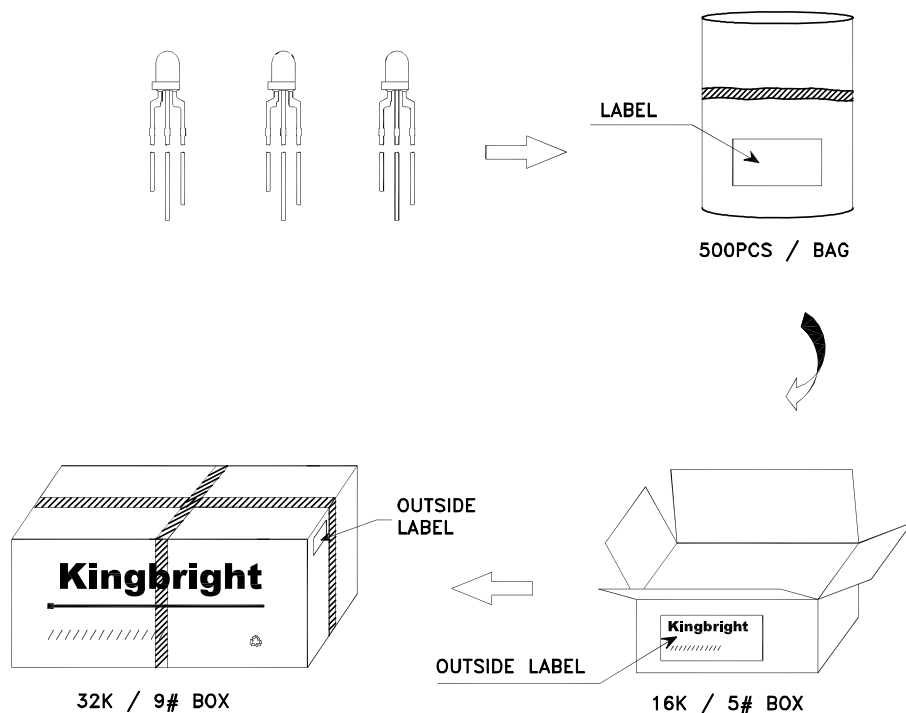


Yellow



PACKING & LABEL SPECIFICATIONS

WP3VEYW



Kingbright		
P/NO: WP3Vxxx		
QTY: 500 pcs	Q.C.	Q C
S/N: XXXX		xx xx xxxx
CODE: XXX		PASSED
LOT NO:		
RoHS Compliant		

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PRECAUTIONS

1. Storage conditions:

- Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- LEDs should be stored with temperature $\leq 30^{\circ}\text{C}$ and relative humidity $< 60\%$.
- Product in the original sealed package is recommended to be assembled within 72 hours of opening.
Product in opened package for more than a week should be baked for 30 (+10/-0) hours at $85 \sim 100^{\circ}\text{C}$.

2. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement.

Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)

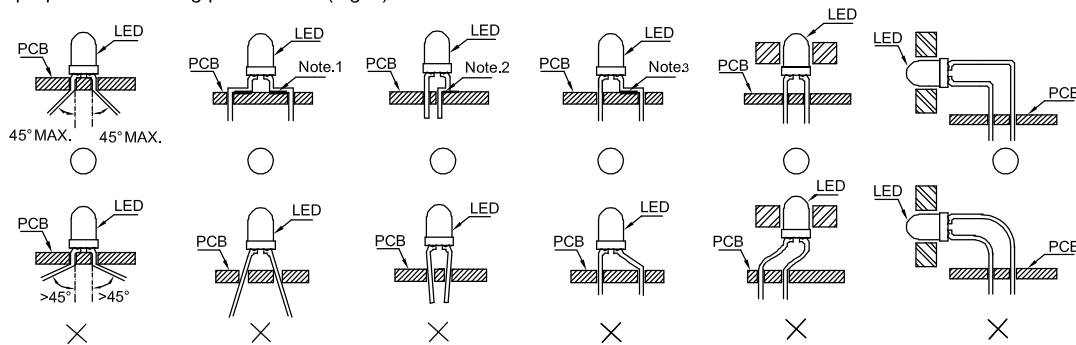


Fig.1

"○" Correct mounting method "X" Incorrect mounting method

Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

- When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure. (Fig. 2)

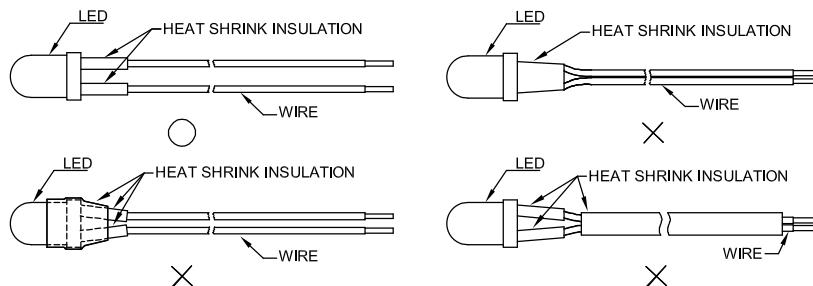


Fig. 2

- Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.

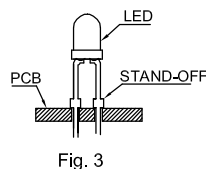


Fig. 3

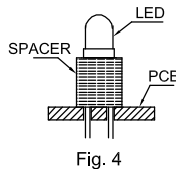


Fig. 4

- Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)

- During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

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