

COMPLIANT HALOGEN

FREE

0.65- Ω , Low Voltage, Negative Swing Capable, **Dual SPST Analog Switch**

DESCRIPTION

The DG2727, DG2728, and DG2729 are 0.6 Ω dual SPST analog switches. When Sx are used as signal input, these devices support AC-coupled audio signals with single rail power supply. Audio signals can swing below ground down to V+ - 4.3 V.

Built on Vishay Siliconix's sub-micro CMOS technology, the DG2747/2748/2749 achieve 0.6 Ω on-resistance and 0.2 Ω flatness at 2.7 V power supply. Its total harmonic distortion is 0.006 % (frequency ranges 20 Hz to 20 kHz).

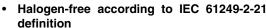
It achieves - 72 dB off-isolation and - 100 dB crosstalk at 100 kHz. Its - 3 dB bandwidth is up to 78 MHz.

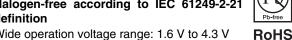
Select pin of control logic can tolerate voltage above power supply up to 4.3 V. It has guaranteed 1.2 V logic high for the power supply 2.7 V to 4.3 V range. This makes it compatible with many low voltage digital control circuits.

Combining wide operation voltage, low power, high speed, low on-resistance and small physical size, the DG2747, DG2748, DG2749 are ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG2747, DG2748, DG2749 come in a small miniQFN-8L package (1.4 mm x 1.4 mm x 0.55 mm) and operate over - 40 °C to + 85 °C extended temperature range.

FEATURES





- Wide operation voltage range: 1.6 V to 4.3 V
- Low 0.6 Ω (typical at 2.7 V) on-resistance
- Guaranteed logic high threshold: $V_{th(high)} = 1.2 \text{ V at } V_{+} = 4.3 \text{ V}$
- 82 dB crosstalk and 76 dB off-isolation at 100 kHz
- 250 MHz, 3 dB bandwidth
- 0.006 % total harmonic distortion
- > 250 mA latch up current per JESD78
- > 8 kV ESD/HBM per MIL-STD 883 (method 3015)
- Compliant to RoHS directive 2002/95/EC

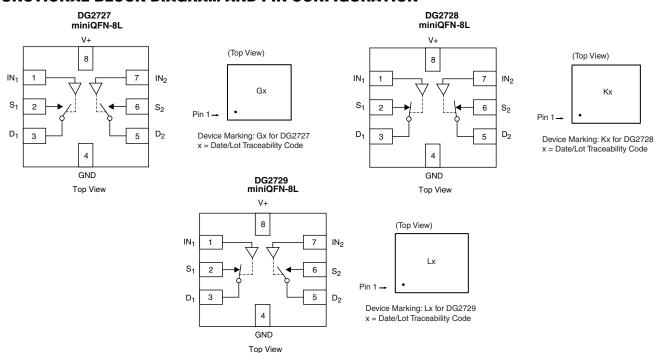
BENEFITS

- Ultra small miniQFN-8L package of 1.4 mm x 1.4 mm x 0.55 mm
- High fidelity audio switch
- Reed relay replacement
- Low power consumption

APPLICATIONS

- Cellular phones
- GPS and portable media player
- Audio and video signal routing
- Hard drives and computer peripherals
- Low voltage data-acquisition circuits
- Medical and test equipment

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



Document Number: 69049 S09-0557-Rev. A, 13-Apr-09

DG2727, DG2728, DG2729

Vishay Siliconix



TRUTH TABLE						
Logio	DG2727		DG2728		DG2729	
Logic	S ₁ and D ₁	S ₂ and D ₂	S ₁ and D ₁	D ₂ and D ₂	S ₁ and D ₁	S ₂ and D ₂
Low	OFF	OFF	ON	ON	ON	OFF
High	ON	ON	OFF	OFF	OFF	ON

ORDERING INFORMATION						
Temp. Range	Package	Part Number				
- 40 °C to 85°C	miniQFN-8L	DG2727DN-T1-E4 DG2728DN-T1-E4 DG2729DN-T1-E4				

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Limit	Unit		
Defence to OND	V+	- 0.3 to 5.0	V		
Reference to GND	IN, D, S ^a	- 0.3 to (V+ + 0.3)]		
Current (Any terminal except S or D)		30			
Continuous Current (S or D)		± 300	mA		
Peak Current (Pulsed at 1 ms, 10 % dut	y cycle)	± 500			
Storage Temperature (D Suffix)		- 65 to 150	°C		
Power Dissipation (Packages) ^b	miniQFN-8L ^c	190	mW		

Notes:

a. Signals on S or D or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC board.

c. Derate 2.4 mW/°C above 70 °C.



		Test Conditions Unless Otherwise Specified		Limits - 40 °C to 85 °C		°C	
Parameter	Symbol	$V+ = 3 \text{ V}, \pm 10 \text{ %}, V_{IN} = 0.4 \text{ V or } 1.2 \text{ V}^e$	Temp.a	Min.b	Typ.c	Max.b	Unit
Analog Switch							
Analog Signal Range ^d	V _{analog}		Full	V+ - 4.3 V		V+	V
On-Resistance	R _{ON}	$V+ = 2.7 V, I_D = 100 mA,$	Room		0.65	1.0	
On resistance	1 ON	V _S = - 1.6 V, - 1 V, 0 V, 2 V, 2.7 V	Full			1.15	
R _{ON} Match	ΔR_{ON}	$V+ = 2.7 \text{ V}, I_D = 100 \text{ mA},$ $V_S = -1.6 \text{ V}, -1 \text{ V}, 0 \text{ V}, 2 \text{ V}, 2.7 \text{ V}$	Room			0.1	Ω
R _{ON} Resistance Flatness	R _{ON} flatness	$V+ = 2.7 \text{ V}, I_D = 100 \text{ mA},$ $V_S = -1.6 \text{ V}, -1 \text{ V}, 0 \text{ V}, 2 \text{ V}, 2.7 \text{ V}$	Room		0.2	0.3	
	1		Room	- 100		100	
Switch Off Leakage	I _{S(off)}	$V+ = 2.7 V$, $V_S = -1.8 V$, 2.4 V	Full	- 500		500	
Current	la (m	$V_D = 0 V$	Room	- 100		100	nA
	I _{D(off)}		Full	- 500		500	
Channel-On Leakage	I _{D(on)}	$V+ = 2.7 \text{ V}, V_S = V_D = -1.8 \text{ V}, 2.4 \text{ V}$	Room	- 100		100	
Current	(on)ط۰	VI = 2.7 V, VS = VD = 1.0 V, 2.1 V	Full	- 500		500	
Digital Control	1			1			
High Level Input Voltage	V _{INH}	V+ = 1.6 V to 2.6 V	1	1.0			V
9 p		V+ = 2.7 V to 4.3 V		1.2			
Low Level Input Voltage		V+ = 1.6 V to 2.6 V	Full			0.3	
		V+ = 2.7 V to 4.3 V	4			0.4	
Input Current	I _{INL} or I _{INH}	V _{IN} = 0 or V+		- 1		1	μΑ
Dynamic Characteristics			Doom		20	67	
Turn-On Time	t _{ON}	V: 10V: 10VV 15V	Room Full		38	67 72	- ns
		V+ = 1.6 V to 4.3 V, V_S = 1.5 V, R_I = 50 Ω, C_I = 35 pF	Room		14	40	
Turn-Off Time		11 <u>1</u> = 30 32, 3 <u>1</u> = 30 pi	Full		14	40	
Break-Before-Make Time			i uii			42	
(DG2729 only)			Full	2	14		ns
Charge Injection ^d	Q	$C_L = 1 \text{ nF, } R_{GEN} = 0 \Omega, V_{GEN} = 0 \text{ V}$	Room		1.2		рC
Off In all all and	O _{IRR}	$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 1 MHz$			- 58		dB
Off-Isolation ^d		$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 100 kHz$	D		- 76		
Crosstalk ^d	X _{TALK}	$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 1 MHz$	Room		- 64		
Crosstalk ^u		$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 100 kHz$	1		- 82		
3 dB Bandwidth ^d		$R_L = 50 \Omega, C_L = 5 pF$	Room		252		MHz
Total Harmonic Distortion ^d	THD	$R_L = 600 \Omega$, 0.5 Vp-p, f = 20 Hz to 20 kHz	Room		0.006		%
Source Off Capacitance ^d	C _{S(off)}	$f = 1 MHz, V_S = 0 V$	Room		31		
$ \begin{array}{ccc} \text{Drain Off Capacitance}^{d} & C_{D(\text{off})} \\ \text{Drain On Capacitance}^{d} & C_{D(\text{on})} \\ \end{array} $		$f = 1 MHz, V_D = 0 V$	Room		31		pF
		$f = 1 \text{ MHz}, V_D = V_S = 0 \text{ V}$	Room		46]
Power Supply	_ (0)					<u> </u>	
Power Supply Range	V+			1.6		4.3	٧
Power Supply Current I+ $V_{IN} = 0$ or V+		$V_{INI} = 0$ or $V+$	Full			1.0	μΑ

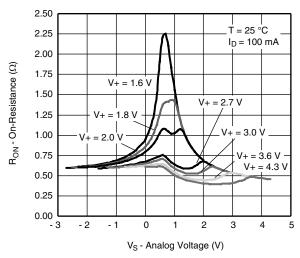
Notes:

- a. Room = 25 °C, Full = as determined by the operating suffix.
- b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- c. Typical values are for design aid only, not guaranteed nor subject to production testing.
- d. Guarantee by design, not subjected to production test.
- e. V_{IN} = input voltage to perform proper function.

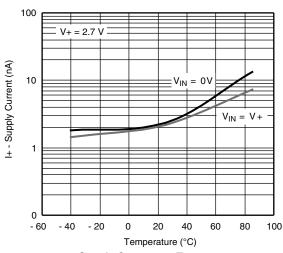
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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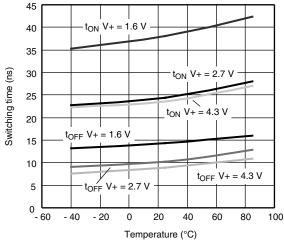
TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



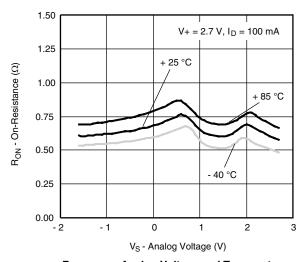
R_{DS(ON)} vs. Analog Voltage and Supply Voltage



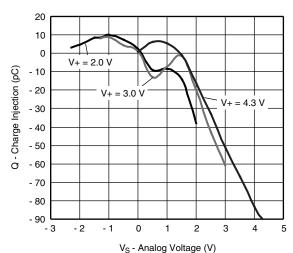
Supply Current vs. Temperature



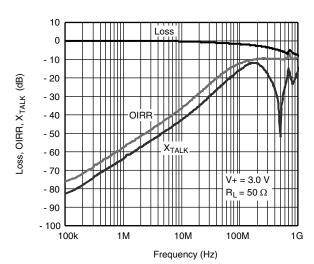
Switching Time vs. Temperature



R_{DS(ON)} vs. Analog Voltage and Temperature

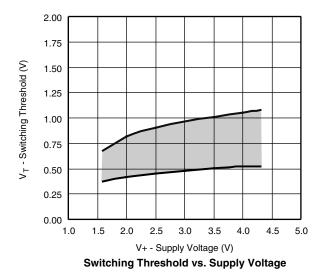


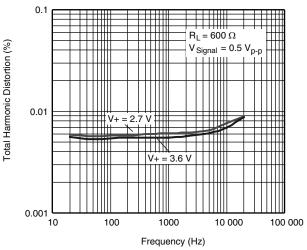
Charge Injection vs. Analog Voltage



Insertion Loss, Off-Isolation and Crosstalk vs. Frequency

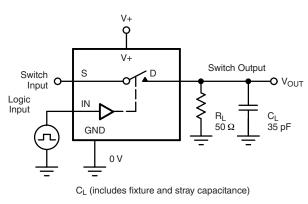
TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted





Total Harmonic Distortion vs. Frequency

TEST CIRCUITS



$$V_{OUT} = V_D \left(\frac{R_L}{R_L + R_{ON}} \right)$$

Logic Input V_{INH} $t_r < 5 \text{ ns}$ $t_f < 5 \text{ ns}$ $t_f < 5 \text{ ns}$ Switch Output t_{ON}

Logic "1" = Switch On Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1. Switching Time

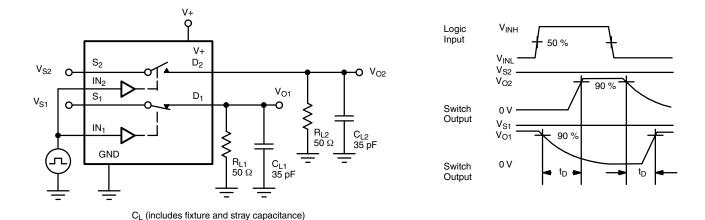
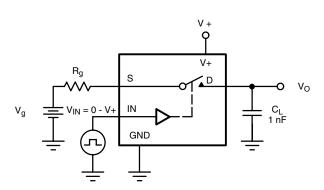


Figure 2. Break-Before-Make (DG2729)

TEST CIRCUITS





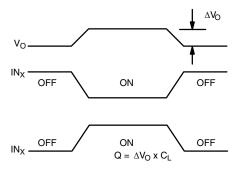
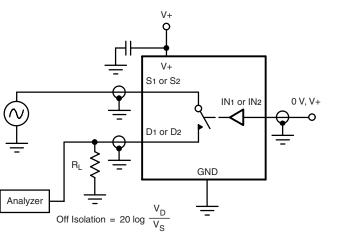


Figure 3. Charge Injection





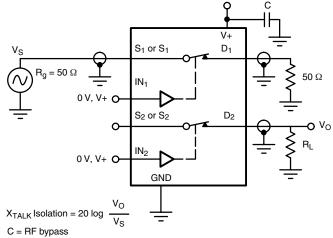


Figure 5. Crosstalk

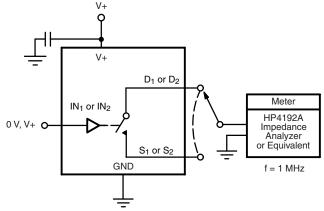
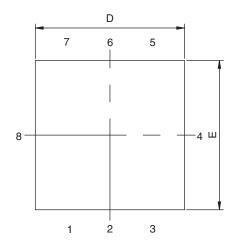


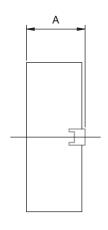
Figure 6. Channel Off/On Capacitance

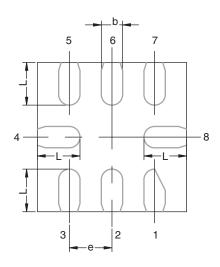
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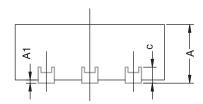


MINIQFN-8L CASE OUTLINE









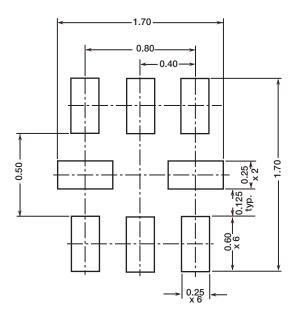
	MILLIMETERS			INCHES			
DIM	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
Α	0.50	0.55	0.60	0.0197	0.0217	0.0236	
A1	0.00	-	0.05	0.000	-	0.002	
b	0.15	0.20	0.25	0.006	0.008	0.010	
С		0.15 REF			0.006 REF		
D	1.35	1.40	1.45	0.053	0.055	0.057	
Е	1.35	1.40	1.45	0.053	0.055	0.057	
е		0.40 BSC		0.016 BSC			
L	0.35	0.40	0.45	0.014	0.016	0.018	
FON C 00000 Day A OF May 00							

ECN: C-08336-Rev. A, 05-May-08

DWG: 5964



RECOMMENDED MINIMUM PADS FOR MINI QFN 8L



Suggested Minimum Pad Dimensions in mm



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Revision: 02-Oct-12 Document Number: 91000

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