DATA SHEET



SILICON POWER MOS FET NE5520379A

3.2 V OPERATION SILICON RF POWER LDMOS FET FOR GSM/DCS DUAL-BAND PHONE TRANSMISSION AMPLIFIERS

DESCRIPTION

The NE5520379A is an N-channel silicon power MOS FET specially designed as the transmission power amplifier for 3.2 V GSM 900 handsets. Dies are manufactured using our NEWMOS technology and housed in a surface mount package. This device can deliver 34.6 dBm output power with 68% power efficiency at 915 MHz under the 2.8 V supply voltage.

FEATURES

High output power
 Pout = 35.5 dBm TYP. (Vps = 3.2 V, Ves = 2.5 V, f = 915 MHz, Pin = 25 dBm)

: $P_{out} = 33.0 \text{ dBm TYP}$. ($V_{DS} = 3.2 \text{ V}$, $V_{GS} = 2.5 \text{ V}$, f = 1.785 MHz, $P_{in} = 25 \text{ dBm}$)

• High power added efficiency : η_{add} = 65% TYP. (Vps = 3.2 V, Vgs = 2.5 V, f = 915 MHz, Pin = 25 dBm)

: $\eta_{add} = 35\%$ TYP. (VDS = 3.2 V, VGS = 2.5 V, f = 1.785 MHz, Pin = 25 dBm)

High linear gain
 G_L = 16.0 dB TYP. (V_{DS} = 3.2 V, V_{GS} = 2.5 V, f = 915 MHz, P_{in} = 10 dBm)

: GL = 8.5 dB TYP. (VDS = 3.2 V, VGS = 2.5 V, f = 1 785 MHz, Pin = 10 dBm)

• Surface mount package : $5.7 \times 5.7 \times 1.1$ mm MAX.

Single supply : VDS = 2.8 to 6.0 V

APPLICATIONS

• Digital cellular phones : 3.2 V GSM/DCS Dual-Band handsets

Others : General purpose amplifiers for 1.6 to 2.0 GHz TDMA applications

ORDERING INFORMATION

Part Number	Package	Marking	Supplying Form
NE5520379A-T1	79A	А3	12 mm wide embossed taping Gate pin face the perforation side of the tape Qty 1 kpcs/reel
NE5520379A-T1A			12 mm wide embossed tapingGate pin face the perforation side of the tapeQty 5 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: NE5520379A-A

Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

ABSOLUTE MAXIMUM RATINGS (TA = +25°C)

Unit Parameter Symbol Ratings Drain to Source Voltage V_{DS} 15.0 V ٧ Gate to Source Voltage V_{GS} 5.0 Drain Current 1.5 Α ΙD $I_{\mathsf{D}}^{\text{ Note}}$ Drain Current (Pulse Test) 3.0 Α **Total Power Dissipation** 20 W P_{tot} °С 125 Channel Temperature T_{ch} ٥С Storage Temperature T_{stg} -65 to +125

Note Duty Cycle \leq 50%, Ton \leq 1 s

RECOMMENDED OPERATING CONDITIONS

Test Conditions MIN. TYP. MAX. Unit Parameter Symbol 6.0 ٧ Drain to Source Voltage $V_{\text{DS}} \\$ 2.8 3.2 Gate to Source Voltage V_{GS} 0 2.5 3.5 ٧ Drain Current (Pulse Test) 1.75 2.0 Α Duty Cycle \leq 50%, $T_{on} \leq$ 1 s ΙD dBm Input Power P_{in} f = 1.8 GHz, Vps = 3.6 V24 25 26

Power Added Efficiency

%

35

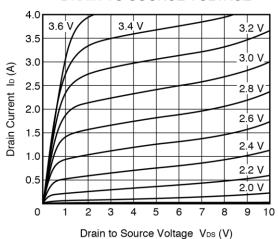
ELECTRICAL CHARACTERISTICS (TA = +25°C)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Gate to Source Leak Current	Igss	Ves = 6.0 V	=	_	100	nA
Drain to Source Leakage Current (Zero Gate Voltage Drain Current)	loss	V _{DS} = 8.5 V	-	-	100	nA
Gate Threshold Voltage	Vth	V _{DS} = 3.5 V, I _D = 1 mA	1.0	1.35	2.0	V
Transconductance	Gm	V _{DS} = 3.5 V, I _D = 0.8 to 1.0 A	-	2.5	-	S
Drain to Source Breakdown Voltage	BVDSS	loss = 10 μ A	15	20	-	V
Thermal Resistance	Rth	Channel to Case	-	_	5	°C/W
Linear Gain	G∟	f = 915 MHz, P _{in} = 10 dBm, V _{DS} = 3.2 V, V _{GS} = 2.5 V, Note	-	16.0	-	dB
Output Power	Pout	f = 915 MHz, Pin = 25 dBm,	=	35.5	=	dBm
Drain Efficiency	η d	V _{DS} = 3.2 V, V _{GS} = 2.5 V, Note	=	68	-	%
Power Added Efficiency	η add		=	65	-	%
Linear Gain	GL	f = 1 785 MHz, P _{in} = 10 dBm, V _{DS} = 3.2 V, V _{GS} = 2.5 V, Note	-	8.5	-	dB
Output Power	Pout	f = 1 785 MHz, Pin = 25 dBm,	31.0	33.0	=	dBm
Drain Efficiency	η d	V _{DS} = 3.2 V, V _{GS} = 2.5 V, Note	29	38	=	%

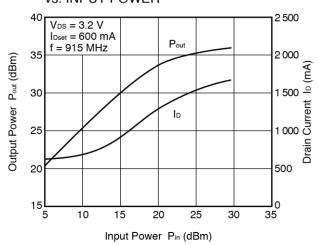
Note DC performance is 100% testing. RF performance is testing several samples per wafer. Wafer rejection criteria for standard devices is 1 reject for several samples.

TYPICAL CHARACTERISTICS (TA = +25°C)

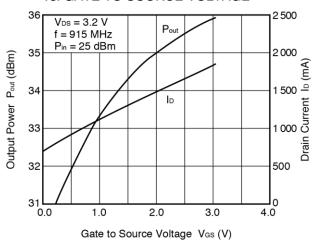




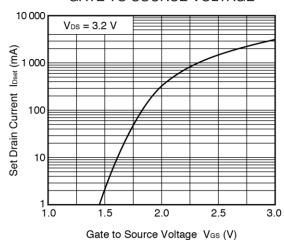
OUTPUT POWER, DRAIN CURRENT vs. INPUT POWER



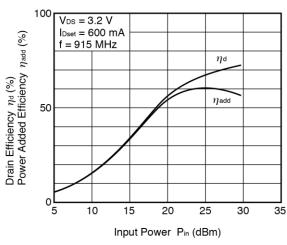
OUTPUT POWER, DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE



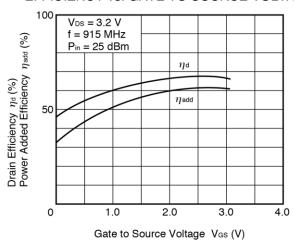
SET DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

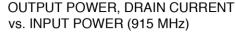


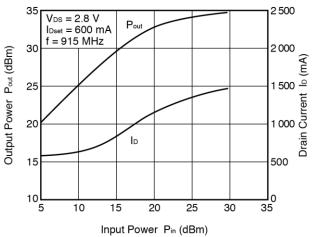
DRAIN EFFICIENCY, POWER ADDED EFFICIENCY vs. INPUT POWER



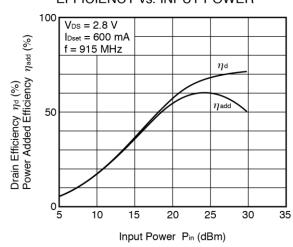
DRAIN EFFICIENCY, POWER ADDED EFFICIENCY vs. GATE TO SOURCE VOLTAGE



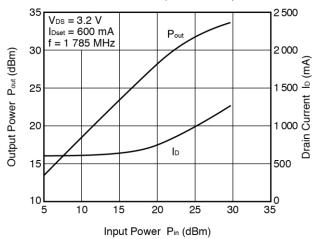




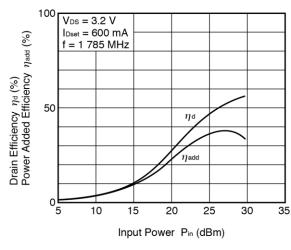
DRAIN EFFICIENCY, POWER ADDED EFFICIENCY vs. INPUT POWER



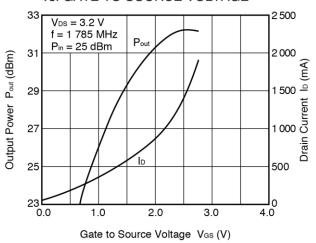
OUTPUT POWER, DRAIN CURRENT vs. INPUT POWER (1 785 MHz)



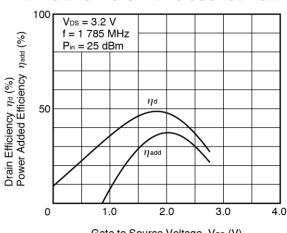
DRAIN EFFICIENCY, POWER ADDED EFFICIENCY vs. INPUT POWER



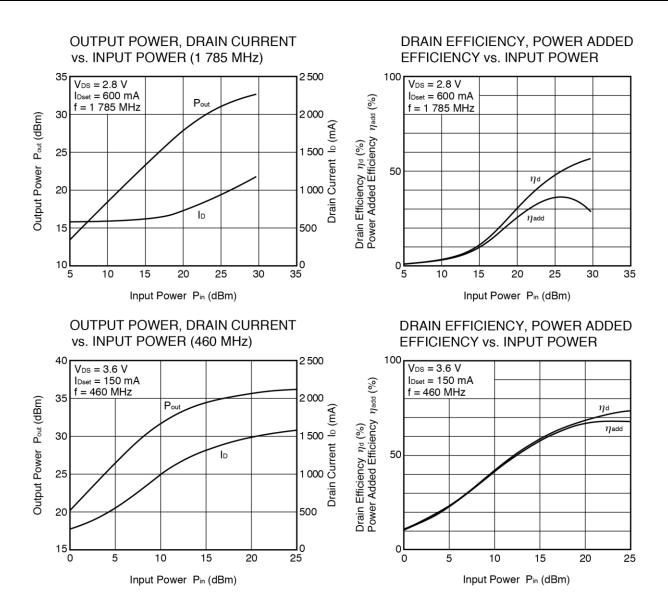
OUTPUT POWER, DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE



DRAIN EFFICIENCY, POWER ADDED EFFICIENCY vs. GATE TO SOURCE VOLTAGE



Gate to Source Voltage Vgs (V)



Remark The graphs indicate nominal characteristics.

S-PARAMETERS

- S-parameters and noise parameters are provided on our Web site in a format (S2P) that enables the direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.
- · Click here to download S-parameters.
- [RF and Microwave] ® [Device Parameters]
- URL http://www.necel.com/microwave/en/

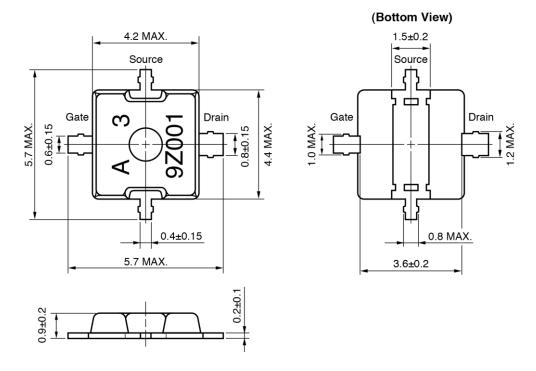
LARGE SIGNAL IMPEDANCE (VDS = 3.2 V, IDset = 600 mA, Pin = 25 dBm)

f (MHz)	$Z_{in}\left(\Omega\right)$	Z ol (Ω) Note		
1 785	TBD	TBD		

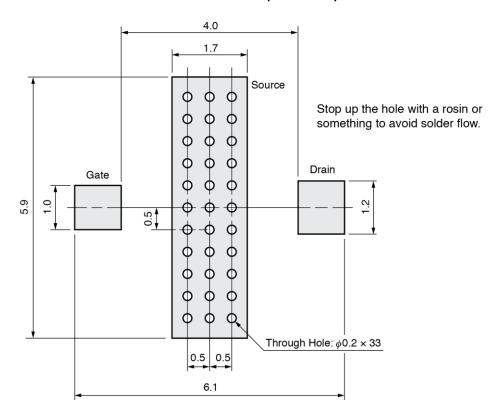
Note ZoL is the conjugate of optimum load impedance at given voltage, idling current, input power and frequency.

■ PACKAGE DIMENSIONS

79A (UNIT: mm)



79A PACKAGE RECOMMENDED P.C.B. LAYOUT (UNIT: mm)



RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260
VPS	Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below	VP215
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) Soldering time (per pin of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350-P3

Caution Do not use different soldering methods together (except for partial heating).

AMEYA360 Components Supply Platform

Authorized Distribution Brand:

























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