

SEMICONDUCTOR®

November 2013

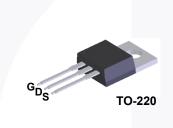
FQP33N10 N-Channel QFET[®] MOSFET 100 V, 33 A, 52 mΩ

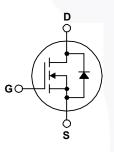
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 33 A, 100 V, $R_{DS(on)}$ = 52 m Ω (Max.) @ V_{GS} = 10 V, I_D = 16.5 A
- Low Gate Charge (Typ. 38 nC)
- Low Crss (Typ. 62 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

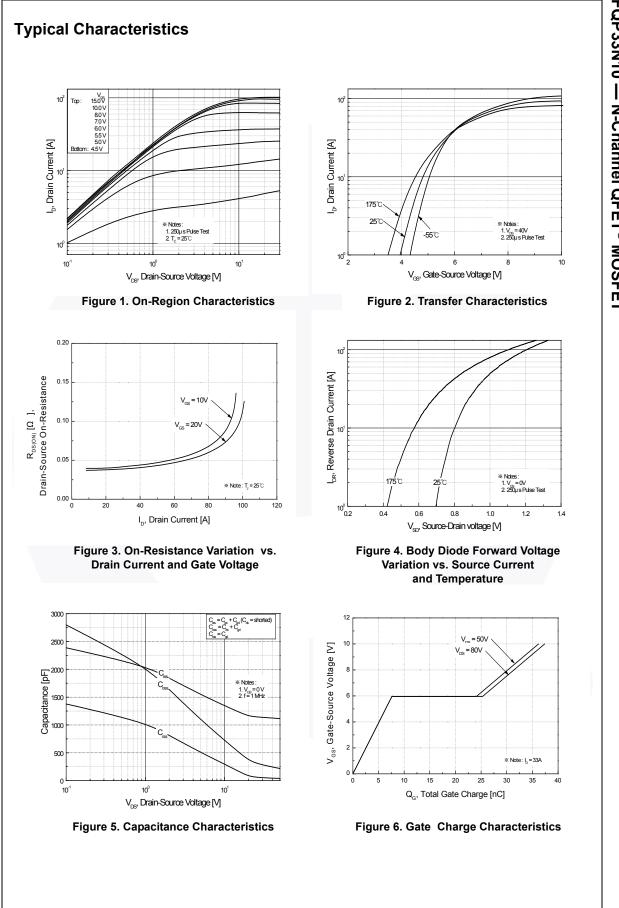
Symbol	Parameter		FQP33N10	Unit	
V _{DSS}	Drain-Source Voltage		100	V	
I _D	Drain Current - Continuous ($T_C = 25^\circ$	C)	33	A	
	- Continuous (T _C = 100	°C)	23	A	
I _{DM}	Drain Current - Pulsed	(Note 1)	132	A	
V _{GSS}	Gate-Source Voltage		± 25	V	
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	435	mJ	
I _{AR}	Avalanche Current	(Note 1)	33	A	
E _{AR}	Repetitive Avalanche Energy	anche Energy (Note 1) 12.7		mJ	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.0	V/ns	
P _D	Power Dissipation (T _C = 25°C)		127	W	
	- Derate above 25°C		0.85	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Ran	ge	-55 to +175	°C	
TL	Maximum Lead Temperature for Solderin 1/8" from Case for 5 seconds	g,	300	°C	

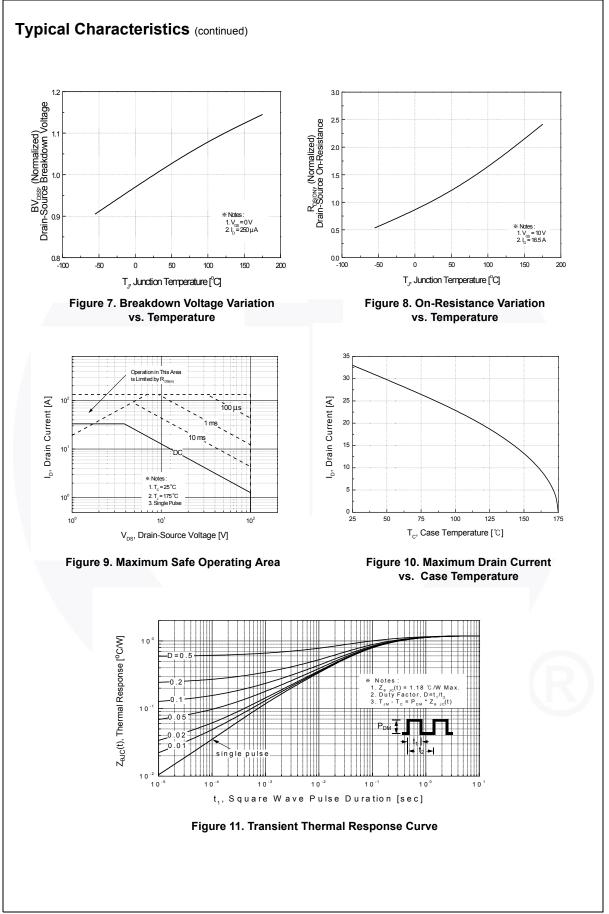
Thermal Characteristics

Symbol	Parameter	FQP33N10	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.18	°C/W	
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

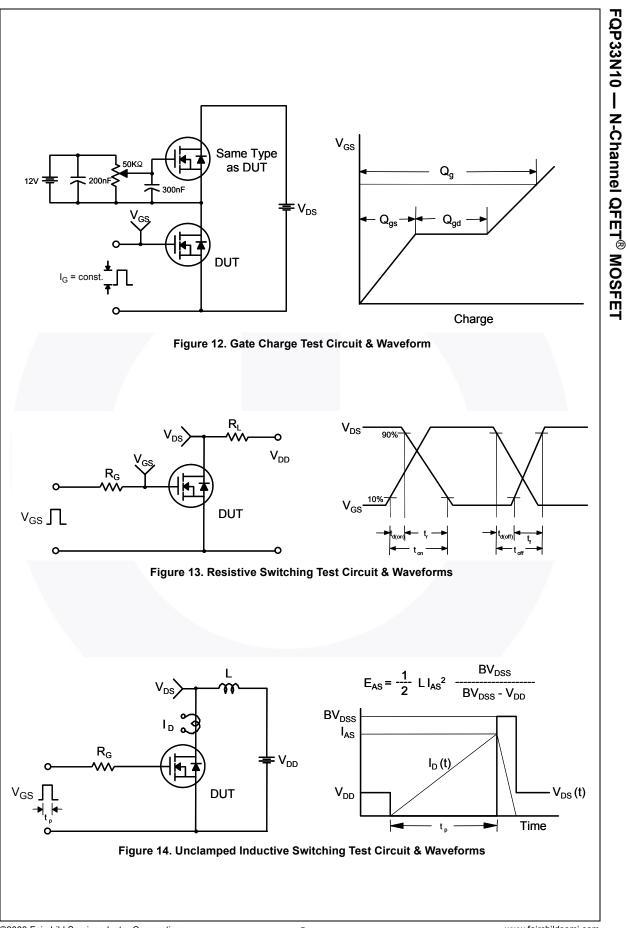
Part Number Top Mark Package		Packing Method	Reel Size	Tape Width N/A		th Q	Quantity 50 units		
FQP33	FQP33N10 FQP33N10 TO-220		Tube N/A			5			
lectri	cal C	haracteristics	T _C = 25°C	unless otherwise noted.					
Symbol		Parameter		Test Condit	ions	Min	Тур	Max	Unit
Off Cha		detiee.							
BV _{DSS}			oltage	V _{GS} = 0 V, I _D = 250 µ	IA	100			V
ABV _{DSS}		Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient		$I_D = 250 \mu\text{A}$, Referenced to 25°C		100			v
ΔT_{J}							0.11		V/°C
DSS	7	Zero Gate Voltage Drain Current		V _{DS} = 100 V, V _{GS} = 0	D V			1	μA
	Zero G			$V_{DS} = 80 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$				10	μA
GSSF	Gate-E	Body Leakage Currer	t, Forward	V_{GS} = 25 V, V_{DS} = 0	V			100	nA
GSSR	Gate-E	Body Leakage Currer	t, Reverse	V_{GS} = -25 V, V_{DS} = 0	V			-100	nA
On Cha	aracter	istics							
/ _{GS(th)}	Gate 1	Threshold Voltage		$V_{DS} = V_{GS}, I_D = 250$	μA	2.0		4.0	V
R _{DS(on)}		Drain-Source esistance		$V_{GS} = 10 \text{ V}, \text{I}_{D} = 16.5 \text{ A}$			0.040	0.052	Ω
FS	Forwa	rd Transconductance		V _{DS} = 40 V, I _D = 16.5	δA		22		S
Dynam D _{iss} Doss	Input (racteristics Capacitance t Capacitance		V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz			1150 320	1500 420	pF pF
S _{rss}		se Transfer Capacita	nce	1 - 1.0 10112			62	80	pF
133									
Switch	ing Ch	aracteristics							
d(on)	Turn-C	On Delay Time		V _{DD} = 50 V, I _D = 33 A	Α.		15	40	ns
r	Turn-C	On Rise Time		$R_{G} = 25 \Omega$			195	400	ns
d(off)	Turn-C	Off Delay Time		0			80	170	ns
	Turn-C	Off Fall Time			(Note 4)	/	110	230	ns
ζ _g	Total C	Sate Charge		V _{DS} = 80 V, I _D = 33 A, V _{GS} = 10 V			38	51	nC
ک _{gs}	Gate-S	Source Charge					7.5		nC
λ _{gd}	Gate-I	Drain Charge			(Note 4)		18		nC
Drain-S	Source	Diode Characte	eristics an	d Maximum Rati	nas				
3		ium Continuous Drair						33	Α
SM	Maxim	um Pulsed Drain-So	urce Diode Fo	orward Current				132	Α
/ _{SD}	Drain-	Source Diode Forwar	d Voltage	V _{GS} = 0 V, I _S = 33 A				1.5	V
r		se Recovery Time	Ŭ	$V_{GS} = 0 V, I_S = 33 A,$ $dI_F / dt = 100 A/\mu s$			80		ns
ς Σ ^{ur}		se Recovery Charge				-	0.22		μC
	1	, 5-					I		

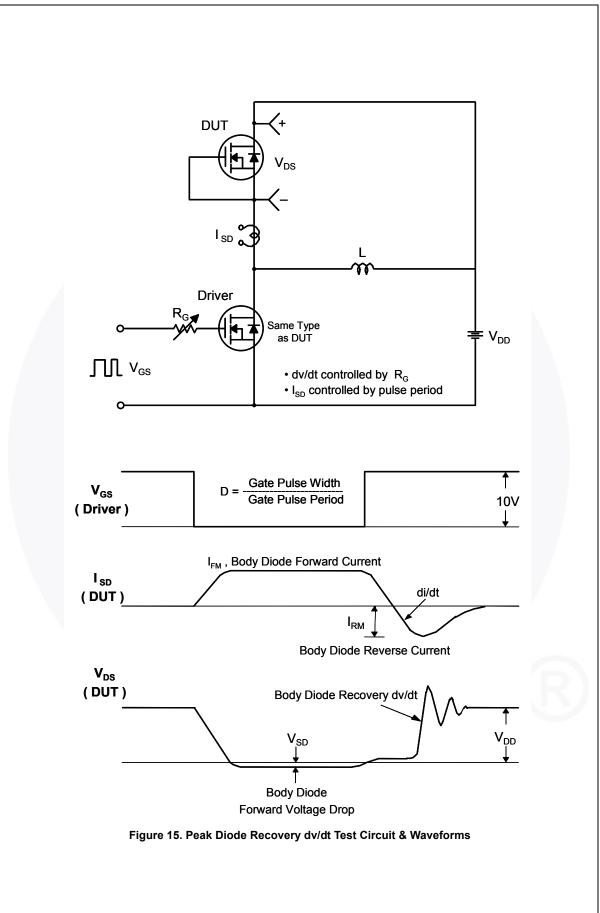
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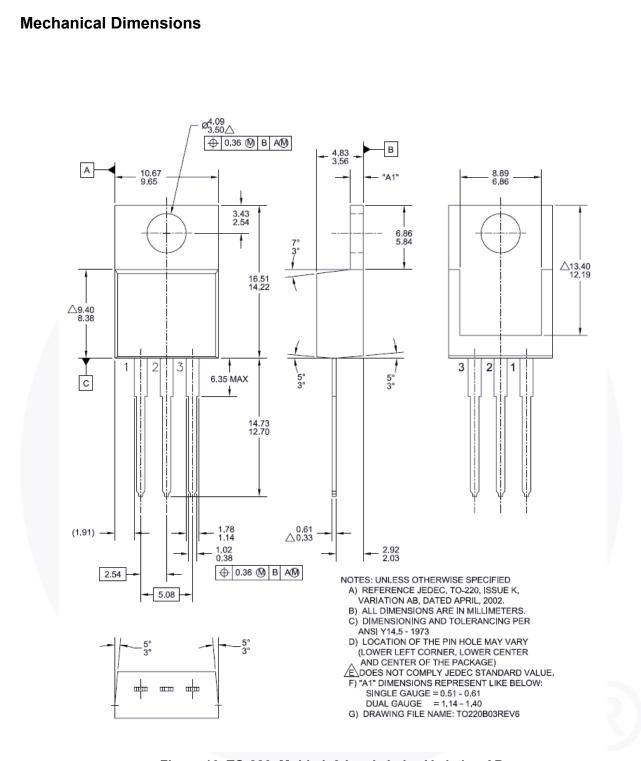


Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

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