

# PS9308L, PS9308L2

## Data Sheet

R08DS0048EJ0200

Rev.2.00

Sep 27, 2013

2.0 A OUTPUT CURRENT, HIGH CMR, IGBT GATE DRIVE, 6-PIN SDIP PHOTOCOUPLER

### DESCRIPTION

The PS9308L and PS9308L2 are optical coupled isolators containing a GaAlAs LED on the input side and a photo diode, a signal processing circuit and a power output transistor on the output side on one chip.

The PS9308L and PS9308L2 are in 6-pin plastic SDIP (Shrink Dual In-line Package). The PS9308L2 has 8 mm creepage distance. The mount area of 6-pin plastic SDIP is half size of 8-pin DIP.

The PS9308L and PS9308L2 are designed specifically for high common mode transient immunity (CMR) and high switching speed. It is suitable for driving IGBTs and MOS FETs.

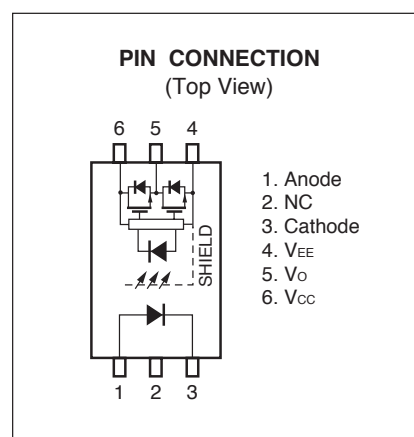
The PS9308L is lead bending type (Gull-wing) for surface mounting.

The PS9308L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

### FEATURES

- Long creepage distance (8 mm MIN.: PS9308L2)
- Half size of 8-pin DIP
- Peak output current (2.0 A MAX., 1.0 A MIN.)
- High speed switching ( $t_{PLH}$ ,  $t_{PHL}$  = 0.25  $\mu$ s MAX.)
- UVLO (Under Voltage Lock Out) protection with hysteresis
- High common mode transient immunity ( $CM_H$ ,  $CM_L$  =  $\pm 25$  kV/ $\mu$ s MIN.)
- Embossed tape product : PS9308L-E3, PS9308L2-E3 : 2 000 pcs/reel
- Pb-Free product
- Safety standards
  - UL approved: No. E72422
  - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
  - SEMKO approved (EN 60065, EN 60950)
  - DIN EN60747-5-5 (VDE0884-5) approved (Option)

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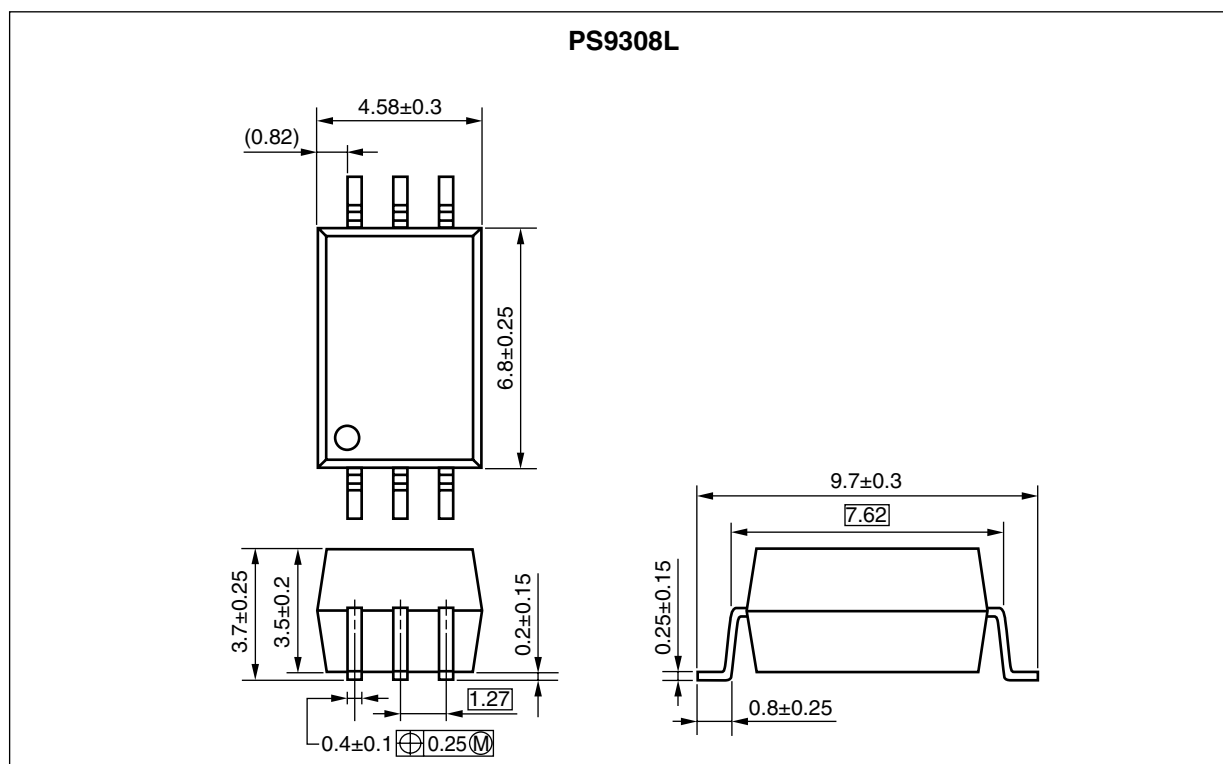
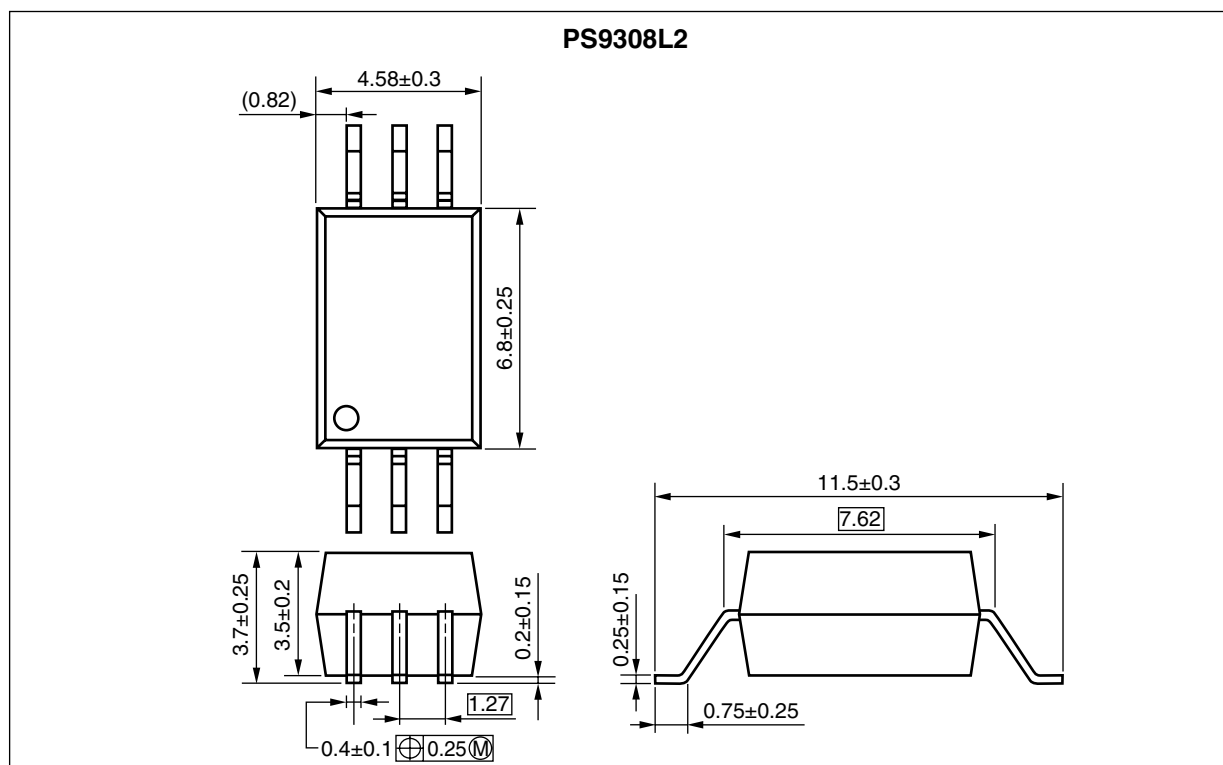


### APPLICATIONS

- IGBT, Power MOS FET Gate Driver
- Industrial inverter
- IH (Induction Heating)

The mark <R> shows major revised points.

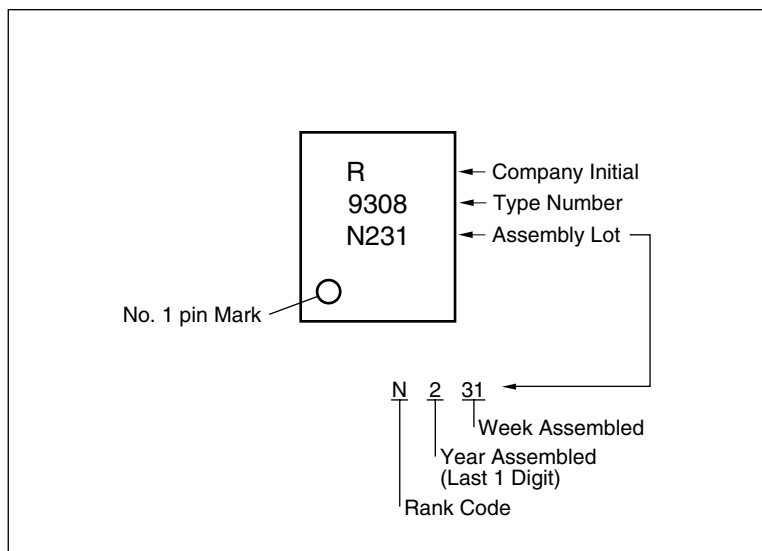
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

**PACKAGE DIMENSIONS (UNIT: mm)****Lead Bending Type (Gull-wing) For Surface Mount****Lead Bending Type (Gull-wing) For Long Creepage Distance (Surface Mount)**

**PHOTOCOUPLER CONSTRUCTION**

Parameter	PS9308L	PS9308L2
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

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**MARKING EXAMPLE**

<R> **ORDERING INFORMATION**

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number <sup>*1</sup>
PS9308L	PS9308L-AX	Pb-Free (Ni/Pd/Au)	20 pcs (Tape 20 pcs cut)	Standard products (UL, CSA, SEMKO approved)	PS9308L
PS9308L-E3	PS9308L-E3-AX		Embossed Tape 2 000 pcs/reel		
PS9308L2	PS9308L2-AX		20 pcs (Tape 20 pcs cut)	SEMKO approved)	PS9308L2
PS9308L2-E3	PS9308L2-E3-AX		Embossed Tape 2 000 pcs/reel		
PS9308L-V	PS9308L-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN 60747-5-5 (VDE 0884-5) approved (Option)	PS9308L
PS9308L-V-E3	PS9308L-V-E3-AX		Embossed Tape 2 000 pcs/reel		
PS9308L2-V	PS9308L2-V-AX		20 pcs (Tape 20 pcs cut)		PS9308L2
PS9308L2-V-E3	PS9308L2-V-E3-AX		Embossed Tape 2 000 pcs/reel		

Note: \*1. For the application of the Safety Standard, following part number should be used.

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ , unless otherwise specified)**

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current	$I_F$	25	mA
	Peak Transient Forward Current (Pulse Width $< 1 \mu\text{s}$ )	$I_{F(\text{TRAN})}$	1.0	A
	Reverse Voltage	$V_R$	5	V
Detector	High Level Peak Output Current *1	$I_{OH(\text{PEAK})}$	2.0	A
	Low Level Peak Output Current *1	$I_{OL(\text{PEAK})}$	2.0	A
	Supply Voltage	$(V_{CC}-V_{EE})$	0 to 35	V
	Output Voltage	$V_O$	0 to $V_{CC}$	V
	Power Dissipation *2	$P_C$	250	mW
Isolation Voltage *3		BV	5 000	Vr.m.s.
Total Power Dissipation *4		$P_T$	300	mW
Operating Frequency *5		f	50	kHz
Operating Ambient Temperature		$T_A$	-40 to +110	$^\circ\text{C}$
Storage Temperature		$T_{\text{stg}}$	-55 to +125	$^\circ\text{C}$

Notes: \*1. Maximum pulse width = 10  $\mu\text{s}$ , Maximum duty cycle = 0.2%\*2. Reduced to 4.8 mW/ $^\circ\text{C}$  at  $T_A = 70^\circ\text{C}$  or more.\*3. AC voltage for 1 minute at  $T_A = 25^\circ\text{C}$ , RH = 60% between input and output.

Pins 1-3 shorted together, 4-6 shorted together.

\*4. Reduced to 5.4 mW/ $^\circ\text{C}$  at  $T_A = 70^\circ\text{C}$  or more.\*5.  $I_{OH(\text{PEAK})} \leq 2.0 \text{ A}$  ( $\leq 0.3 \mu\text{s}$ ),  $I_{OL(\text{PEAK})} \leq 2.0 \text{ A}$  ( $\leq 0.3 \mu\text{s}$ )**RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	$(V_{CC}-V_{EE})$	15		30	V
Forward Current (ON)	$I_{F(\text{ON})}$	7	10	16	mA
Forward Voltage (OFF)	$V_{F(\text{OFF})}$	-2		0.8	V
Operating Ambient Temperature	$T_A$	-40		110	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS (at RECOMMENDED OPERATING CONDITIONS,  
V<sub>EE</sub> = GND, unless otherwise specified)**

Parameter		Symbol	Conditions	MIN.	TYP.* <sup>1</sup>	MAX.	Unit
Diode	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA, T <sub>A</sub> = 25°C	1.2	1.56	1.8	V
	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 3 V, T <sub>A</sub> = 25°C			10	μA
	Input Capacitance	C <sub>IN</sub>	f = 1 MHz, V <sub>F</sub> = 0 V, T <sub>A</sub> = 25°C		30		pF
Detector	High Level Output Current	I <sub>OH</sub>	V <sub>O</sub> = (V <sub>CC</sub> - 4 V) <sup>*2</sup>	1.0	1.5		A
			V <sub>O</sub> = (V <sub>CC</sub> - 15 V) <sup>*3</sup>	1.5			
	Low Level Output Current	I <sub>OL</sub>	V <sub>O</sub> = (V <sub>EE</sub> + 2.5 V) <sup>*2</sup>	1.0	1.5		A
			V <sub>O</sub> = (V <sub>EE</sub> + 15 V) <sup>*3</sup>	1.5			
	High Level Output Voltage	V <sub>OH</sub>	I <sub>O</sub> = -100 mA <sup>*4</sup>	V <sub>CC</sub> - 3.0	V <sub>CC</sub> - 1.3		V
	Low Level Output Voltage	V <sub>OL</sub>	I <sub>O</sub> = 100 mA		0.1	0.5	V
	High Level Supply Current	I <sub>CCH</sub>	I <sub>F</sub> = 10 mA, V <sub>O</sub> = open		1.2	2.0	mA
	Low Level Supply Current	I <sub>CCL</sub>	V <sub>F</sub> = 0 to 0.8 V, V <sub>O</sub> = open		1.2	2.0	mA
	UVLO Threshold	V <sub>UVLO+</sub>	V <sub>O</sub> > 5V, I <sub>F</sub> = 10 mA	10.8	12.3	13.4	V
		V <sub>UVLO-</sub>		9.5	11.0	12.5	
	UVLO Hysteresis	UVLO <sub>HYS</sub>		0.4	1.3		
Coupled	Threshold Input Current (L → H)	I <sub>FLH</sub>	I <sub>O</sub> = 0 mA, V <sub>O</sub> > 5 V		1.8	5.0	mA
	Threshold Input Voltage (H → L)	V <sub>FHL</sub>	I <sub>O</sub> = 0 mA, V <sub>O</sub> < 5 V	0.8			V

Notes: \*1. Typical values at T<sub>A</sub> = 25°C.

\*2. Maximum pulse width = 50 μs, Maximum duty cycle = 0.5%.

\*3. Maximum pulse width = 10 μs, Maximum duty cycle = 0.2%.

\*4. V<sub>OH</sub> is measured with the DC load current in this testing. (Maximum pulse width = 2 ms, Maximum duty cycle = 20%)

<R> **SWITCHING CHARACTERISTICS (at RECOMMENDED OPERATING CONDITIONS,  $V_{EE} = GND$ , unless otherwise specified)**

Parameter	Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Propagation Delay Time (L → H)	$t_{PLH}$	$R_g = 10\ \Omega$ , $C_g = 10\ nF$ , $f = 10\ kHz$ , Duty Cycle = 50%, $I_F = 10\ mA$		0.08	0.25	$\mu s$
Propagation Delay Time (H → L)	$t_{PHL}$			0.10	0.25	$\mu s$
Pulse Width Distortion (PWD)	$ t_{PHL} - t_{PLH} $			0.02	0.1	$\mu s$
Propagation Delay Time (Difference Between Any Two Products)	$t_{PHL} - t_{PLH}$		-0.1		0.1	$\mu s$
Rise Time	$t_r$			50		ns
Fall Time	$t_f$			50		ns
Common Mode Transient Immunity at High Level Output	$ CM_H $	$T_A = 25^\circ C$ , $I_F = 10\ mA$ , $V_{CC} = 30\ V$ , $V_{CM} = 1.5\ kV$ , $V_{O(MIN.)} = 26\ V$	25			$kV/\mu s$
Common Mode Transient Immunity at Low Level Output	$ CM_L $	$T_A = 25^\circ C$ , $I_F = 0\ mA$ , $V_{CC} = 30\ V$ , $V_{CM} = 1.5\ kV$ , $V_{O(MAX.)} = 1\ V$	25			$kV/\mu s$

Note: \*1. Typical values at  $T_A = 25^\circ C$ .

## TEST CIRCUIT

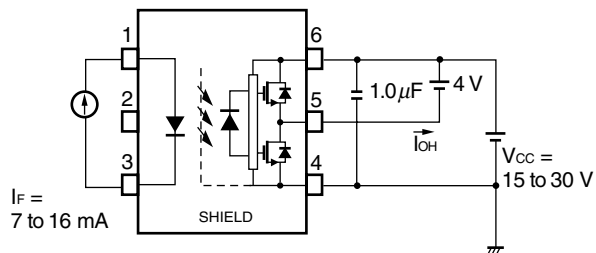
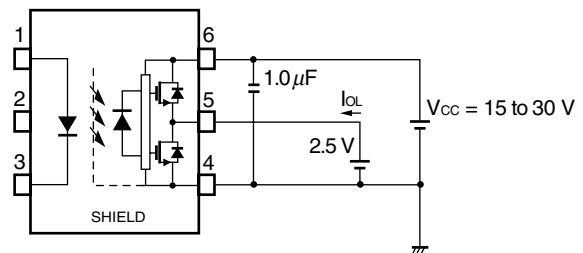
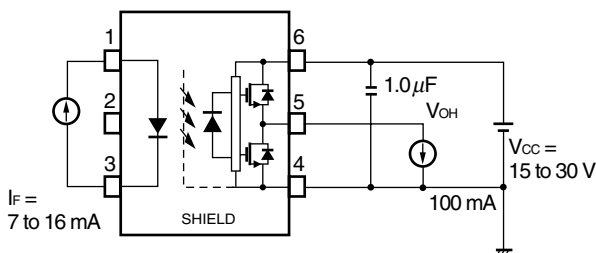
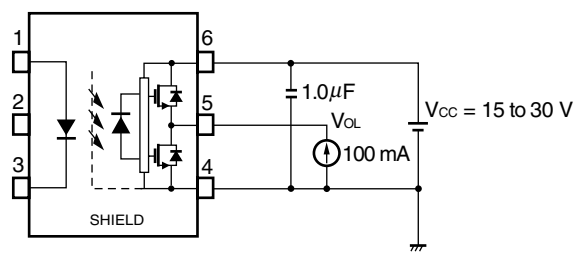
Fig. 1  $I_{OH}$  Test CircuitFig. 2  $I_{OL}$  Test CircuitFig. 3  $V_{OH}$  Test CircuitFig. 4  $V_{OL}$  Test Circuit

Fig. 5 UVLO Test Circuit

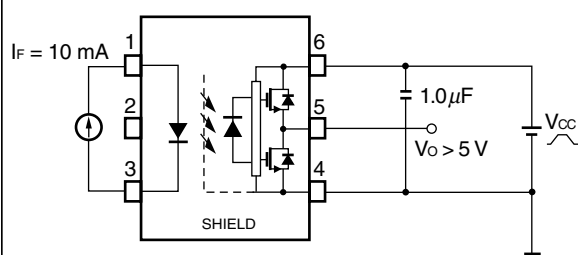
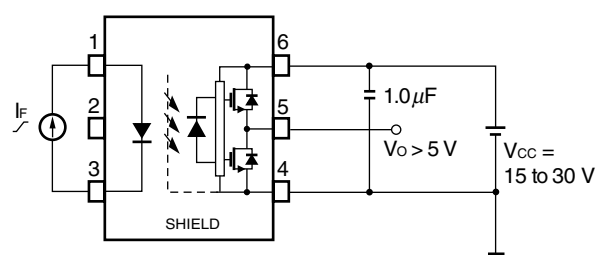
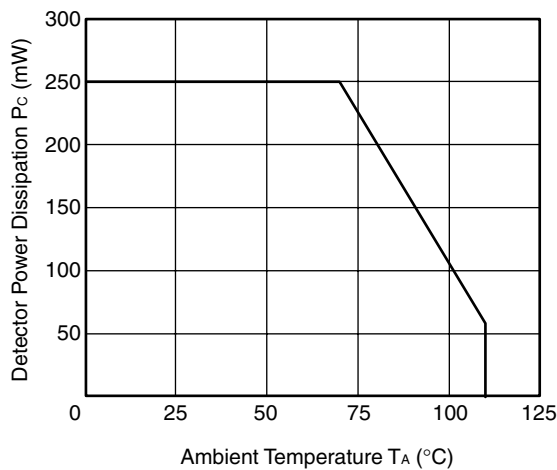
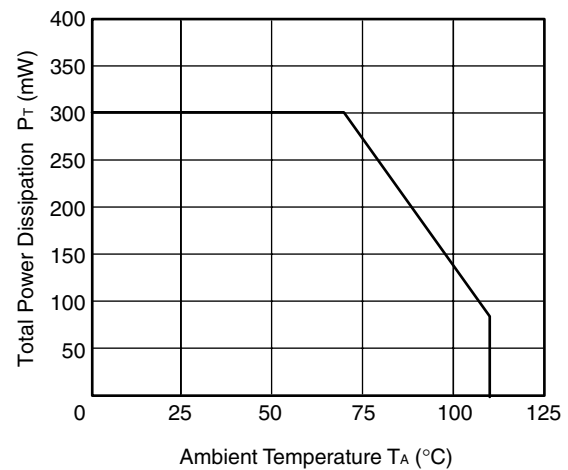
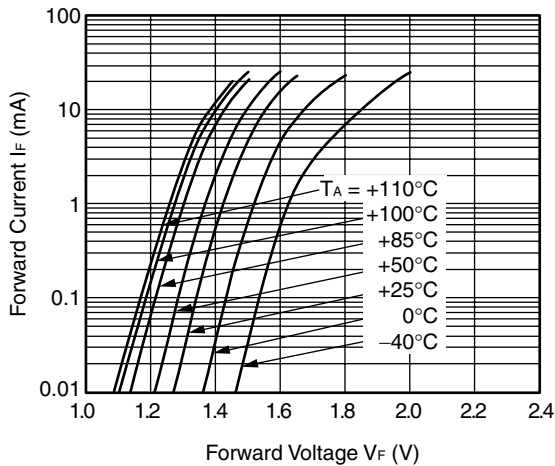
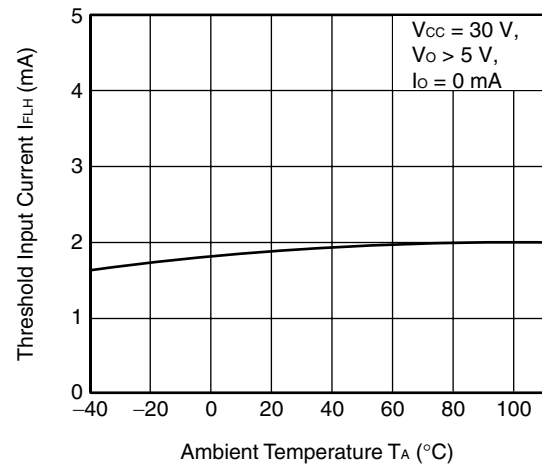
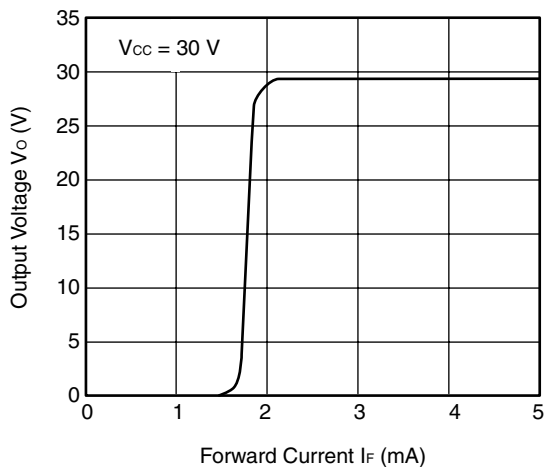
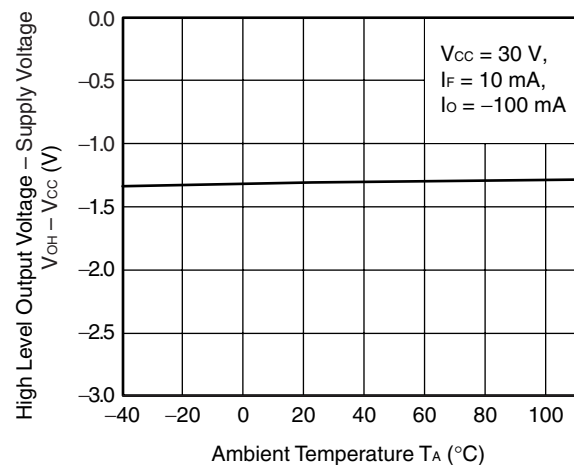
Fig. 6  $I_{FLH}$  Test Circuit

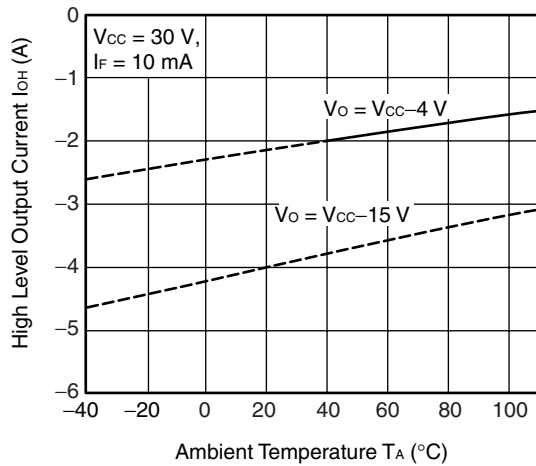


Figure 1 consists of a schematic diagram and two sets of waveforms. The schematic diagram shows a MOSFET with a shield, a 1.0 μF capacitor, and a 30 V supply. The gate is driven by a pulse with  $V_{CM} = 1.5$  kV. The drain is connected to a load and a 30 V supply. The output voltage  $V_O$  is shown for two cases: Switch A ( $I_F = 10$  mA) and Switch B ( $I_F = 0$  mA). The waveforms show  $V_{CM}$ ,  $V_O$ , and the derivative  $\frac{dV}{dt}$ .

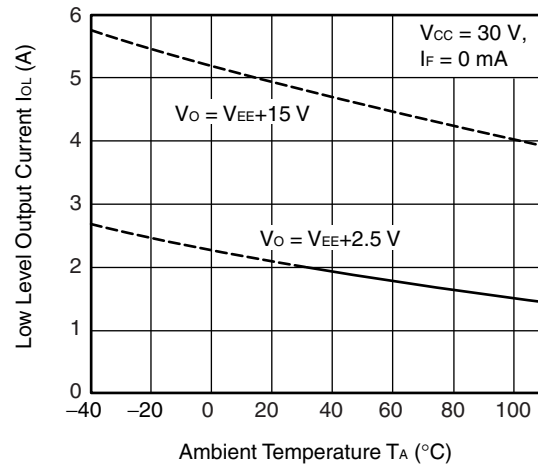
**<R> TYPICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ , unless otherwise specified)****DETECTOR POWER DISSIPATION  
vs. AMBIENT TEMPERATURE****TOTAL POWER DISSIPATION  
vs. AMBIENT TEMPERATURE****FORWARD CURRENT vs.  
FORWARD VOLTAGE****THRESHOLD INPUT CURRENT vs.  
AMBIENT TEMPERATURE****OUTPUT VOLTAGE vs.  
FORWARD CURRENT****HIGH LEVEL OUTPUT VOLTAGE – SUPPLY  
VOLTAGE vs. AMBIENT TEMPERATURE**

**Remark** The graphs indicate nominal characteristics.

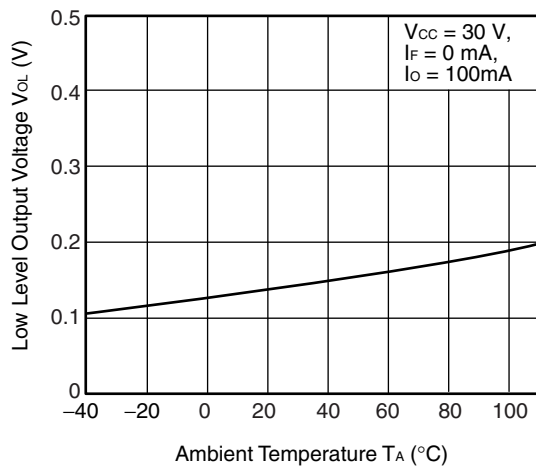
HIGH LEVEL OUTPUT CURRENT vs. AMBIENT TEMPERATURE



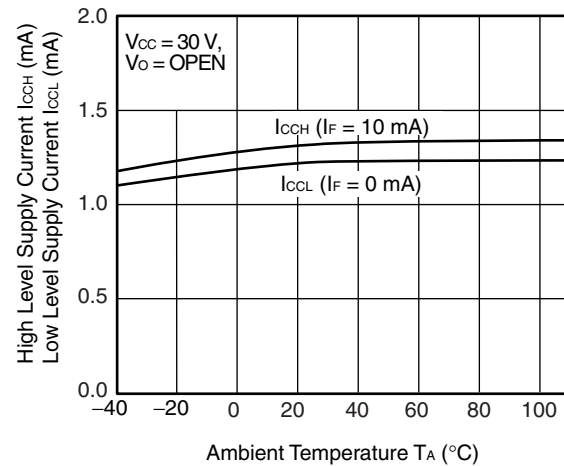
LOW LEVEL OUTPUT CURRENT vs. AMBIENT TEMPERATURE



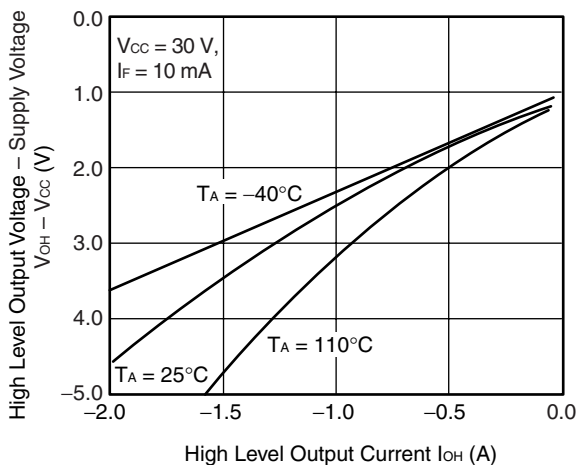
LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE



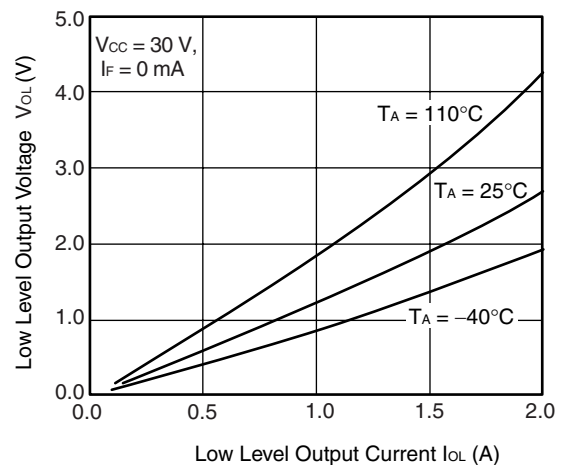
SUPPLY CURRENT vs. AMBIENT TEMPERATURE



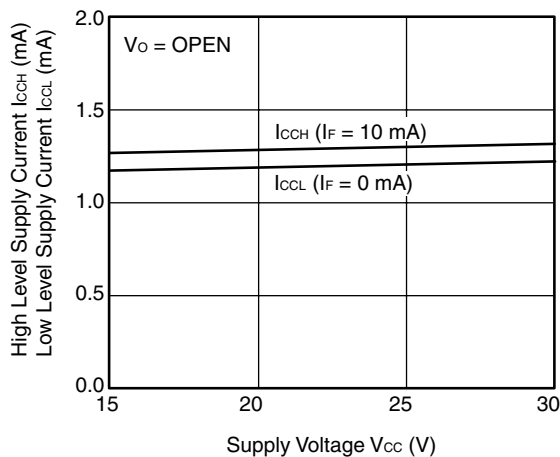
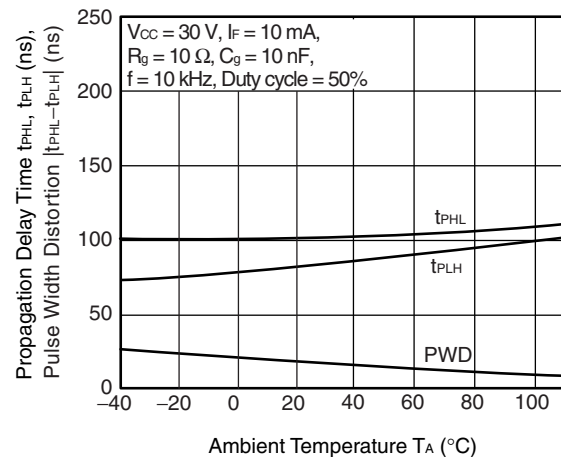
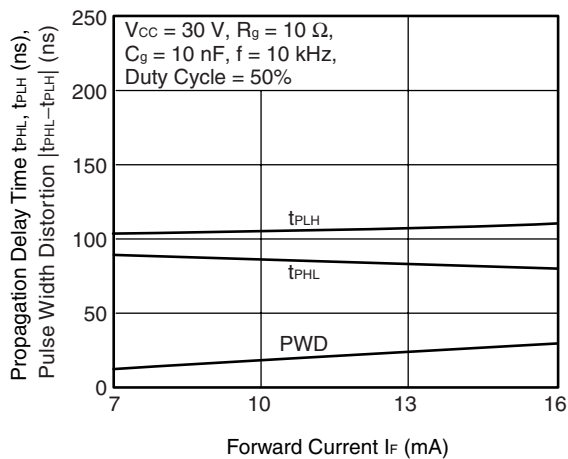
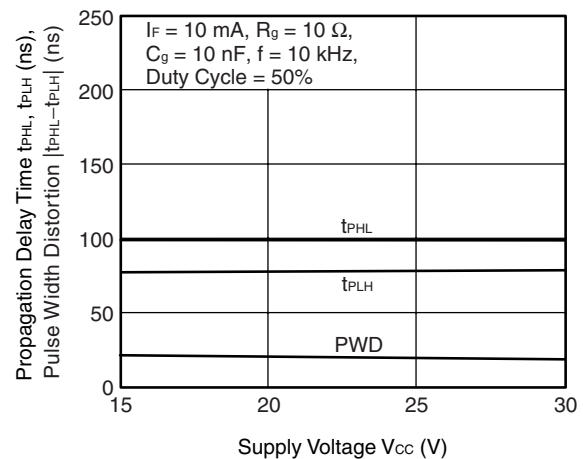
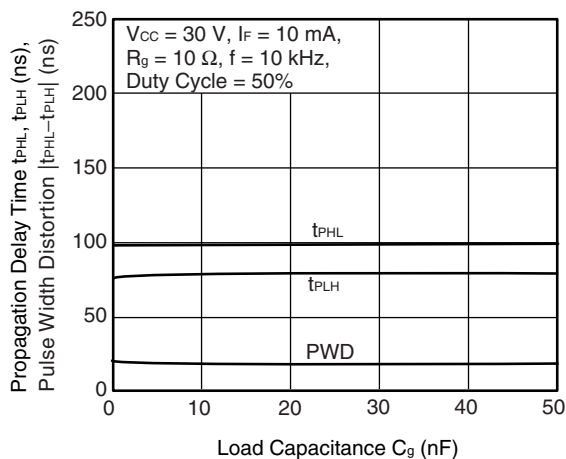
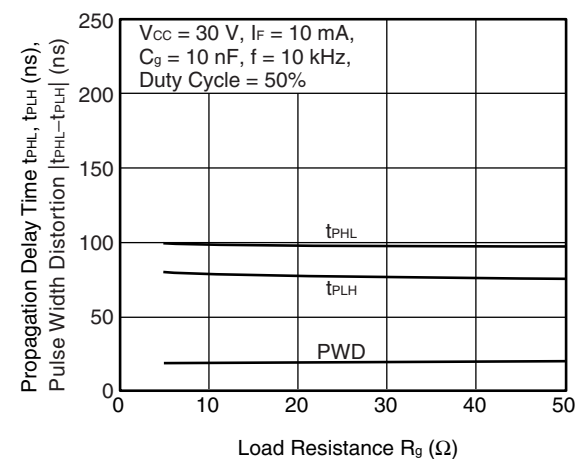
HIGH LEVEL OUTPUT VOLTAGE – SUPPLY VOLTAGE vs. HIGH LEVEL OUTPUT CURRENT



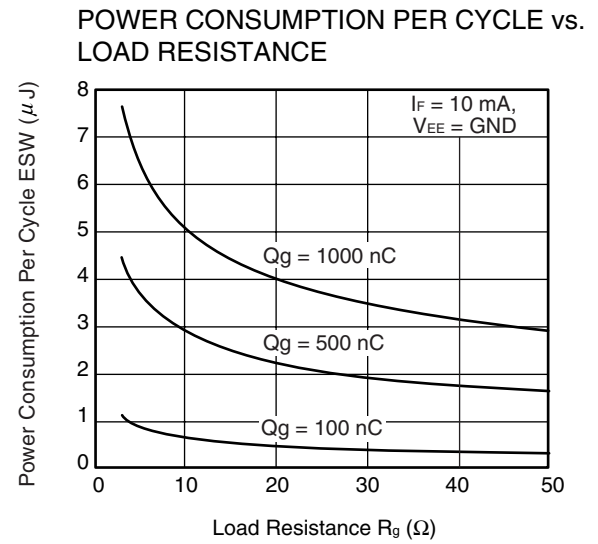
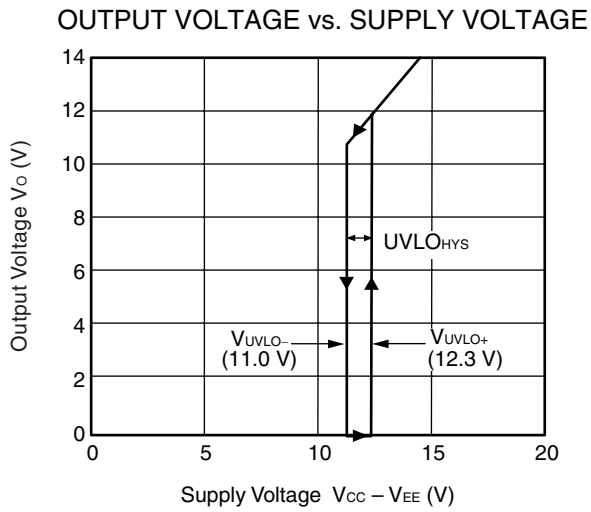
LOW LEVEL OUTPUT VOLTAGE vs. LOW LEVEL OUTPUT CURRENT



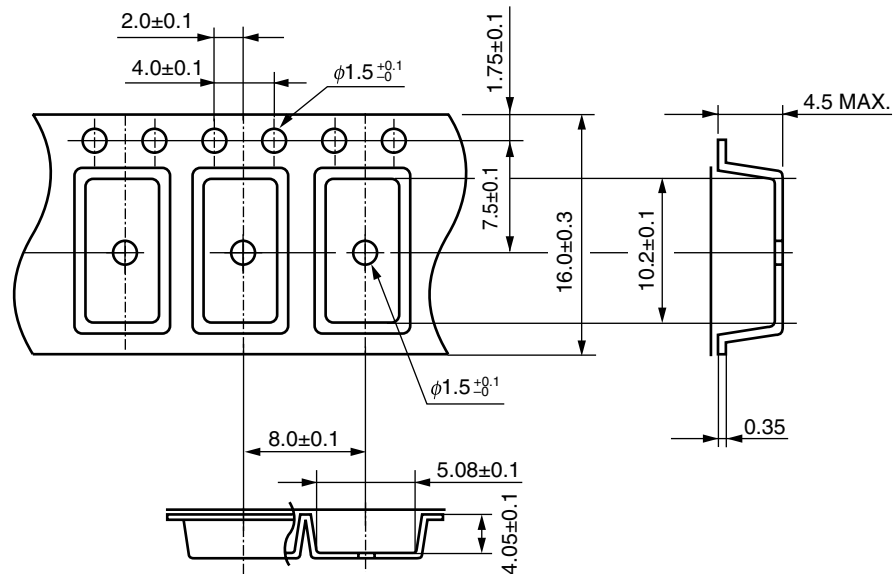
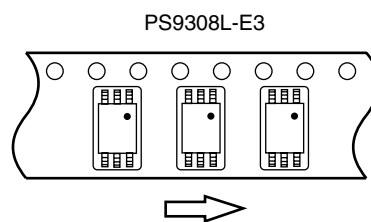
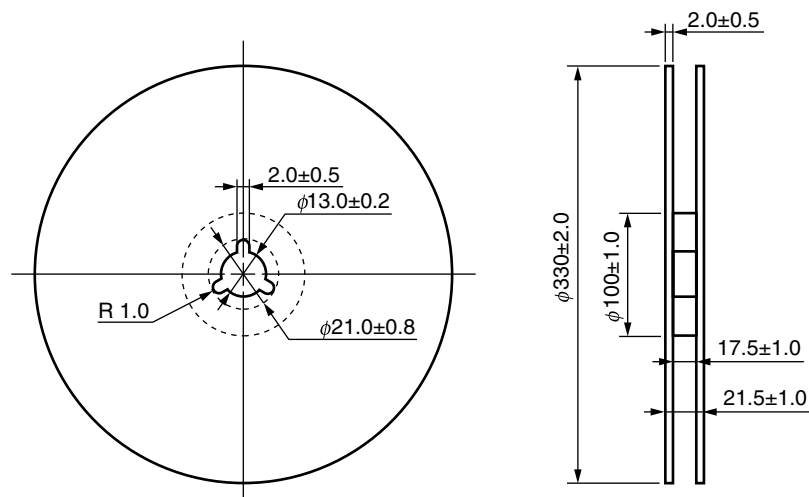
**Remark** The graphs indicate nominal characteristics.

SUPPLY CURRENT vs.  
SUPPLY VOLTAGEPROPAGATION DELAY TIME, PULSE WIDTH  
DISTORTION vs. AMBIENT TEMPERATUREPROPAGATION DELAY TIME, PULSE WIDTH  
DISTORTION vs. FORWARD CURRENTPROPAGATION DELAY TIME, PULSE WIDTH  
DISTORTION vs. SUPPLY VOLTAGEPROPAGATION DELAY TIME, PULSE WIDTH  
DISTORTION vs. LOAD CAPACITANCEPROPAGATION DELAY TIME, PULSE WIDTH  
DISTORTION vs. LOAD RESISTANCE

**Remark** The graphs indicate nominal characteristics.

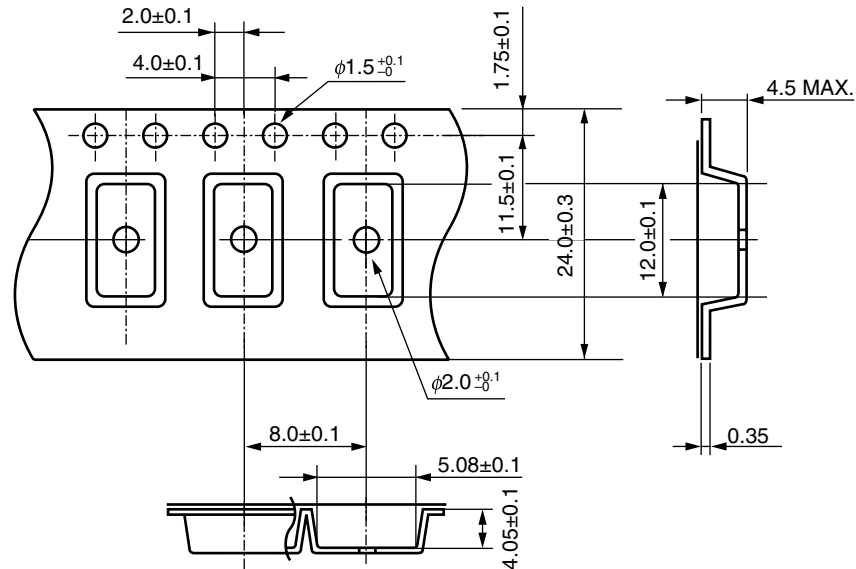


**Remark** The graphs indicate nominal characteristics.

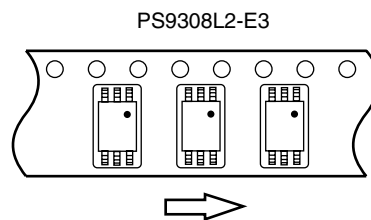
**<R> TAPING SPECIFICATIONS (UNIT: mm)****Outline and Dimensions (Tape)****Tape Direction****Outline and Dimensions (Reel)**

Packing: 2 000 pcs/reel

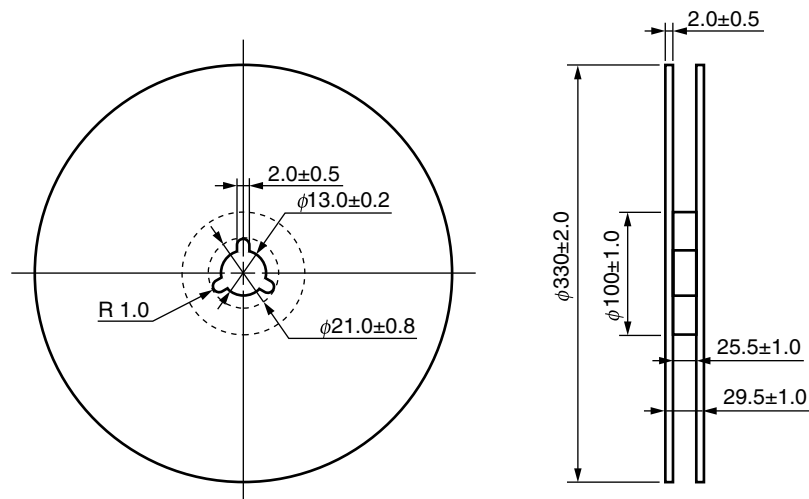
## Outline and Dimensions (Tape)



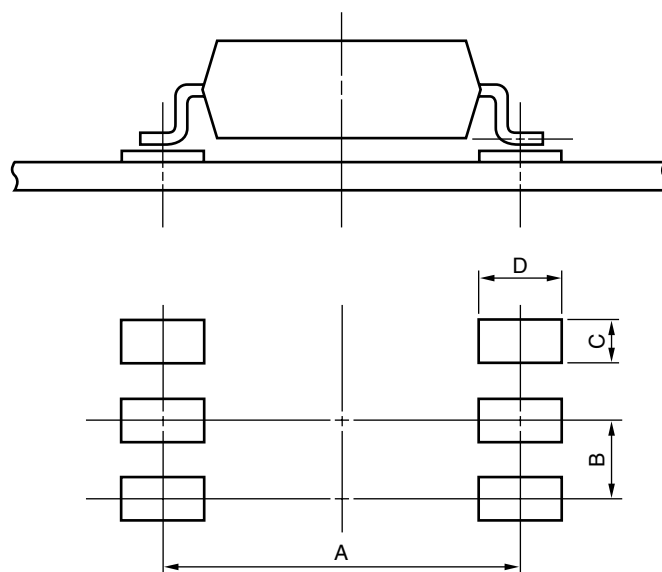
## Tape Direction



## Outline and Dimensions (Reel)



Packing: 2 000 pcs/reel

**RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)**

Part Number	Lead Bending	A	B	C	D
PS9308L	lead bending type (Gull-wing) for surface mount	9.2	1.27	0.8	2.2
PS9308L2	lead bending type (Gull-wing) for long creepage distance (surface mount)	10.2	1.27	0.8	2.2



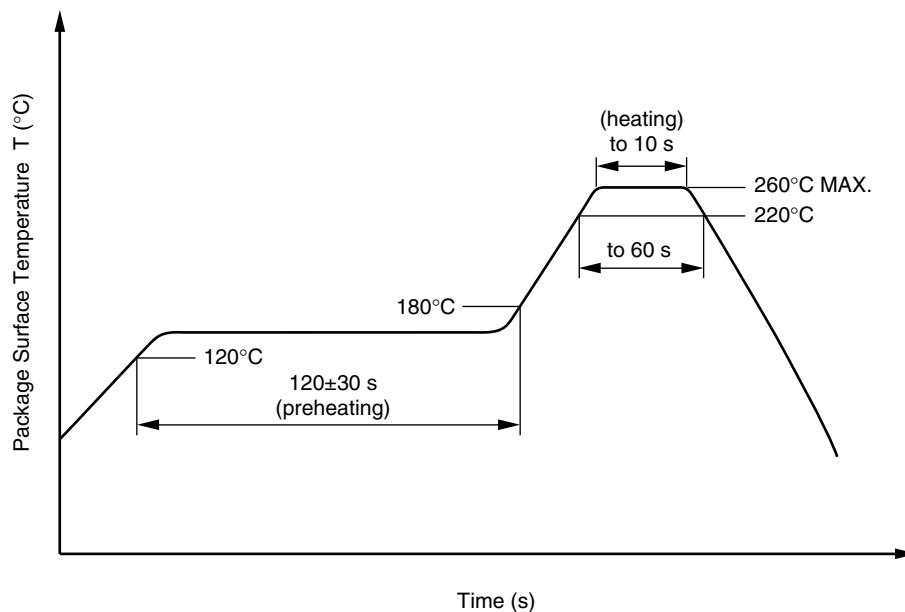
**NOTES ON HANDLING (UNIT: mm)**

## 1. Recommended soldering conditions

## (1) Infrared reflow soldering

- Peak reflow temperature 260°C or below (package surface temperature)
- Time of peak reflow temperature 10 seconds or less
- Time of temperature higher than 220°C 60 seconds or less
- Time to preheat temperature from 120 to 180°C 120 ± 30 s
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



## (2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

## (3) Soldering by Soldering Iron

- Peak Temperature (lead part temperature) 350°C or below
- Time (each pins) 3 seconds or less
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

## (4) Cautions

- Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

## 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

## USAGE CAUTIONS

1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
2. Board designing
  - (1) By-pass capacitor of more than 1.0  $\mu$ F is used between  $V_{CC}$  and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
  - (2) When designing the printed wiring board, ensure that the pattern of the IGBT collectors/emitters is not too close to the input block pattern of the photocoupler.

If the pattern is too close to the input block and coupling occurs, a sudden fluctuation in the voltage on the IGBT output side might affect the photocoupler's LED input, leading to malfunction or degradation of characteristics. (If the pattern needs to be close to the input block, to prevent the LED from lighting during the off state due to the abovementioned coupling, design the input-side circuit so that the bias of the LED is reversed, within the range of the recommended operating conditions, and be sure to thoroughly evaluate operation.)
  - (3) Pin 2 (which is an NC<sup>\*1</sup> pin) can either be connected directly to the GND pin on the LED side or left open.

Unconnected pins should not be used as a bypass for signals or for any other similar purpose because this may degrade the internal noise environment of the device.

**Note:** \*1. NC: Non-Connection (No Connection).
3. Make sure the rise/fall time of the forward current is 0.5  $\mu$ s or less.
4. In order to avoid malfunctions, make sure the rise/fall slope of the supply voltage is 3 V/ $\mu$ s or less.
5. Avoid storage at a high temperature and high humidity.

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Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/110/21	
Dielectric strength			
maximum operating isolation voltage	$U_{IORM}$	1 130	$V_{peak}$
Test voltage (partial discharge test, procedure a for type test and random test)	$U_{pr}$	1 808	$V_{peak}$
$U_{pr} = 1.6 \times U_{IORM}$ , $P_d < 5 \text{ pC}$			
Test voltage (partial discharge test, procedure b for all devices)	$U_{pr}$	2 119	$V_{peak}$
$U_{pr} = 1.875 \times U_{IORM}$ , $P_d < 5 \text{ pC}$			
Highest permissible overvoltage	$U_{TR}$	8 000	$V_{peak}$
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	$T_{stg}$	-55 to +125	°C
Operating temperature range	$T_A$	-40 to +110	°C
Isolation resistance, minimum value			
$V_{IO} = 500 \text{ V dc}$ at $T_A = 25^\circ\text{C}$	Ris MIN.	$10^{12}$	$\Omega$
$V_{IO} = 500 \text{ V dc}$ at $T_A \text{ MAX.}$ at least $100^\circ\text{C}$	Ris MIN.	$10^{11}$	$\Omega$
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve)			
Package temperature	$T_{si}$	175	°C
Current (input current $I_F$ , $P_{si} = 0$ )	$I_{si}$	400	mA
Power (output or total power dissipation)	$P_{si}$	700	mW
Isolation resistance			
$V_{IO} = 500 \text{ V dc}$ at $T_A = T_{si}$	Ris MIN.	$10^9$	$\Omega$

**Caution**

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
  1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
  2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

<b>Revision History</b>	<b>PS9308L, PS9308L2 Data Sheet</b>
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Rev.	Date	Description	
		Page	Summary
1.00	Aug 31, 2011	–	First edition issued
2.00	Sep 27, 2013	p.1	Modification of <b>FEATURES</b>
		p.3	Modification of <b>MARKING EXAMPLE</b>
		p.4	Modification of <b>ORDERING INFORMATION</b>
		p.7	Modification of <b>SWITCHING CHARACTERISTICS</b>
		pp.10 to 13	Addition of <b>TYPICAL CHARACTERISTICS</b>
		pp.14 to 15	Modification of <b>TAPING SPECIFICATIONS</b>
		p.19	Modification of <b>SPECIFICATION OF VDE MARKS LICENCE DOCUMENT</b>

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