# **Schottky Barrier Diodes**

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

### Features

- Extremely Fast Switching Speed
- Low Forward Voltage -0.35 V (Typ) @ I<sub>F</sub> = 10 mAdc

MAXIMUM RATINGS (T<sub>J</sub> = 125°C unless otherwise noted)

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and **PPAP** Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant\*

		,	
Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	30	V
Forward Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>F</sub>	225 1.8	mW mW/°C
Forward Current (DC)	١ <sub>F</sub>	200 Max	mA
Non–Repetitive Peak Forward Current t <sub>p</sub> < 10 msec	I <sub>FSM</sub>	600	mA
Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66%	I <sub>FRM</sub>	300	mA
Junction Temperature	TJ	-55 to 150	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



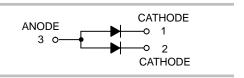
### **ON Semiconductor®**

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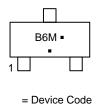
### **30 VOLT** SCHOTTKY BARRIER DETECTOR AND SWITCHING DIODES



SOT-23 (TO-236) **CASE 318** STYLE 12



### **MARKING DIAGRAM**



B6 Μ

= Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

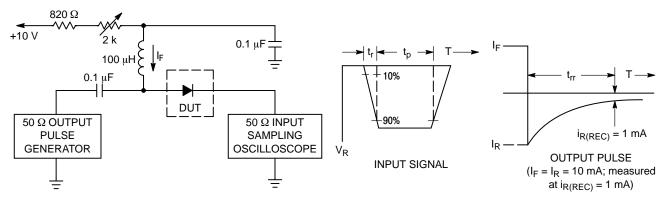
Device	Package	Shipping <sup>†</sup>
BAT54ALT1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
SBAT54ALT1G	SOT-23 (Pb-Free)	3,000/Tape & Reel
BAT54ALT3G	SOT-23 (Pb-Free)	10,000/Tape & Reel
SBAT54ALT3G	SOT-23 (Pb-Free)	10,000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## BAT54AL

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage $(I_R = 10 \ \mu A)$	V <sub>(BR)R</sub>	30	_	-	V
Total Capacitance ( $V_R = 1.0 V, f = 1.0 MHz$ )	CT	_	7.6	10	pF
Reverse Leakage $(V_R = 25 V)$	I <sub>R</sub>	_	0.5	2.0	μAdc
Forward Voltage $(I_F = 0.1 \text{ mA})$ $(I_F = 1.0 \text{ mA})$ $(I_F = 10 \text{ mA})$ $(I_F = 30 \text{ mA})$ $(I_F = 100 \text{ mA})$	VF	- - - -	0.22 0.29 0.35 0.41 0.52	0.24 0.32 0.40 0.50 0.80	V
Reverse Recovery Time ( $I_F = I_R = 10$ mAdc, $I_{R(REC)} = 1.0$ mAdc, Figure 1)	t <sub>rr</sub>	_	_	5.0	ns



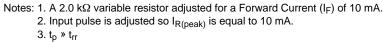


Figure 1. Recovery Time Equivalent Test Circuit

### BAT54AL

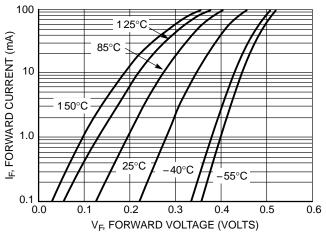
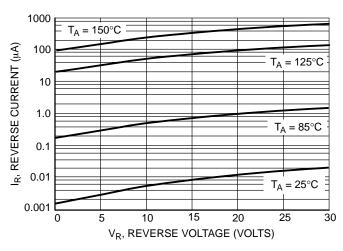


Figure 2. Forward Voltage





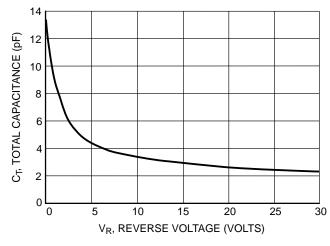
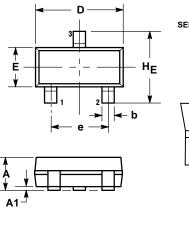


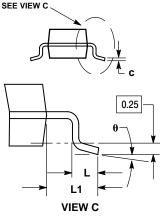
Figure 4. Total Capacitance

### BAT54AL

#### PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AP





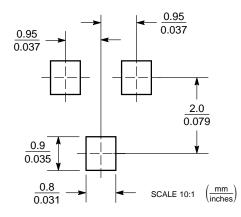
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  CONTROLLING DIMENSION: INCH.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM
- THICKNESS OF BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.040	0.044
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.37	0.44	0.50	0.015	0.018	0.020
С	0.09	0.13	0.18	0.003	0.005	0.007
D	2.80	2.90	3.04	0.110	0.114	0.120
Е	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.081
L	0.10	0.20	0.30	0.004	0.008	0.012
L1	0.35	0.54	0.69	0.014	0.021	0.029
HE	2.10	2.40	2.64	0.083	0.094	0.104
θ	0°		10°	0°		10°

STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE

SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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