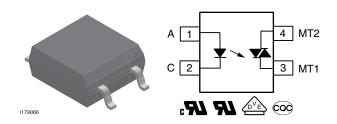


www.vishay.com

# Vishay Semiconductors

# Optocoupler, Phototriac Output, Non-Zero Crossing, 0.5 kV/µs dV/dt, 600 V



#### **FEATURES**

- High static dV/dt > 0.5 kV/µs
- Input sensitivity I<sub>FT</sub> = 5 mA, 7 mA, and 10 mA
- On-state RMS current I<sub>T(RMS)</sub> = 70 mA
- 600 V peak off-state blocking voltage
- Isolation test voltage 3750 V<sub>RMS</sub>
- Material categorization:
   For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>





COMPLIANT

GREEN

(5-2008)

#### **DESCRIPTION**

The VOM160 series phototriac consist a AlGaAs infrared emitting diode (IRED) optically coupled to a photosensitive non-zero crossing TRIAC packaged in a SOP-4 package. It has a IRED trigger current of 5 mA, 7 mA, and 10 mA.

The VOM160 series phototriac isolate low-voltage logic from 120  $V_{AC}$ , 240  $V_{AC}$ , and 380  $V_{AC}$  lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

#### **APPLICATIONS**

- Consumer appliances
- Triac drives
- · Solid-state relays
- Motor controls
- · Office equipment

#### **AGENCY APPROVALS**

- UL1577, file no. E52744, double protection
- cUL-file no. E52744, equivalent to CSA bulletin 5A
- VDE 0884-5, DIN EN 60747-5-5
- CQC: GB8898, GB4943

ORDERING INFORMATION						
V 0 M	PART NUMBER	0 X T	7.21 mm			
AGENCY CERTIFIED/PACKAGE	TRIGGER CURRENT I <sub>FT</sub>					
AGENCT CENTIFIED/PACKAGE		IRIGGER CURRENT IFT				
UL, cUL, CQC	5 mA	7 mA	10 mA			
	<b>5 mA</b> VOM160NT		10 mA VOM160RT			
UL, cUL, CQC	-	7 mA	10 1101			

#### Notes

- For additional information on the available options refer to option information.
- The product is available only on tape and reel.



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# Vishay Semiconductors

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	SYMBOL VALUE			
INPUT							
Reverse voltage			$V_R$	6	V		
Forward current			I <sub>F</sub>	60	mA		
Peak surge current	100 μs, 200 pps		I <sub>FSM</sub>	0.5	Α		
Power dissipation			P <sub>diss</sub>		mW		
OUTPUT							
Peak off-state voltage			$V_{DRM}$	600	V		
RMS on-state current			I <sub>T(RMS)</sub>	70	mA		
Peak non-repetitive surge current	PW = 100 ms, 120 pps		I <sub>TSM</sub>	1	Α		
Power dissipation			P <sub>diss</sub>	200	mW		
COUPLER							
Isolation test voltage	t = 1 min		V <sub>ISO</sub>	3750	V <sub>RMS</sub>		
Power dissipation			P <sub>tot</sub>	300	mW		
Storage temperature range			T <sub>stg</sub> - 55 to + 150		°C		
Ambient temperature range			T <sub>amb</sub>	- 40 to + 100	°C		
Soldering temperature (1)			T <sub>sld</sub>	260	°C		

#### Notes

- Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to Absolute Maximum Ratings for extended periods of the time can adversely affect reliability.
- (1) Wave soldering three cycles are allowed. Also refer to "Assembly Instructions" for surface mounted devices (www.vishay.com/doc?80054).

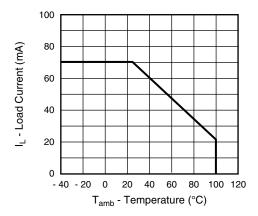


Fig. 1 - Recommended Operating Condition



# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>		1.2	1.5	٧
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>			10	μA
Input capacitance	$V_F = 0 V, f = 1 MHz$		Cı		25		pF
OUTPUT							
Off-state current	$V_D = V_{DRM}$		I <sub>DRM</sub>			100	nA
On-state voltage	I <sub>T</sub> = 100 mA		V <sub>TM</sub>			2.8	V
Critical rate of rise off-state voltage	$V_D = 0.67 V_{DRM}, T_J = 25  ^{\circ}C$		dV/dt <sub>cr</sub>	500			V/µs
Critical rate of rise of voltage at current commutation			dV/dt <sub>crq</sub>		0.13		V/µs
COUPLER							
LED trigger current, current required to latch output	V <sub>D</sub> = 3 V	VOM160N	I <sub>FT</sub>			5	mA
		VOM160P	I <sub>FT</sub>			7	mA
		VOM160R	I <sub>FT</sub>			10	mA
Capacitance (input - output)	f = 1 MHz, V <sub>IO</sub> = 0 V		C <sub>IO</sub>		0.8		pF
Peak off-state voltage	I <sub>C</sub> = 100 μA		$V_{DRM}$	600			V
Holding current			I <sub>hold</sub>		0.3		mA

#### Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

SAFETY AND INSULATION RATINGS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)			40/100/21		
Pollution degree (DIN VDE 0109)			2		
Comparative tracking index	CTI	175		399	
Peak transient overvoltage	V <sub>IOTM</sub>			6000	V <sub>peak</sub>
Peak insulation voltage	V <sub>IORM</sub>			707	V <sub>peak</sub>
Isolation resistance at T <sub>amb</sub> = 100 °C, V <sub>DC</sub> = 500 V	R <sub>IO</sub>	10 <sup>11</sup>			Ω
Isolation resistance at T <sub>amb</sub> = 25 °C, V <sub>DC</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω
Safety rating - power rating	P <sub>SO</sub>			400	mW
Safety rating - input current	I <sub>SI</sub>			150	mA
Safety rating - temperature	T <sub>SI</sub>			165	°C
Creepage distance		5			mm
Clearance distance		5			mm
Insulation thickness		0.4			mm

# Vishay Semiconductors

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

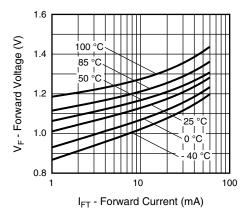


Fig. 2 - Forward Current vs. Forward Voltage

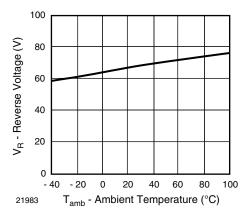


Fig. 3 - Reverse Voltage vs. Ambient Temperature

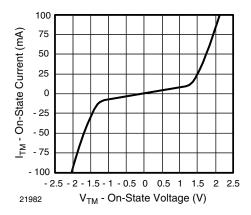


Fig. 4 - On-State Current vs. On-State Voltage

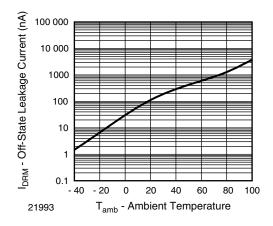


Fig. 5 - Off-State Leakage Current vs. Ambient Temperature

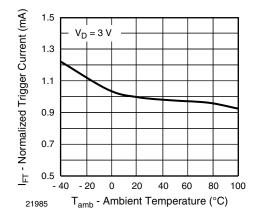


Fig. 6 - Normalized Trigger Current vs. Ambient Temperature

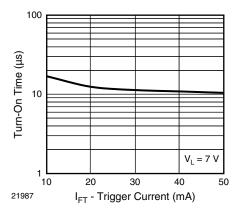


Fig. 7 - Trigger Current vs. Turn-On Time





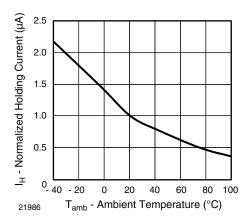


Fig. 8 - Normalized Holding Current vs. Ambient Temperature

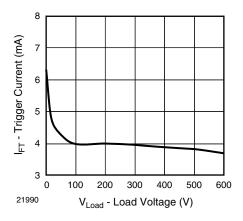


Fig. 11 - Trigger Current vs. Load Voltage

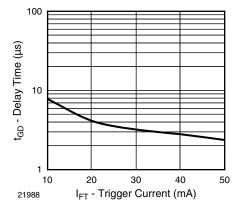


Fig. 9 - Trigger Current vs. Delay Time

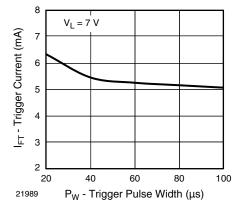
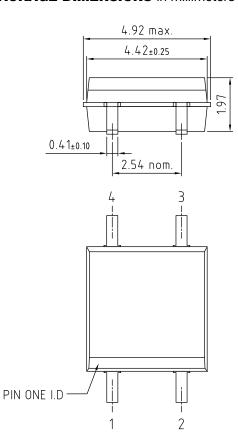
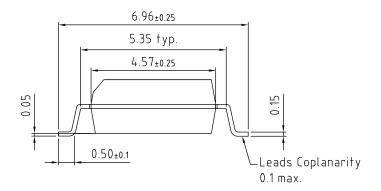


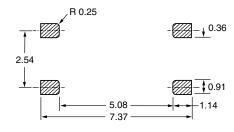
Fig. 10 - Trigger Current vs. Trigger Pulse Width

# Vishay Semiconductors

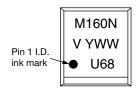
#### **PACKAGE DIMENSIONS** in millimeters







#### **PACKAGE MARKING** (example)



#### TAPE AND REEL PACKAGING

Dimensions in millimeters

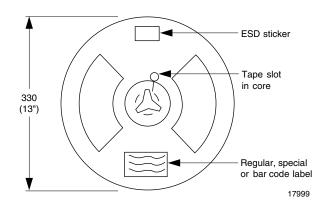


Fig. 12 - Tape and Reel Shipping Medium, 2000 units per reel

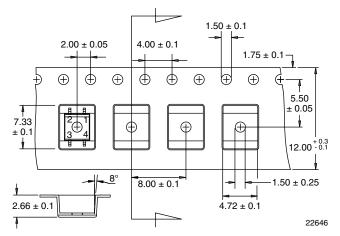


Fig. 13 - Tape Dimensions



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Revision: 02-Oct-12 Document Number: 91000

# AMEYA360 Components Supply Platform

# **Authorized Distribution Brand:**

























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