

DEM-DAI1772/1773 EVM

User's Guide

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It is important to operate this EVM within the input voltage range of 5 V and the output voltage range of 5 V.

Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 55°C. The EVM is designed to operate properly with certain components above 55°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

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Description

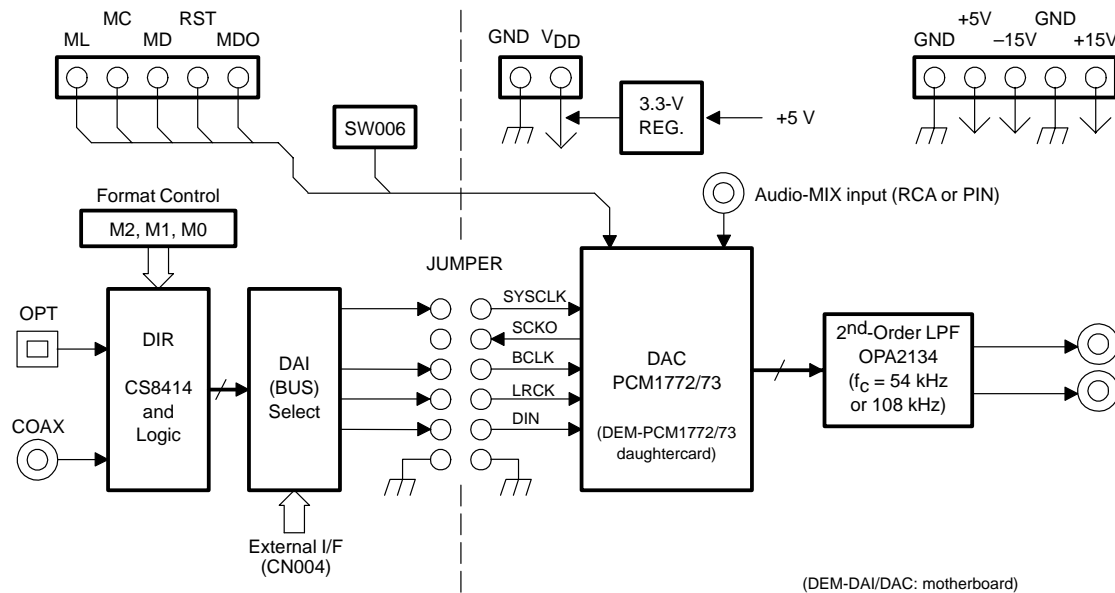
The DEM-DAI1772/1773 is an evaluation board for the PCM1772/1773, a 24-bit, 50-kHz, two-channel audio DAC with digital audio receiver, mode control switch, analog mixing input, stereo headphone output, and more.

Although the PCM1772/1773 requires a 2.5-V or 3.3-V supply, the DEM-DAI1772 operates from a 5-V analog power supply with S/PDIF input signal and has an onboard 2.5-V or 3.3-V voltage regulator. In order to control the PCM1772/1773, demonstration software (only for the PCM1772) is provided. This software controls the device via a PC-compatible parallel port.

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1.1 Block Diagram

Figure 1–1. DEM-DAI1772/1773 Block Diagram



1.2 DEM-DAI1772/1773 Basic Connection and Operation

- ☐ Connect a 5-V power supply to V_{CC} and GND on connectors CN054 and CN055, respectively.
- ☐ Connect the S/PDIF signal into CN001 (COAX) or U001 (OPT).
- ☐ The system clock and other digital signals are supplied from a digital audio receiver through jumpers to the PCM1772/1773.
- ☐ Set the input data format using switches SW001–SW003 and the demonstration software (PCM1772) or switch SW006 (PCM1773).
- ☐ For PCM1772: Set all the DIP switches in SW006 to H for software control.
- ☐ For PCM1773: Set the DIP switches in SW006 as in Table 1–2 for manual configuration control.
- ☐ Connect audio cables to RCA connectors CN104 and CN105 for DAC left- and right-channel output (2 V rms full scale).
- ☐ Connect an external audio RCA cable to CN002 as an analog mixing input if necessary (0.584 V_{HP-PP} full scale).

1.2.1 Configuration Controls

Table 1–1. SW001/002/003: CS8414 (Digital Audio I/F Receiver) on DEM-DAI/DAC Board

SW001 (M0)	SW002 (M1)	SW003 (M2)	Data Format Selection
L	L	L	16–24-bit left-justified, MSB-first
L	H	L	I ² S
H	L	H	16-bit standard, right-justified
L	H	H	18-bit standard, right-justified

- SW004: Manual reset
- SW005: Digital audio interface selection
Internal – CS8414
External – (CN004)
- JP001 : BCK selection
- JP101–JP106: for f_C of second-order post-filter

When using the left-justified MSB-first data format, remove the jumper from BCK to BCK (left justified).

Table 1–2. SW006: Hardware Control (Only for the PCM1773)

Position	SW006(ML) Audio I/F Data Format	SW006(MD) De-Emphasis Selection	SW006(MC) Analog Mixing Control
L	16 to 24 bits, left-justified format	44.1-kHz de-emphasis off	Analog mixing off
H	16 to 24 bits, I ² S format	44.1-kHz de-emphasis on	Analog mixing on

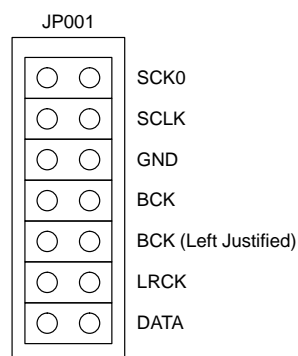
1.3 Jumpers

There are eight jumpers onboard.

- ☐ JP001: for digital signals
- ☐ JP101–JP106: for post-filter frequency
- ☐ CN057: for 3.3-V power supply

1.3.1 JP001

The digital signals generated by the digital audio receiver are input to this jumper. For each shorted pair of pins, the corresponding digital signal is input to the PCM1772/1773.



1.3.2 JP101–JP106 (six pieces)

These jumpers determine f_C of the second-order post filter.

- ☐ JP101–JP106 shorted, $f_C = 54$ kHz
- ☐ JP101–JP106 open, $f_C = 108$ kHz

1.3.3 CN057

The board has a 3.3-V voltage regulator for the digital power supply of U003/005/006. This jumper determines whether the onboard voltage regulator is used or not.

CN057 shorted

Supply 3.3 V from the onboard voltage regulator, REG1117-3.3.

CN057 open

Use an external 3.3-V power supply.

1.3.4 SW001 on PCM1772/1773 Daughtercard

Power-down (PD) operation can be switched by SW001 on the PCM1772/1773 daughtercard.

L: Power-down operation

H: Normal operation

1.4 Demonstration Software (Version 1.0) for the PCM1770 and PCM1772

Demonstration software is provided to control the PCM1772 internal register using a PC under Microsoft Windows™ 3.1/95/98. For use of this software, a printer cable is required to connect between the PC and connector CN003 of the DEM-DA1772. This demonstration software is for common use by the PCM1770 and PCM1772.

1.4.1 Installation

The demonstration software includes a total of five files in the DEM1772 directory (folder) on a floppy disk. These files are

Dem1770.exe

Dem1770.ini

Vbrjp200.dll

inpout.dll

Ver.dll

Copy these files to an appropriately named directory (folder) such as

C:\DEM1772. Then change the printer port address in Dem1770.ini to &H378, &H278 or &H3BC (most PCs use &H378 as the default printer port address).

1.4.2 Operation

After starting execution of Dem1770.exe, the following window appears on screen. This panel shows contents of internal register.



There are two menus on the top of the panel.

Execute (E):

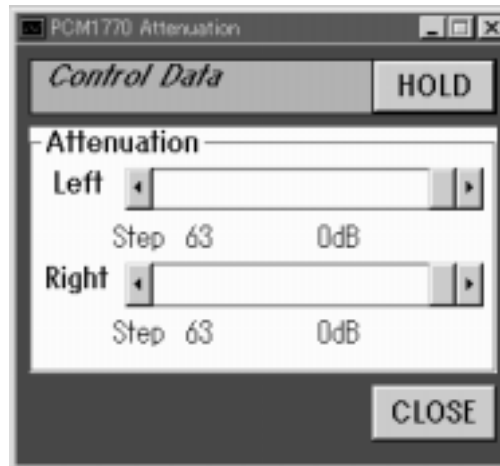
Initialize (I): Initialize all internal registers

Reset (R) : Reset all selected data to PCM1772

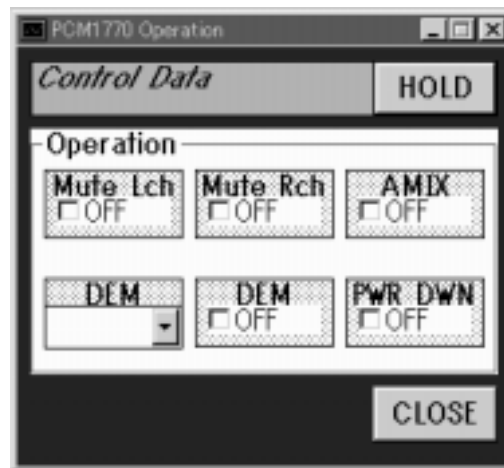
Exit (X) : Exit this program

Window (W):

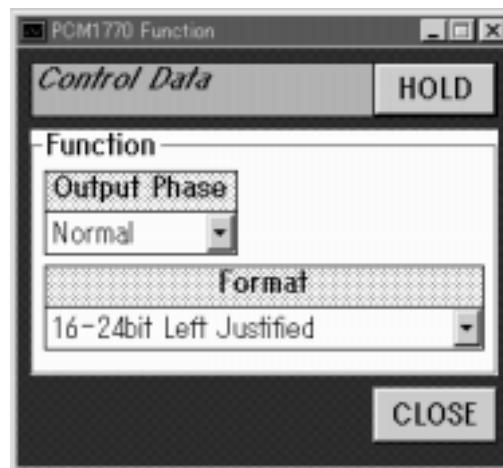
Attenuation (A) : This panel controls the attenuation function of the PCM1772.



Operational control (O): This panel controls mute, DAC operation, de-emphasis, analog mixing, and the power-down function.



Functional control (F) : This panel controls the clock format and output phase functions.



Schematics and Printed-Circuit Boards

This chapter presents the DEM-DAI/DAC and the DEM-PCM1772/1773 printed-circuit boards and schematic diagrams.

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Figure 2-2. DEM-DAI/DAC Top View

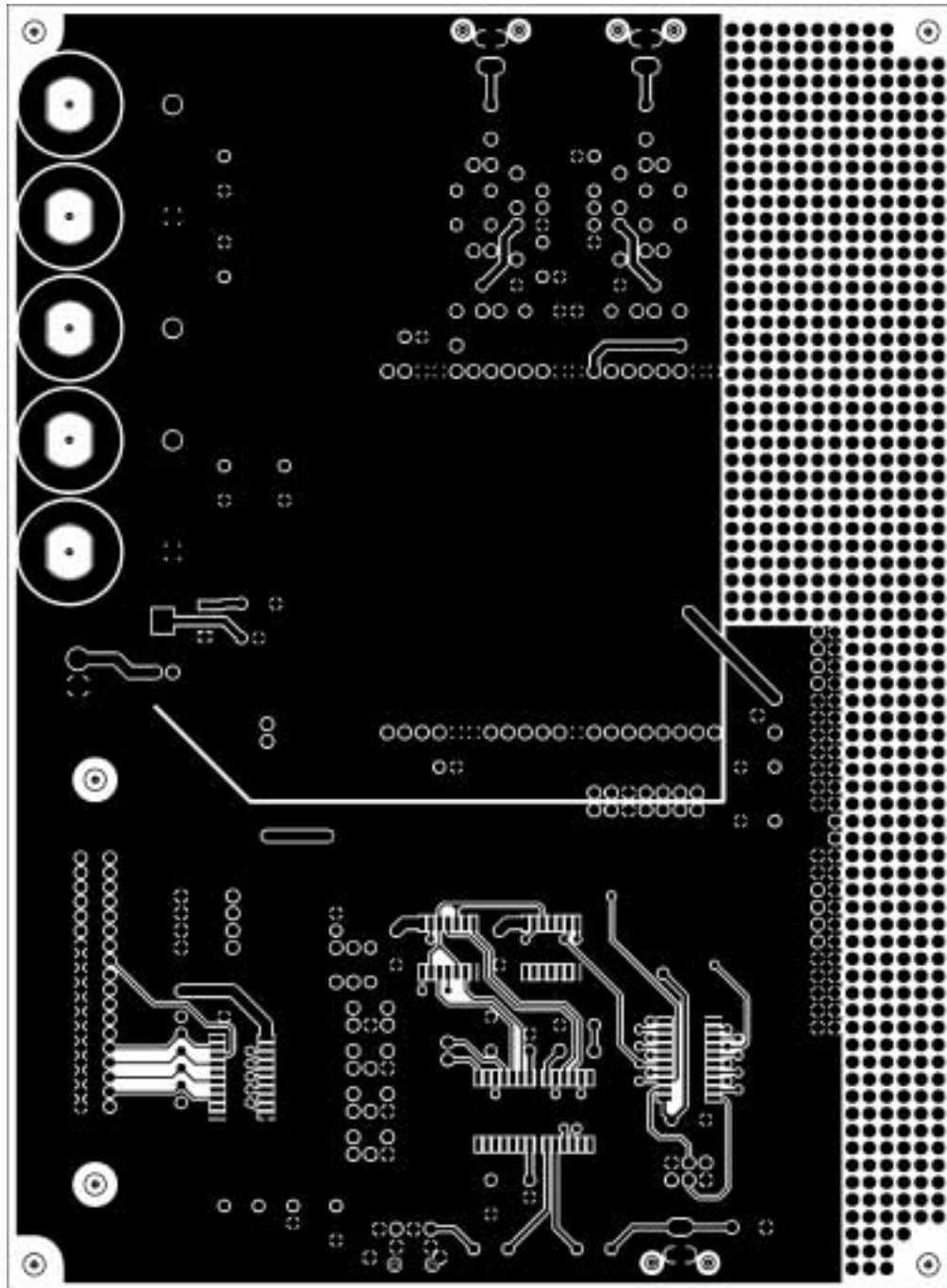
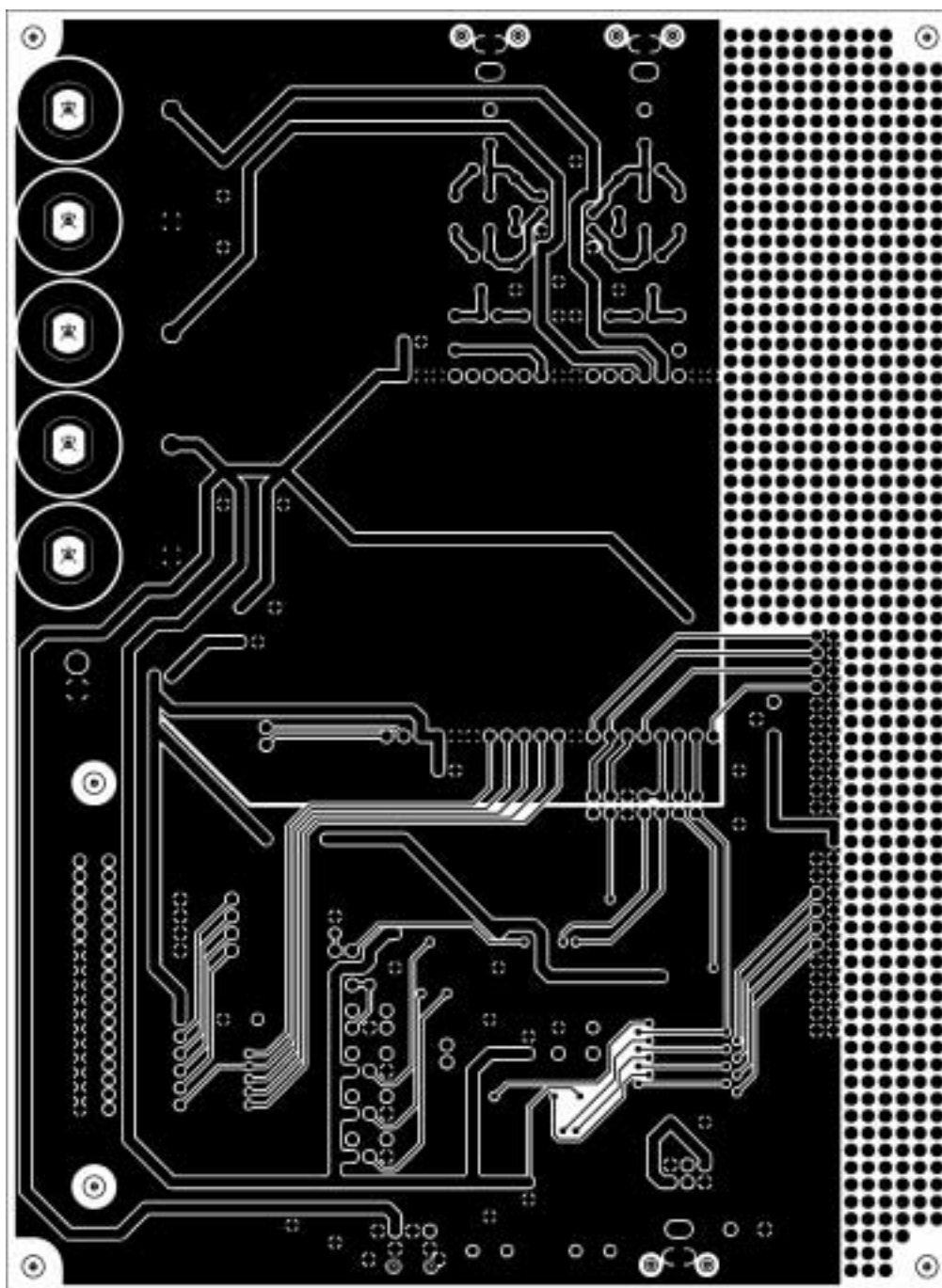


Figure 2–3. DEM-DAI/DAC Bottom View



2.2 DEM-PCM1772/1773 Daughtercard Printed-Circuit Board

Figure 2–4. DEM-PCM1772/1773 Silkscreen

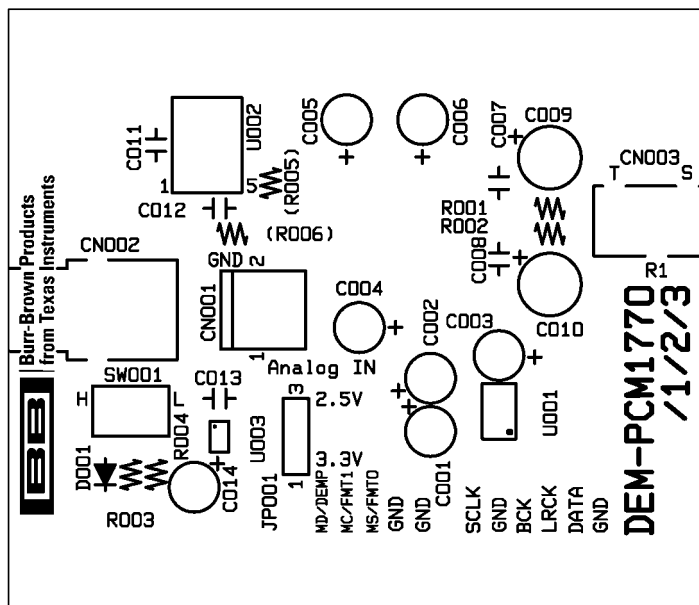


Figure 2–5. DEM-PCM1772/1773 Top View

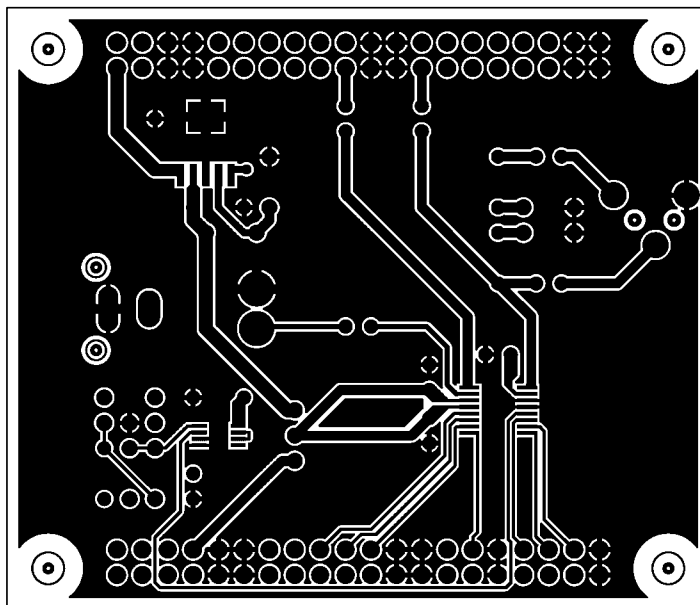


Figure 2–6. DEM-PCM1772/1773 Bottom View

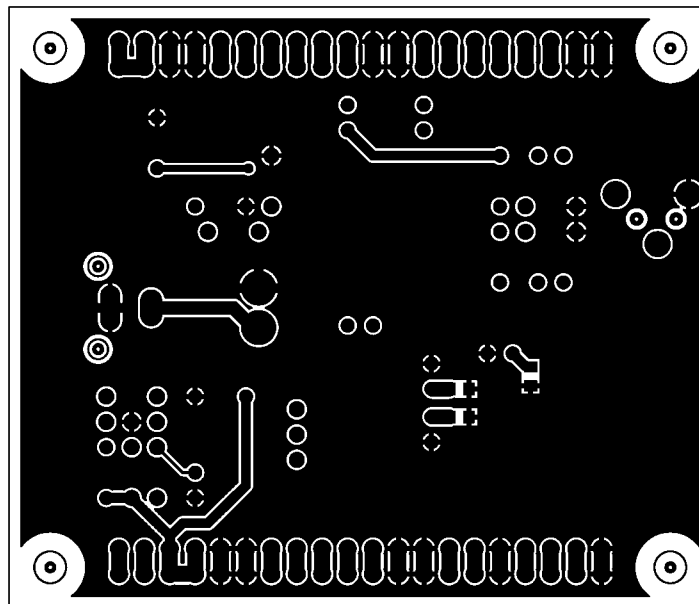
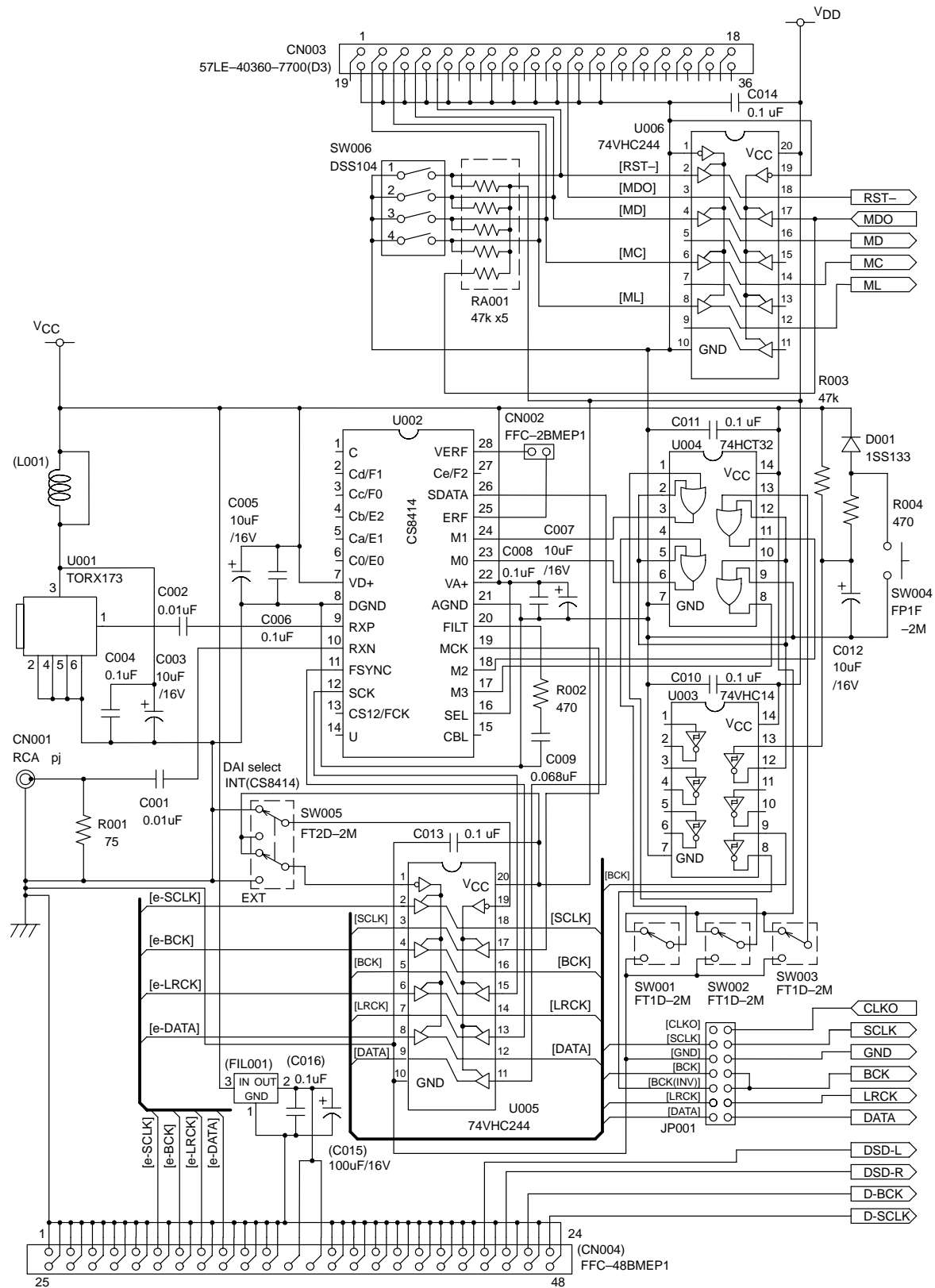
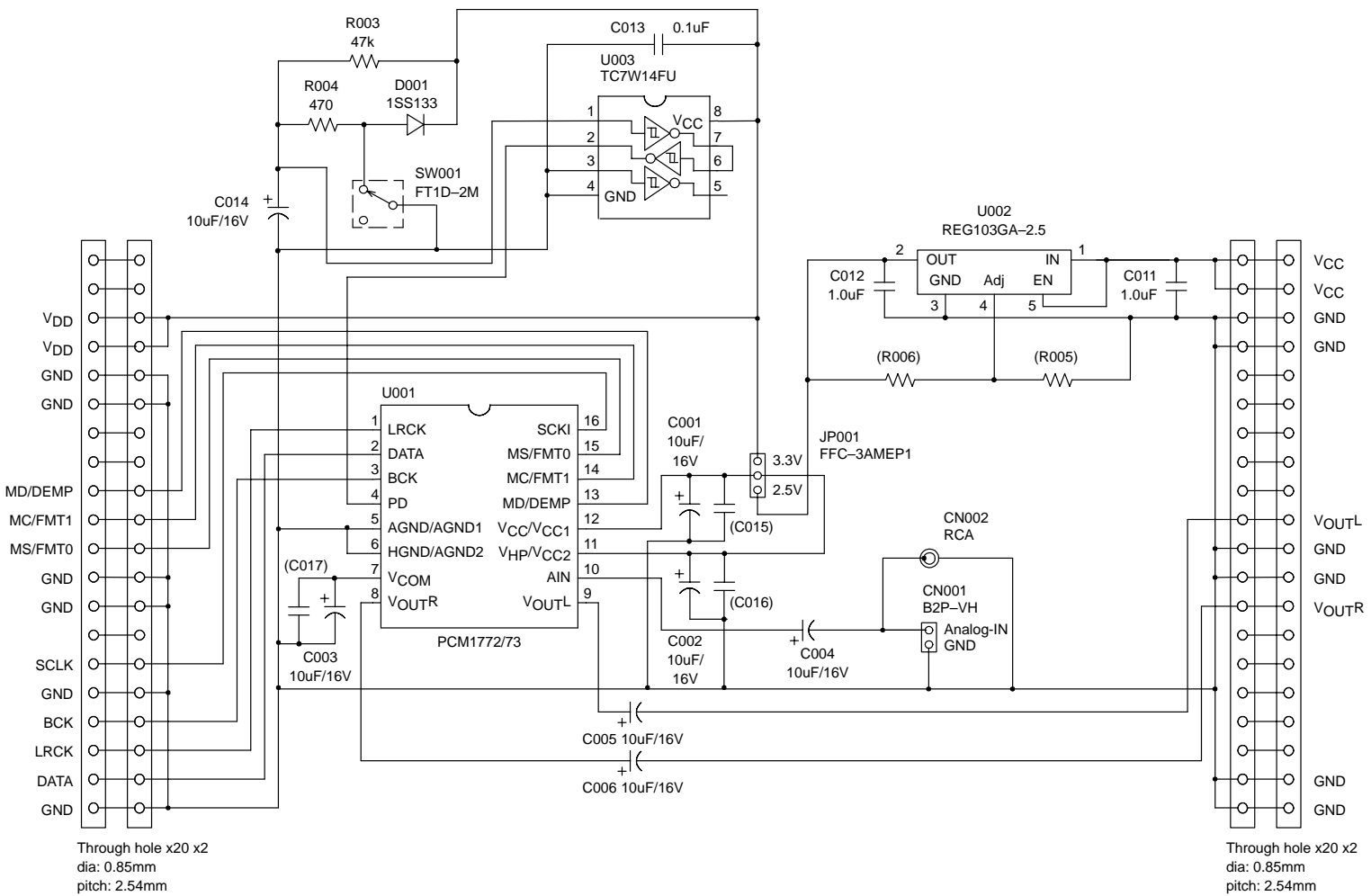


Figure 2–9. DEM-DAI/DAC Digital Audio Interface



2.4 DEM-PCM1772/1773 Daughtercard Schematics

Figure 2–10. DEM-PCM1772/1773



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