

DMN6013LFG

60V N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI®

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
60V	$13m\Omega$ @ $V_{GS} = 10V$	10.3A
60 V	18mΩ @ V_{GS} = 4.5 V	8.8A

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

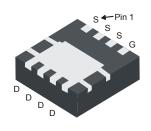
Features and Benefits

- Low R_{DS(ON)} ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

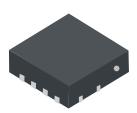
Mechanical Data

- Case: POWERDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (approximate)

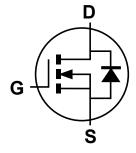
POWERDI®3333-8







Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging	
DMN6013LFG-7	POWERDI [®] 3333-8	2,000/Tape & Reel	
DMN6013LFG-13	POWERDI [®] 3333-8	3,000/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



N63= Product Type Marking Code YYWW = Date Code Marking YY = Last digit of year (ex: 13 = 2013) WW = Week code (01 ~ 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	60	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Prain Current (Note C) V = 40V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	10.3 8.3	Α
Continuous Drain Current (Note 6) V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ΙD	45 28	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	58.3	Α	
Maximum Continuous Body Diode Forward Current (Note 6)		Is	3	Α
Avalanche Current, L = 0.1mH		I _{AS}	33.3	Α
Avalanche Energy, L = 0.1mH		Eas	56.8	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

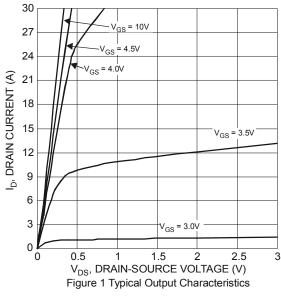
Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)		P _D	1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	р	123	°C/W
Thermal Nesistance, sunction to Ambient (Note 3)	t < 10s	$R_{ hetaJA}$	69	
Total Power Dissipation (Note 6)		P_{D}	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	D	60	°C/W
Thermal Resistance, Junction to Ambient (Note 0)	t < 10s	$R_{ hetaJA}$	34	
Total Power Dissipation (Note 6)	P_D	40	W	
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	6.7	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

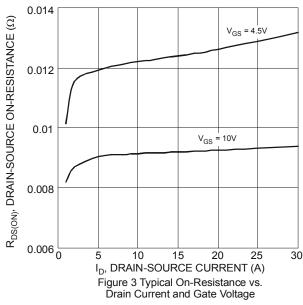
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current, T _J = +25°C	I _{DSS}	_	_	1	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	1	1.8	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	D	_	9.3	13	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	12.3	18	11177	V _{GS} = 4.5V, I _D = 8A	
Diode Forward Voltage	V_{SD}		0.7	1.2	V	V _{GS} = 0V, I _S = 1.7A	
DYNAMIC CHARACTERISTICS (Note 8)			•				
Input Capacitance	C _{iss}	1	2577	-	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	162	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	132	_	pF		
Gate Resistance	Rg	_	0.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	26.6	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	55.4	_	nC		
Gate-Source Charge	Q_{gs}	_	9.3	_	nC	$V_{DS} = 30V, I_D = 10A$	
Gate-Drain Charge	Q_{gd}		12.6	_	nC	1	
Turn-On Delay Time	t _{D(on)}		6.2	_	ns		
Turn-On Rise Time	t _r		9.9	-	ns	$V_{GS} = 10V, V_{DS} = 30V,$ $R_G = 3\Omega, I_D = 10A$	
Turn-Off Delay Time	t _{D(off)}		27.6	_	ns		
Turn-Off Fall Time	t _f		11.7	_	ns		
Body Diode Reverse Recovery Time	t _{rr}	_	9.4	_	nS	1 - 400 di/dt - 4000/	
Body Diode Reverse Recovery Charge	Q _{rr}	_	18.6	_	nC	-I _F = 10A, di/dt = 100A/μs	

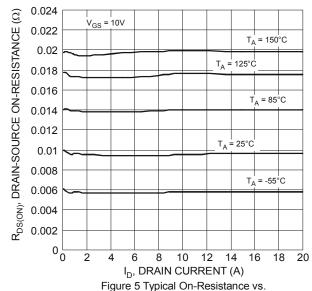
Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





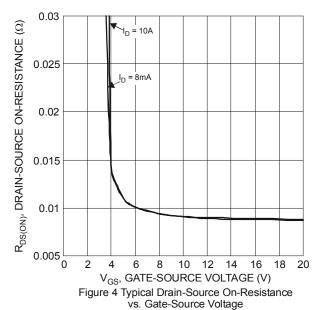






Drain Current and Temperature

30 V_{DS} = 5.0V 25 ID, DRAIN CURRENT (A) 20 15 10 T_A = 150°C r_A = 85°C 5 0 <u></u> 1.5 5 2 2.5 3 3.5 4.5 V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2 Typical Transfer Characteristics



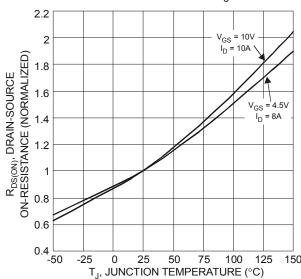
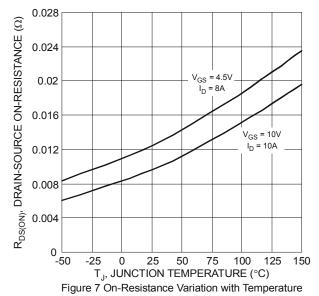
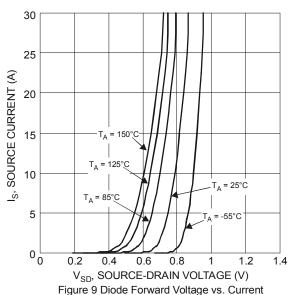


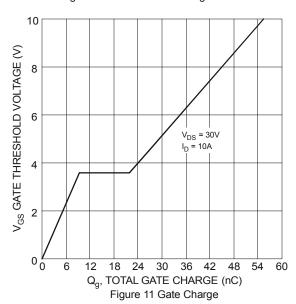
Figure 6 On-Resistance Variation with Temperature











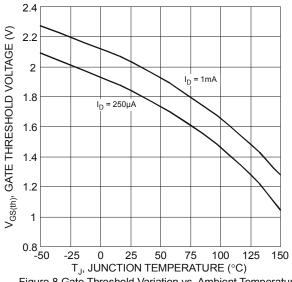
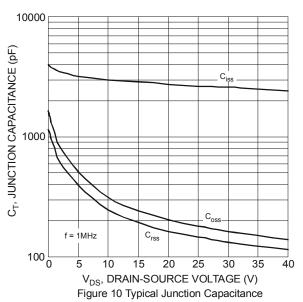
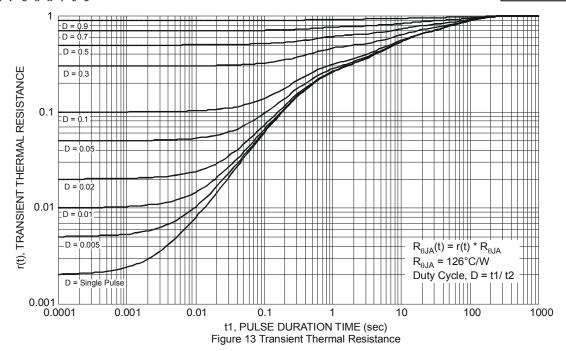


Figure 8 Gate Threshold Variation vs. Ambient Temperature



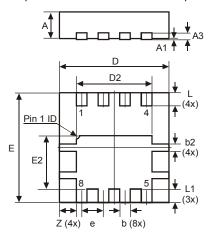
100 F R_{DS(on)} Limited 10 ID, DRAIN CURRENT (A) 0.1 $T_{J(max)} = 150$ °C 0.01 $T_A = 25^{\circ}C$ V_{GS} = 10V Single Pulse DUT on 1 * MRP Board 0.001 0.1 100 V_{DS} , DRAIN-SOURCE VOLTAGE (V) Figure 12 SOA, Safe Operation Area





Package Outline Dimensions

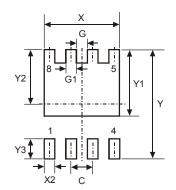
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



POWERDI 3333-8					
Dim	Min	Max	Тур		
D	3.25	3.35	3.30		
Е	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
E2	1.56	1.66	1.61		
Α	0.75	0.85	0.80		
A1	0	0.05	0.02		
A3	_	_	0.203		
b	0.27	0.37	0.32		
b2	_	_	0.20		
L	0.35	0.45	0.40		
L1	_	_	0.39		
е	_	_	0.65		
Z	_	_	0.515		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	0.650			
G	0.230			
G1	0.420			
Y	3.700			
Y1	2.250			
Y2	1.850			
Y3	0.700			
X	2.370			
X2	0.420			



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