

Features

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High-speed performance
- Small SMT package
- RoHS compliant*
- Agency listing: <a>\$\mathbb{A}\$\$

Applications

- Ethernet ports
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

TBU-DT Series - TBU® High-Speed Protectors

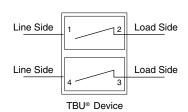
General Information

The TBU-DT Series of Bourns® TBU® (Transient Blocking Unit) products are very low capacitance dual unidirectional high-speed surge protection components designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.

The TBU-DT series is a unidirectional TBU® device; the TBU® protector will trip in less than 1 μ s when the current reaches the maximum value in one direction only, that is when Pin 1 is positive in voltage with respect to Pin 2, and Pin 4 is positive with respect to Pin 3. No current limiting exists in the opposite polarity, and the TBU® device appears as resistive in nature. The reverse current should not exceed the maximum trigger current level of the TBU® device. An external diode may be used to prevent reverse current in DC biased applications.

The TBU® protector blocks surges and provides an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events. After the surge, the TBU® device resets when the voltage across the TBU® device falls to the V_{reset} level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below Vreset (such as unpowered signal lines).

The TBU® device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.



Agency Listing

Description					
UL	File Number: E315805				

Absolute Maximum Ratings (@ T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Part Number	Value	Unit		
V.	Book impulse voltage withstand with duration less than 10 mg	TBU-DT065-xxx-WH	650	V		
V _{imp}	Peak impulse voltage withstand with duration less than 10 ms	TBU-DT085-xxx-WH	850	, v		
V	Continuous A.C. DMC voltage	TBU-DT065-xxx-WH	300	V		
V _{rms}	Continuous A.C. RMS voltage	TBU-DT085-xxx-WH	425	V		
Top	Operating temperature range	-40 to +125	°C			
T _{stg}	Storage temperature range	-65 to +150	°C			

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter		Part Number	Min.	Тур.	Max.	Unit
I _{trigger}	Current required for the protected state	e device to go from operating state to	TBU-DTxxx-100-WH TBU-DTxxx-200-WH TBU-DTxxx-300-WH TBU-DTxxx-500-WH	100 200 300 500	150 300 450 750	200 400 600 1000	mA
R _{device} Series resistance of the TBU® device		V _{imp} = 650 V I _{trigger} (min.) = 100 mA V _{imp} = 650 V I _{trigger} (min.) = 200 mA V _{imp} = 650 V I _{trigger} (min.) = 300 mA V _{imp} = 650 V I _{trigger} (min.) = 500 mA V _{imp} = 850 V I _{trigger} (min.) = 100 mA V _{imp} = 850 V I _{trigger} (min.) = 200 mA V _{imp} = 850 V I _{trigger} (min.) = 300 mA V _{imp} = 850 V I _{trigger} (min.) = 300 mA V _{imp} = 850 V I _{trigger} (min.) = 500 mA		/H /H /H /H /H /H	8.5 5.6 4.6 4.0 10.3 7.4 6.5 5.8	10.0 6.6 5.6 4.8 12.1 8.7 7.7 6.9	Ω
R _{match}	Package resistance m	atching of the TBU® device #1 - TBU® de	-0.5		+0.5	Ω	
t _{block}	Time for the device to	go from normal operating state to protect	ted state			1	μs
IQ	Current through the tri	ggered TBU® device with 50 Vdc circuit v	oltage	0.25	0.50	1.00	mA
V _{reset}	Voltage below which t	ne triggered TBU® device will transition to	10	14	18	V	
R _{th(j-l)}	Junction to package p	ads - FR4 using recommended pad layou		116		°C/W	
R _{th(i-l)}	Junction to package p	ads - FR4 using heat sink on board (6 cm	1 ²)		96		°C/W

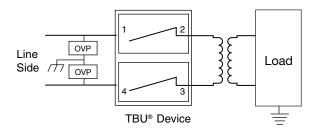
^{*}RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011. Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

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Reference Application

The TBU® device can be used to protect against excessive voltage surges in transformer coupled equipment, as shown in the figure below. The TBU® protector prevents any surges from causing damage. An overvoltage protection device, such as an MOV or GDT, may be used to provide additional overvoltage protection if the surge voltage is likely to be above the maximum rating of the TBU® device.



Basic TBU Operation

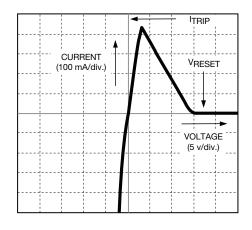
The TBU® device is a silicon-based, solid-state, resettable device which is placed in series with a signal path. The TBU® device operates in approximately 1 μs - once line current exceeds the TBU® device's trigger current $I_{trigger}.$ When operated, the TBU® device restricts line current to less than 1 mA typically. When operated, the TBU® device will block all system voltages and any other voltages including the surge up to rated limits.

After the surge, the TBU® device resets when the voltage across the TBU® device falls to the V_{reset} level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below V_{reset} (such as unpowered signal lines).

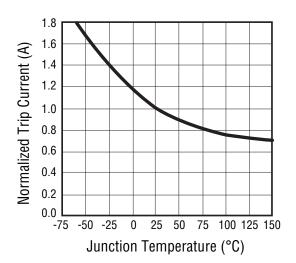
If the line has a normal DC bias above V_{reset}, the voltage across the TBU® device may not fall below V_{reset} after the surge. In such cases, special care needs to be taken to ensure that the TBU® device will reset, otherwise an automatic or manual power down will be required. Bourns application engineers can provide further assistance.

Performance Graphs

V-I Characteristic - TBU-DT085-300-WH (Pin 2-1 & Pin 3-4)



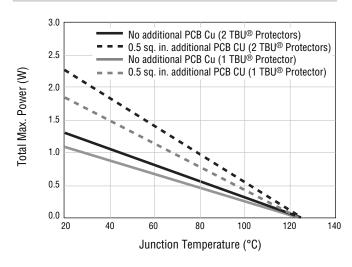
Typical Trigger Current vs. Temperature



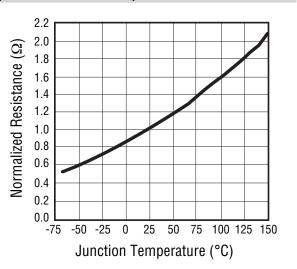
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Performance Graphs (Continued)

Power Derating Curve

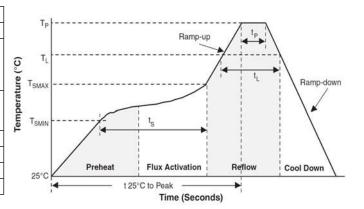


Typical Resistance vs. Temperature



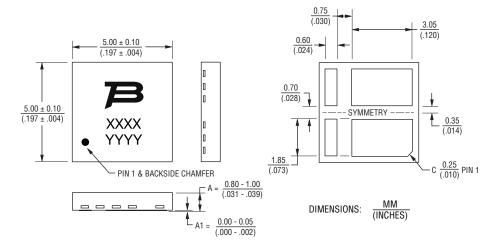
Reflow Profile

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/sec. max.
Preheat - Temperature Min. (Tsmin) - Temperature Max. (Tsmax) - Time (tsmin to tsmax)	150 °C 200 °C 60-180 sec.
Time maintained above: - Temperature (TL) - Time (tL)	217 °C 60-150 sec.
Peak/Classification Temperature (Tp)	260 °C
Time within 5 °C of Actual Peak Temp. (tp)	20-40 sec.
Ramp-Down Rate	6 °C/sec. max.
Time 25 °C to Peak Temperature	8 min. max.



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Product Dimensions

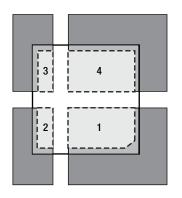


Recommended Pad Layout

TBU® High-Speed Protectors have a 100 % matte-tin termination finish. For improved thermal dissipation, the recommended layout uses PCB copper areas which extend beyond the exposed solder pad. The exposed solder pads should be defined by a solder mask which matches the pad layout of the TBU® device in size and spacing. It is recommended that they should be the same dimension as the TBU® pads but if smaller solder pads are used, they should be centered on the TBU® package terminal pads and not more than 0.10-0.12 mm (0.004-0.005 in.) smaller in overall width or length. Solder pad areas should not be larger than the TBU® pad sizes to ensure adequate clearance is maintained. The recommended

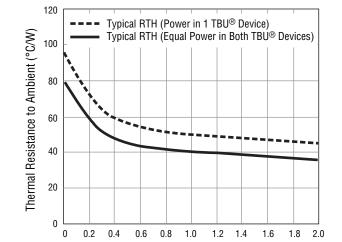
stencil thickness is 0.10-0.12 mm (0.004-0.005 in.) with a stencil opening size 0.025 mm (0.0010 in.) less than the solder pad size. Extended copper areas beyond the solder pad significantly improve the junction to ambient thermal resistance, resulting in operation at lower junction temperatures with a corresponding benefit of reliability. All pads should soldered to the PCB, including pads marked as NC or NU but no electrical connection should be made to these pads. For minimum parasitic capacitance, it is recommended that signal, ground or power signals are not routed beneath any pad.

Thermal Resistance vs. Additional PCB Cu Area



Pad Designation							
Pad #	Pin Out						
1	Line Side 1						
2	Line Load 1						
3	Line Load 2						
4	Line Side 2						

Dark grey areas show added PCB copper area for better thermal resistance.

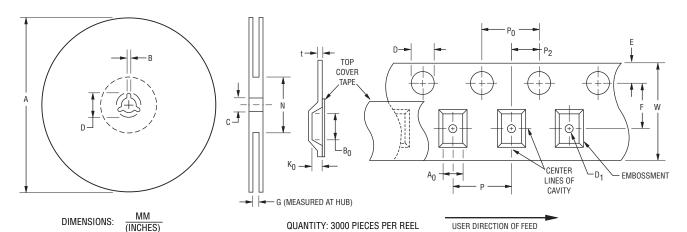


Added Cu Area (Sq. In.)

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TBU - DT 065 - 500 - WH TBU® Product Series DT = Dual Uni-Series Impulse Voltage Rating 065 = 650 V 085 = 850 V Trigger Current 100 = 100 mA 300 = 300 mA 200 = 200 mA 500 = 500 mA Hold to Trip Ratio Suffix W = Hold to Trip Ratio

Packaging Specifications



Min. Max. Min.		A B		С		D		G	N
		Min.	Max.	Min.	Max.	Min.	Max.	Ref.	Ref.
326	330	1.5	2.5	12.8	13.5	20.2		16.5	102
(12.835)	(13.002)	(.059)	(.098)	(.504)	(.531)	(.795)	_	(.650)	(4.016)

A ₀		A0 B0			D		D1		E		F	
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	max.
	5.15	5.35	5.15	5.35	1.5	1.6	1.5		1.65	1.85	5.45	5.55
	(.203)	(.211)	(.203)	(.211)	(.059)	(.063)	(.059)	-	(.065)	(.073)	(.214)	(.218)
K ₀		K ₀ P		P ₀		P2		t		W		
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
	1.0	1.2	7.9	8.1	3.8	4.2	1.95	2.05	0.25	0.35	11.7	12.3
	(.039)	(.047)	(.311)	(.319)	(.150)	(.165)	(.077)	(.081)	(.010)	(.014)	(.461)	(.484)

≥* 5

Asia-Pacific: Tel: +886-2 2562-4117 • Fax: +886-2 2562-4116

EMEA: Tel: +36 88 520 390 • Fax: +36 88 520 211

The Americas: Tel: +1-951 781-5500 • Fax: +1-951 781-5700

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Website:

Welcome to visit www.ameya360.com

Contact Us:

Address:

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

Sales:

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

Customer Service :

Email service@ameya360.com

Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com