



Features

General Description

The MAX2209A is a wideband (800MHz to 2GHz) RF power detector. It takes an RF signal from the directional coupler at the input, and outputs a DC voltage proportional to the RF peak voltage. The change in output voltage versus temperature is very repeatable from part to part and enables a lookup table based on nominal behavior, minimizing the effective detection error to less than ±0.5dB relative to room temperature.

The MAX2209A comes in a space-saving 2 x 2, 0.5mm pitch UCSP™.

- → -25dBm to -5dBm Power Detection Range
- ♦ ±0.5dB Detection Error Due to Temperature
- ♦ +2.7V to +5V Single-Supply Operation
- ◆ Space-Saving 4-Bump, 1mm² UCSP Package
- **♦ Shutdown Control**
- ♦ 140ns Step-Response Time

Applications

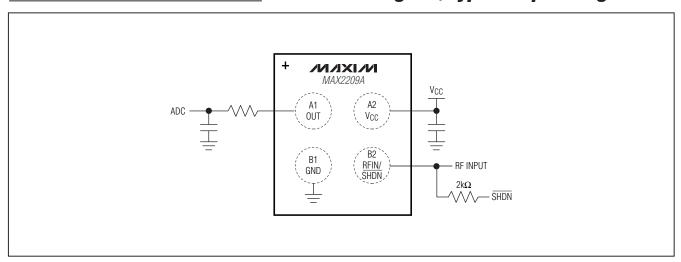
Dual-Band WCDMA Handsets High-Speed Downlink Packet Access (HSDPA) High-Speed Uplink Packet Access (HSUPA)

Ordering Information

PART	TEMP RANGE	PIN- PACKAGE	TOP MARK	
MAX2209AEBS+	-40°C to +85°C	4 UCSP	AGJ	

⁺Denotes a lead(Pb)-free/RoHS-compliant package.

Functional Diagram/Typical Operating Circuit



UCSP is a trademark of Maxim Integrated Products, Inc.

MIXIM

ABSOLUTE MAXIMUM RATINGS

VCC to GND	0.3V to +6V
RFIN to GND	0.3V to $+ (VCC + 0.3V)$
OUT to GND	0.3V to $+ (VCC + 0.3V)$
RFIN Input Power	+10dBm
Continuous Power Dissipation (T	$A = +70^{\circ}C$
4-Bump WLP (derate 3mW/°C	above +70°C)238mW

Junction-to-Ambient Thermal	
Resistance (θJA) (Note 1)	335°C/W
Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +160°C
Junction Temperature	+150°C
Bump Temperature (soldering, Note 2)	
Infrared (15s)	+260°C
Soldering Temperature (reflow)	+240°C

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a 4-layer board. For detailed information on package thermal considerations, refer to www.maxim-ic.com/thermal-tutorial.

Note 2: For detailed information on soldering, refer to Application Note 1891: Wafer-Level Packaging (WLP) and Its Applications.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



DC ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 2.7V \text{ to } 5.0V, \text{ no RF signal applied}, T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$. Typical values are at $V_{CC} = 2.8V, T_A = +25^{\circ}\text{C}$, unless otherwise noted.) (Note 3)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage		2.7		5.0	V
Supply Current			3.9	6	mA
Idle Output Voltage			35		mV
Output Current Source Capability	P _{IN} = -5dBm, V _{OUT} forced 100mV lower than open- circuit output voltage	1000	2300		μΑ
Output Current Sink Capability	P _{IN} = -25dBm, V _{OUT} forced 10mV higher than open- circuit output voltage	75	150		μА
Shutdown Current	VSHDN = 0V		25	50	μΑ
SHDN Logic-High	V _{IH} , including 2kΩ resistor	1.2			V
SHDN Logic-Low	V_{IL} , including $2k\Omega$ resistor			0.45	V
Turn-On Time	SHDN transitions to V _{IH} , V _{OUT} is within 90% of final value (Note 4)		1.5	2	μs
RF Step-Response Time	RF transitions from $<$ -25dBm to -5dBm, V_{OUT} is within 90% of final value, $1k\Omega + 10pF$ load (Note 4)		140	200	ns

AC ELECTRICAL CHARACTERISTICS

 $(50\Omega \text{ system}, \text{VCC} = 2.8 \text{V}, \text{TA} = -40 ^{\circ}\text{C} \text{ to } +85 ^{\circ}\text{C}. \text{ Typical values are at TA} = +25 ^{\circ}\text{C}, \text{ unless otherwise noted.})$ (Note 3)

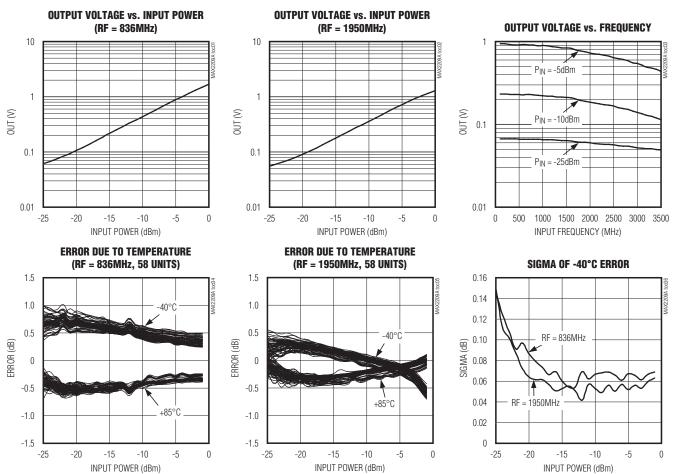
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS	
RF Input Frequency		800		2000	MHz	
DE Input Datum Lago	800MHz		16		dB	
RF Input Return Loss	2000MHz		9			
Output Valtage 920ML	-5dBm input		0.88		V	
Output Voltage, 836MHz	-25dBm input		0.06			
Output Valtage 1050MHz	-5dBm input		0.72		V	
Output Voltage, 1950MHz	-25dBm input		0.06		V	
Residual Error after Room	-5dBm input			±0.5	dB	
Temperature Calibration (T _A = -40°C to +85°C) (Note 4)	-25dBm input			±1.5	ub	

Note 3: Guaranteed by production test at $T_A = +25$ °C. Guaranteed by design and characterization at $T_A = -40$ °C and $T_A = +85$ °C.

Note 4: Guaranteed by design and characterization. See the Typical Operating Characteristics.

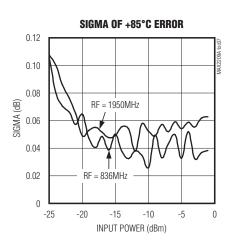
Typical Operating Characteristics

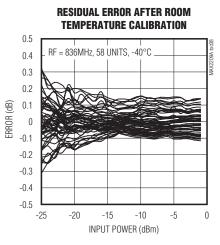
($V_{CC} = 2.8V$. Typical values are at $T_A = +25$ °C, unless otherwise noted.)

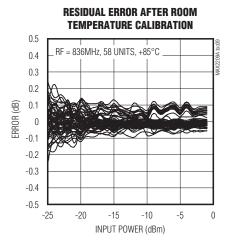


Typical Operating Characteristics (continued)

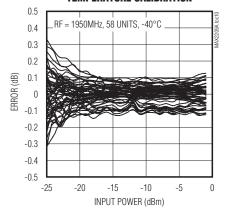
($V_{CC} = 2.8V$. Typical values are at $T_A = +25$ °C, unless otherwise noted.)



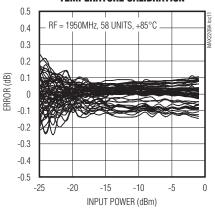




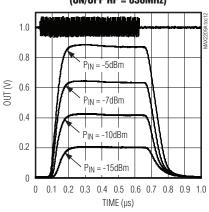
RESIDUAL ERROR AFTER ROOM TEMPERATURE CALIBRATION



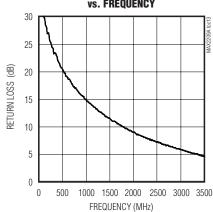
RESIDUAL ERROR AFTER ROOM TEMPERATURE CALIBRATION



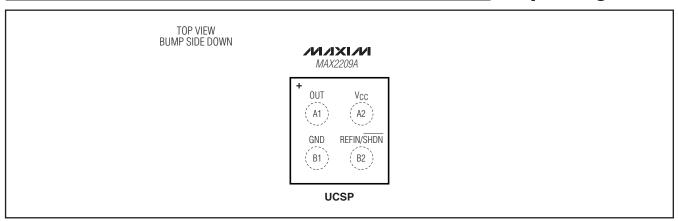
SETTLING TIME FROM RF POWER (ON/OFF RF = 836MHz)



RF RETURN LOSS vs. FREQUENCY



Bump Configuration



Bump Description

BUMP	NAME	FUNCTION		
A1	OUT	Detector Output		
A2	Vcc	Power Supply. Bypass to GND with a capacitor as close as possible to the bump.		
B1	GND	Ground Connection. Connect to PCB ground plane with as low inductance as possible.		
B2 RFIN/SHDN		RF Input and Shutdown Control. AC-couple the RF input and DC couple the shutdown control through a $2k\Omega$ resistor to this pin.		

Detailed Description

The MAX2209A power detector is designed to operate from 800MHz to 2.0GHz. The device is ideal for wideband code-division multiple access (WCDMA), cdma2000®, and high-speed downlink/uplink packet access. The MAX2209A accepts an RF signal at the input, and outputs a temperature-independent voltage related to the input signal power. The output voltage expressed in dBV is proportional to the input power expressed in dBm. The device has a detection range from -25dBm to 0dBm.

_Applications Information

The typical application circuit, as taken from the MAX2209A EV Kit, is shown in Figure 1. The IC can be shut down by forcing the RFIN/SHDN DC voltage low through a $2k\Omega$ resistor. The output of the detector goes to an ADC for further processing by the baseband system. Connect a series resistor and shunt capacitor to the MAX2209A output to reduce residual amplitude ripple. The series resistor should not be less than $1k\Omega$.

EV kit gerber files, schematic, BOM, and updates are available on the MAX2209A product page at Maxim's website (www.maxim-ic.com).

Layout

There are two areas that require attention: the GND pin and the supply bypassing. Connect the GND pin to the PCB ground with a GND via as close as possible, and bypass VCC to ground with a capacitor as close as possible to the part.

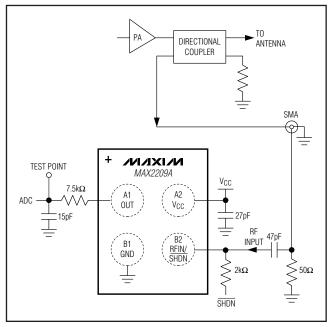


Figure 1. Typical Application Circuit from MAX2209A EV Kit

_Chip Information

PROCESS: BIPOLAR

_Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE	PACKAGE	OUTLINE	LAND
TYPE	CODE	NO.	PATTERN NO.
4 UCSP	B4+4	21-0007	

cdma2000 is a registered trademark of the Telecommunications Industry Association.

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	
0	9/10	Initial release	_

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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Website:

Welcome to visit www.ameya360.com

Contact Us:

> Address:

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

> Sales:

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

Customer Service :

Email service@ameya360.com

Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com