

The Art of Embedded Systems Development – made Easy™

Document status: Preliminary

LPC2478-32 OEM Board Feature Highlights

The LPC2478-32 OEM Board provides a guick and easy solution for implementing a high-performance ARM7TDMI based design around the LPC2478 from NXP.

- Build around NXP's ARM7TDMI LPC2478 microcontroller with 512Kbyte internal FLASH and 96Kbyte internal SRAM
- 32MByte external SDRAM, via 32-bit databus
- 128 Mbyte NAND FLASH, via 8-bit databus
- 4 MByte NOR FLASH, via 16-bit databus
- 100/10Mbps Ethernet interface based on DP83848 ETH-PHY
- 12.000 MHz and 32.768 kHz crystals for LPC2478
- 32Kbyte I2C E2PROM for storing non-volatile parameters
- Buffered 32-bit data bus for external expansion
- +3.3V powering
- 200 pos expansion connector (as defined in popular SO-DIMM industry standard), 0.6mm pitch
- Compact design with dimensions: 68 x 48 mm



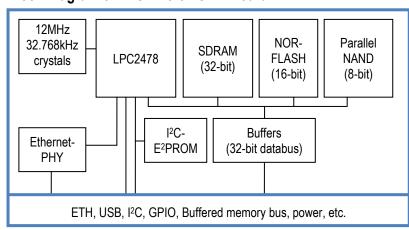
- Access to Embedded Artists support page containing
 - **Schematics**
 - User's Manual 0
 - Sample software applications
 - **OEM Board Integration Guide**
- Supported by Developer's Kit
- Volume discount available
- Customization service available for optimized high-volume design

Block Diagram of LPC2478-32 OEM Board

Embedded Artists AB

Davidshallsgatan 16

SE-211 45 Malmö



NXP Partner

Embedded Artists is a partner of NXP. Together we give engineers an excellent base to work from when creating advanced embedded systems. We have a close co-operation and know everything there is to know about the NXP processors. Take advantage of our unique knowledge! For further information, please contact: support@EmbeddedArtists.com





ă ă ă ă ă ă



The Art of Embedded Systems Development – made Easy™

Document status: Preliminary

Absolute Maximum Ratings

Parameter	Rating
VDD to GND (Supply voltage)	-0.5V to +3.6V
General for Digital/Analog Input/Output Voltage	-0.5V to VDD+0.5V
+5V tolerant pins on LPC2478	-0.5V to +6.0V (see LPC2478 DS for details)
Storage temperature	-40°C to 100°C

Stress above these limits may cause permanent damage to the board.

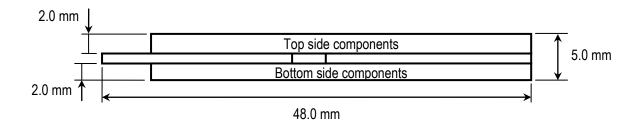
Technical Data

Parameter	Min	Typical	Max
Supply voltage	3.10V	3.30V	3.50V
Ripple with frequency contents < 100kHz			50mV
Ripple with frequency contents ≥ 100kHz			10mV
Supply current			
- power down mode (12 MHz)		30 mA	
- executing from internal FLASH (72MHz)		185 mA	
- executing from external SDRAM (72MHz)		315 mA	
VBAT current		TBD	
Operating temperature ^[1]			
- 66 MHz core frequency	0°C		70°C
- 72 MHz core frequency	10°C		40°C
Relative Humidity (RH)			
$0^{\circ}\text{C} < \text{T}_{A} \le 50^{\circ}\text{C}$, non-condensing	5%		80%
50°C < T _A ≤ 60°C, non-condensing	5%		50%
$60^{\circ}\text{C} < T_A \le 70^{\circ}\text{C}$, non-condensing	5%		35%

^[1] Extended temperature range can be supplied on request. Subject to minimum order volume.

Mechanical Dimensions

Board width according to SO-DIMM standard: 67.6 mm. Board height (top and bottom) according to picture below:



ESD CAUTION

SE-211 45 Malmö

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features ESD protection damages may occur on devices subjected to high energy ESD. Therefore, proper ESD precaution should be taken to avoid performance degradation or loss of functionality.







The Art of Embedded Systems Development – made Easy™

Document status: Preliminary

Pin Information

Pin Information			
SO- DIMM pins	I/O, Application Details	Connected to	
1	A, Ethernet TXP	Ethernet-PHY	
2	A, Ethernet RXP	Ethernet-PHY	
3	A, Ethernet TXN	Ethernet-PHY	
4	A, Ethernet RXP	Ethernet-PHY	
5	P, VDD3_3A		
6	P, GND		
7	OD, ETH-LED1	Ethernet-PHY	
8	OD, ETH-LED2	Ethernet-PHY	
9	P, VBAT-IN	LPC2478, vbat	
10	O, RTC-ALARM	LPC2478, rtc-alarm	
11	I, RESET-IN	LPC2478, rst-in	
12	O, RESET-OUT	LPC2478, rst-out	
13	1	ETH-PHY, power down	
14	1	LPC2478, dbgen	
15	O, TCK	LPC2478, tck	
16	O, RTCK	LPC2478, rtck	
17	I, TRST	LPC2478, trst	
18	I, TMS	LPC2478, tms	
19	I, TDI	LPC2478, tdi	
20	O, TDO	LPC2478, tdo	
21	P, V3A	LPC2478, v3a	
22	P, VREF	LPC2478, vref	
23	P, VSSA	LPC2478, vssa	
24	P, GND		
25	B, GPIO	LPC2478, P2.0	
26	B, GPIO	LPC2478, P2.1	
27	B, GPIO	LPC2478, P2.2	
28	B, GPIO	LPC2478, P2.3	
29	B, GPIO	LPC2478, P2.4	
30	B, GPIO	LPC2478, P2.5	
31	B, GPIO	LPC2478, P2.6	
32	B, GPIO	LPC2478, P2.7	
33	B, GPIO	LPC2478, P2.8	
34	B, GPIO	LPC2478, P2.9	
35	B, GPIO	LPC2478, P2.10	
36	B, GPIO	LPC2478, P2.11	
37	P, VCC		
38	P, GND		
39 40	P, VCC		
41	P, GND	LDC2478 D0 20	
41	A, USBA-DP A, USBB-DP	LPC2478, P0.29 LPC2478, P0.31	
42	A, USBA-DM	LPC2478, P0.31 LPC2478, P0.30	
43	A, USBB-DM	LPC2478, P0.30 LPC2478, USBB-DM	
44	B, GPIO	LPC2478, USBB-DW LPC2478, P2.12	
46	B, GPIO	LPC2478, P2.12 LPC2478, P2.13	
40	B, GPIO	LPC2476, P2.13 LPC2478. P0.0	
48	B, GPIO	LPC2478, P0.1	
49	B, GPIO	LPC2478, P0.2	
50	B, GPIO	LPC2478, P0.3	
51	B, GPIO	LPC2478, P0.4	
52	B, GPIO	LPC2478, P0.5	
52	D, OP 10	L DO0470 D0 C	

SO- DIMM pins	I/O, Application Details	Connected to
101	P, GND	
102	P, GND	
103	NC	
104	NC	
105	NC	
106	NC	
107	NC	
108	NC	
109	NC	
110	NC	
111	NC	
112	NC	
113	NC	
114	NC	
115	NC	
116	B, GPIO	LPC2478, P2.14
		LPC2478, P2.15
117	B, GPIO	LPC2478, P2.19
118	B, GPIO	·
119	B, GPIO	LPC2478, P2.21
120	B, GPIO	LPC2478, P2.22
121	B, GPIO	LPC2478, P2.23
122	B, GPIO	LPC2478, P2.25
123	B, GPIO	LPC2478, P2.26
124	B, GPIO	LPC2478, P2.27
125	NC	
126	NC	
127	B, GPIO	LPC2478, P4.28
128	B, GPIO	LPC2478, P4.29
129	P, GND	
130	P, GND	
131	O, Buffered Address bus 15	LPC2478, P4.15 via buffer
132	O, Buffered DQM1	LPC2478, P2.29 via buffer
133	O, Buffered Address bus 14	LPC2478, P4.14 via buffer
134	O, Buffered DQM0	LPC2478, P2.28 via buffer
135	O, Buffered Address bus 13	LPC2478, P4.13 via buffer
136	O, Buffered BCAS	LPC2478, P2.16 via buffer
137	O, Buffered Address bus 12	LPC2478, P4.12 via buffer
138	O, Buffered BRAS	LPC2478, P2.17 via buffer
139	O, Buffered Address bus 11	LPC2478, P4.11 via buffer
140	O, Buffered BLS1	LPC2478, P4.27 via buffer
141	O, Buffered Address bus 10	LPC2478, P4.10 via buffer
142	O, Buffered BLS0	LPC2478, P4.26 via buffer
143	O, Buffered Address bus 9	LPC2478, P4.9 via buffer
144	O, Buffered WE	LPC2478, P4.25 via buffer
145	O, Buffered Address bus 8	LPC2478, P4.8 via buffer
146	O. Buffered OE	LPC2478, P4.24 via buffer
147	O, Buffer Address bus 7	LPC2478, P4.7 via buffer
148	O, Buffer Address bus 23	LPC2478, P4.23 via buffer
149	O, Buffer Address bus 6	LPC2478, P4.6 via buffer
150	O, Buffer Address bus 22	LPC2478, P4.22 via buffer
151	O, Buffer Address bus 5	LPC2478, P4.5 via buffer
152	O, Buffer Address bus 21	LPC2478, P4.21 via buffer
153	O, Buffer Address bus 4	LPC2478, P4.4 via buffer
133	O, Dullel Address bus 4	LI OZTIO, I T.T VIA DUIIGI

B, GPIO

Embedded Artists AB

Davidshallsgatan 16 SE-211 45 Malmö

53

LPC2478, P0.6



The Art of Embedded Systems Development – made Easy™

Document status: Preliminary

85	B, GPIO	LPC2478, P1.13
84	B, GPIO	LPC2478, P1.12
83	B, GPIO	LPC2478, P1.11
82	B, GPIO	LPC2478, P1.7
81	B, GPIO	LPC2478, P1.6
80	B, GPIO	LPC2478, P1.5
79	B, GPIO	LPC2478, P1.3
78	B, GPIO	LPC2478, P1.2
77	P, GND	L DO0470 D4 0
76	P, GND	LI 02470, I 0.20
75	B, I2C-SCL	LPC2478, P0.28
74	B, I2C-SDA	LPC2478, P0.27
73	B, GPIO	LPC2478, P0.26
72	B, GPIO	LPC2478, P0.25
		·
71	B, GPIO	LPC2478, P0.24
70	B, GPIO	LPC2478, P0.23
69	B, GPIO	LPC2478, P0.22
68	B, GPIO	LPC2478, P0.21
67	B, GPIO	LPC2478, P0.20
66	B, GPIO	LPC2478, P0.19
65	B, GPIO	LPC2478, P0.18
	,	· ·
64	B. GPIO	LPC2478, P0.17
63	B, GPIO	LPC2478, P0.16
62	B. GPIO	LPC2478, P0.15
61	B, GPIO	LPC2478, P0.14
60	B, GPIO	LPC2478, P0.13
59	B, GPIO	LPC2478, P0.12
58	B, GPIO	LPC2478, P0.11
57	B, GPIO	LPC2478, P0.10
56	B, GPIO	LPC2478, P0.9
55	B, GPIO	LPC2478, P0.8
54	B, GPIO	LPC2478, P0.7
ΕΛ	D CDIO	LDC2479 D0 7

I/O leg	end
O: outp	ut
I: input	

B: Bidirectional

Embedded Artists AB Davidshallsgatan 16 SE-211 45 Malmö Sweden

P: Power A: Analog

154	O, Buffer Address bus 20	LPC2478, P4.20 via buffer
155	O, Buffer Address bus 3	LPC2478, P4.3 via buffer
156	O, Buffer Address bus 19	LPC2478, P4.19 via buffer
157	O, Buffer Address bus 2	LPC2478, P4.2 via buffer
158	O, Buffer Address bus 18	LPC2478, P4.18 via buffer
159	O, Buffer Address bus 1	LPC2478, P4.1 via buffer
160	O, Buffer Address bus 17	LPC2478, P4.17 via buffer
161	O, Buffer Address bus 0	LPC2478, P4.0 via buffer
162	O, Buffer Address bus 16	LPC2478, P4.16 via buffer
163	I, DBUS_EN	Enable data bus buffers
164	I, ABUF EN	Enable address buffers
165	P, Buffer-VCC	Enable address ballers
166	P, GND	
167	B, Buffer Data bus 15	LPC2478, P3.15 via buffer
168	B, Buffer Data bus 31	LPC2478, P3.31 via buffer
169	B, Buffer Data bus 14	LPC2478, P3.14 via buffer
170	B, Buffer Data bus 30	LPC2478, P3.30 via buffer
-	·	LPC2478, P3.13 via buffer
171	B, Buffer Data bus 13	LPC2478, P3.13 via buffer
172	B, Buffer Data bus 29	
173	B, Buffer Data bus 12	LPC2478, P3.12 via buffer LPC2478, P3.28 via buffer
174	B, Buffer Data bus 28	*
175	B, Buffer Data bus 11	LPC2478, P3.11 via buffer
176	B, Buffer Data bus 27	LPC2478, P3.27 via buffer
177	B, Buffer Data bus 10	LPC2478, P3.10 via buffer
178	B, Buffer Data bus 26	LPC2478, P3.26 via buffer
179	B, Buffer Data bus 9	LPC2478, P3.9 via buffer
180	B, Buffer Data bus 25	LPC2478, P3.25 via buffer
181	B, Buffer Data bus 8	LPC2478, P3.8 via buffer
182	B, Buffer Data bus 24	LPC2478, P3.24 via buffer
183	B, Buffer Data bus 7	LPC2478, P3.7 via buffer
184	B, Buffer Data bus 23	LPC2478, P3.23 via buffer
185	B, Buffer Data bus 6	LPC2478, P3.6 via buffer
186	B, Buffer Data bus 22	LPC2478, P3.22 via buffer
187	B, Buffer Data bus 5	LPC2478, P3.5 via buffer
188	B, Buffer Data bus 21	LPC2478, P3.21 via buffer
189	B, Buffer Data bus 4	LPC2478, P3.4 via buffer
190	B, Buffer Data bus 20	LPC2478, P3.20 via buffer
191	B, Buffer Data bus 3	LPC2478, P3.3 via buffer
192	B, Buffer Data bus 19	LPC2478, P3.19 via buffer
193	B, Buffer Data bus 2	LPC2478, P3.2 via buffer
194	B, Buffer Data bus 18	LPC2478, P3.18 via buffer
195	B, Buffer Data bus 1	LPC2478, P3.1 via buffer
196	B, Buffer Data bus 17	LPC2478, P3.17 via buffer
197	B, Buffer Data bus 0	LPC2478, P3.0 via buffer
198	B, Buffer Data bus 16	LPC2478, P3.16 via buffer
199	P, Buffer-VCC	
200	P, GND	
0.0	<u> </u>	

OD: Open-drain output GPIO: General purpose I/O GPI: General purpose input GPO: General purpose output





The Art of Embedded Systems Development – made Easy™

Document status: Preliminary

Disclaimers

Embedded Artists reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Customer is responsible for the design and operation of their applications and products using Embedded Artists' products, and Embedded Artists accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Embedded Artists' product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Embedded Artists does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Embedded Artists' products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Embedded Artists does not accept any liability in this respect.

Embedded Artists does not accept any liability for erratas on individual components.

All Embedded Artists' products are sold pursuant to Embedded Artists' terms and conditions of sale: http://www.embeddedartists.com/sites/default/files/docs/General_Terms_and_Conditions.pdf

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by Embedded Artists for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN EMBEDDED ARTISTS' TERMS AND CONDITIONS OF SALE EMBEDDED ARTISTS DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF EMBEDDED ARTISTS PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY THE CEO OF EMBEDDED ARTISTS, PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, NUCLEAR, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE.

Resale of Embedded Artists' products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by Embedded Artists for the Embedded Artists' product or service described herein and shall not create or extend in any manner whatsoever, any liability of Embedded Artists.

This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Definition of Document Status

Embedded Artists AB

Davidshallsgatan 16

SE-211 45 Malmö

Preliminary – The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Embedded Artists does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information. The document is in this state until the product has passed Embedded Artists product qualification tests.

Approved – The information and data provided define the specification of the product as agreed between Embedded Artists and its customer, unless Embedded Artists and customer have explicitly agreed otherwise in writing.



AMEYA360 Components Supply Platform

Authorized Distribution Brand:

























Website:

Welcome to visit www.ameya360.com

Contact Us:

> Address:

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd Minhang District, Shanghai , China

Sales:

Direct +86 (21) 6401-6692

Email amall@ameya360.com

QQ 800077892

Skype ameyasales1 ameyasales2

Customer Service :

Email service@ameya360.com

Partnership :

Tel +86 (21) 64016692-8333

Email mkt@ameya360.com