

## 3Gbps HD/SD SDI Cable Driver

Check for Samples: [LMH0302](#)

### FEATURES

- SMPTE 424M, SMPTE 292M, SMPTE 344M, and SMPTE 259M Compliant
- Data Rates to 2.97 Gbps
- Supports DVB-ASI at 270 Mbps
- Differential Input
- 75Ω Differential Output
- Selectable Slew Rate
- Output Driver Power Down Control
- Single 3.3V Supply Operation
- Industrial Temperature Range: –40°C to +85°C
- Typical Power Consumption: 125 mW in SD Mode and 165 mW in HD Mode
- 16-pin WQFN Package
- Footprint Compatible With the LMH0002SQ
- Replaces the Gennum GS2978

### DESCRIPTION

The LMH0302 3Gbps HD/SD SDI Cable Driver is designed for use in SMPTE 424M, SMPTE 292M, SMPTE 344M, and SMPTE 259M serial digital video applications. The LMH0302 drives 75Ω transmission lines (Belden 1694A, Belden 8281, or equivalent) at data rates up to 2.97 Gbps.

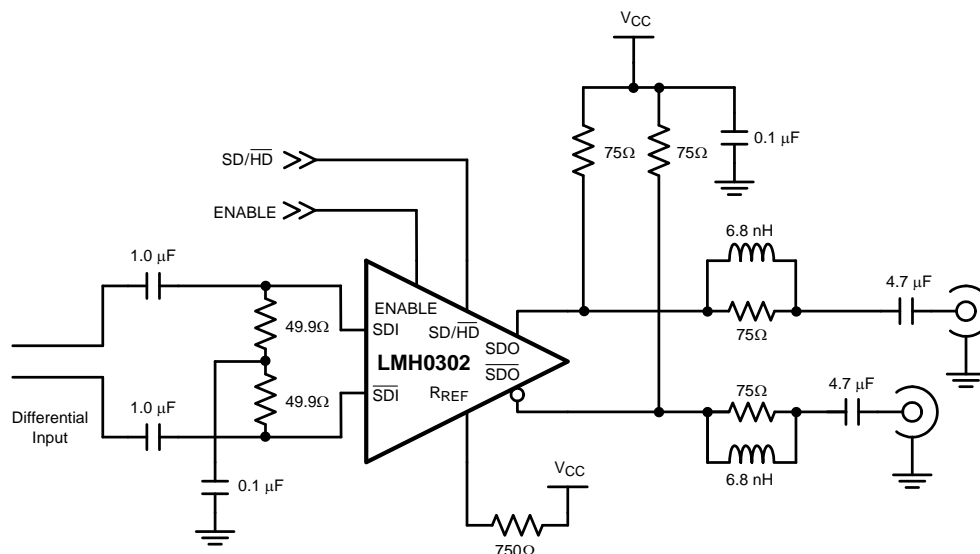
The LMH0302 provides two selectable slew rates for SMPTE 259M and SMPTE 424M / 292M compliance. The output driver may be powered down via the output driver enable pin.

The LMH0302 is powered from a single 3.3V supply. Power consumption is typically 125 mW in SD mode and 165 mW in HD mode. The LMH0302 is available in a 16-pin WQFN package.

### APPLICATIONS

- SMPTE 424M, SMPTE 292M, SMPTE 344M, and SMPTE 259M Serial Digital Interfaces
- Digital Video Routers and Switches
- Distribution Amplifiers

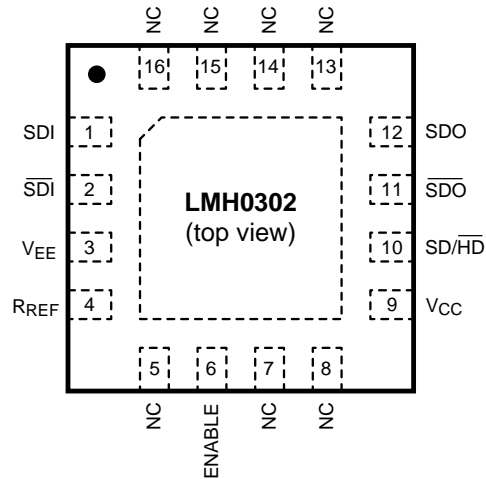
### Typical Application



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## Connection Diagram



The exposed die attach pad is a negative electrical terminal for this device. It should be connected to the negative power supply voltage.

**Figure 1. 16-Pin WQFN  
See Package Number RUM**

### PIN DESCRIPTIONS

Pin	Name	Description
1	SDI	Serial data true input.
2	$\overline{\text{SDI}}$	Serial data complement input.
3	$V_{EE}$	Negative power supply (ground).
4	$R_{REF}$	Output driver level control. Connect a resistor to $V_{CC}$ to set output voltage swing.
5	NC	No connect. Not bonded internally.
6	ENABLE	Output driver enable. When low, the $\text{SDO}/\overline{\text{SDO}}$ output driver is powered off. ENABLE has an internal pullup.
7	NC	No connect. Not bonded internally.
8	NC	No connect. Not bonded internally.
9	$V_{CC}$	Positive power supply (+3.3V).
10	$\text{SD}/\overline{\text{HD}}$	Output slew rate control. Output rise/fall time complies with SMPTE 424M / 292M when low and SMPTE 259M when high.
11	$\overline{\text{SDO}}$	Serial data complement output.
12	SDO	Serial data true output.
13	NC	No connect. Not bonded internally.
14	NC	No connect. Not bonded internally.
15	NC	No connect. Not bonded internally.
16	NC	No connect. Not bonded internally.
DAP	$V_{EE}$	Connect exposed DAP to negative power supply (ground).



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### Absolute Maximum Ratings<sup>(1)</sup>

Supply Voltage		-0.5V to 3.6V
Input Voltage (all inputs)		-0.3V to $V_{CC}+0.3V$
Output Current		28 mA
Storage Temperature Range		-65°C to +150°C
Junction Temperature		+125°C
Lead Temperature (Soldering 4 Sec)		+260°C
Package Thermal Resistance	$\theta_{JA}$ 16-pin WQFN	+58°C/W
	$\theta_{JC}$ 16-pin WQFN	+21°C/W
ESD Rating	HBM	4.5 kV
	MM	250V
	CDM	2 kV

- (1) "Absolute Maximum Ratings" are those parameter values beyond which the life and operation of the device cannot be ensured. The stating herein of these maximums shall not be construed to imply that the device can or should be operated at or beyond these values. The table of [Electrical Characteristics](#) specifies acceptable device operating conditions.

### Recommended Operating Conditions

Supply Voltage ( $V_{CC} - V_{EE}$ )	3.3V ±5%
Operating Free Air Temperature ( $T_A$ )	-40°C to +85°C

### DC Electrical Characteristics

Over Supply Voltage and Operating Temperature ranges, unless otherwise specified <sup>(1)</sup> <sup>(2)</sup>.

Symbol	Parameter	Conditions	Reference	Min	Typ	Max	Units
$V_{CMIN}$	Input Common Mode Voltage		SDI, $\overline{SDI}$	$1.1 + V_{SDI}/2$		$V_{CC} - V_{SDI}/2$	V
$V_{SDI}$	Input Voltage Swing	Differential		100		2200	mV <sub>P-P</sub>
$V_{CMOUT}$	Output Common Mode Voltage		SDO, $\overline{SDO}$		$V_{CC} - V_{SDO}$		V
$V_{SDO}$	Output Voltage Swing	Single-ended, 75Ω load, $R_{REF} = 750\Omega$ 1%		720	800	880	mV <sub>P-P</sub>
$V_{IH}$	Input Voltage High Level		SD/ $\overline{HD}$ , ENABLE	2.0			V
$V_{IL}$	Input Voltage Low Level					0.8	V
$I_{CC}$	Supply Current	SD/ $\overline{HD} = 0$ , SDO/ $\overline{SDO}$ enabled			50	59	mA
		SD/ $\overline{HD} = 0$ , SDO/ $\overline{SDO}$ disabled			26	33	mA
		SD/ $\overline{HD} = 1$ , SDO/ $\overline{SDO}$ enabled			38	48	mA
		SD/ $\overline{HD} = 1$ , SDO/ $\overline{SDO}$ disabled			15	22	mA

- (1) Current flow into device pins is defined as positive. Current flow out of device pins is defined as negative. All voltages are stated referenced to  $V_{EE} = 0$  Volts.  
 (2) Typical values are stated for  $V_{CC} = +3.3V$  and  $T_A = +25^\circ C$ .

## AC Electrical Characteristics

Over Supply Voltage and Operating Temperature ranges, unless otherwise specified <sup>(1)</sup>.

Symbol	Parameter	Conditions	Reference	Min	Typ	Max	Units
DR <sub>SDI</sub>	Input Data Rate		SDI, $\overline{\text{SDI}}$			2970	Mbps
t <sub>jit</sub>	Additive Jitter	2.97 Gbps	SDO, $\overline{\text{SDO}}$		20		pSp-p
		1.485 Gbps			18		pSp-p
		270 Mbps			15		pSp-p
t <sub>r</sub> , t <sub>f</sub>	Output Rise Time, Fall Time	SD/ $\overline{\text{HD}}$ = 0, 20% – 80%,			90	130	ps
		SD/ $\overline{\text{HD}}$ = 1, 20% – 80%		400		800	ps
	Mismatch in Rise/Fall Time	SD/ $\overline{\text{HD}}$ = 0				30	ps
		SD/ $\overline{\text{HD}}$ = 1				50	ps
	Duty Cycle Distortion	SD/ $\overline{\text{HD}}$ = 0, 2.97 Gbps <sup>(2)</sup>				27	ps
		SD/ $\overline{\text{HD}}$ = 0, 1.485 Gbps <sup>(2)</sup>				30	ps
		SD/ $\overline{\text{HD}}$ = 1 <sup>(2)</sup>				100	ps
t <sub>OS</sub>	Output Overshoot	SD/ $\overline{\text{HD}}$ = 0 <sup>(2)</sup>				10	%
		SD/ $\overline{\text{HD}}$ = 1 <sup>(2)</sup>				8	%
RL <sub>SDO</sub>	Output Return Loss	5 MHz - 1.5 GHz <sup>(3)</sup>		15			dB
		1.5 GHz - 3.0 GHz <sup>(3)</sup>		10			dB

(1) Typical values are stated for V<sub>CC</sub> = +3.3V and T<sub>A</sub> = +25°C.

(2) Specification is ensured by characterization.

(3) Output return loss is dependent on board design. The LMH0302 meets this specification on the SD302 evaluation board.

## DEVICE OPERATION

### INPUT INTERFACING

The LMH0302 accepts either differential or single-ended input. The inputs are self-biased, allowing for simple AC or DC coupling. DC-coupled inputs must be kept within the specified common-mode range.

### OUTPUT INTERFACING

The LMH0302 uses current mode outputs. Single-ended output levels are 800 mV<sub>P-P</sub> into 75Ω AC-coupled coaxial cable with an R<sub>REF</sub> resistor of 750Ω. The R<sub>REF</sub> resistor is connected between the R<sub>REF</sub> pin and V<sub>CC</sub>. The only resistor value that should be used for R<sub>REF</sub> is 750Ω.

The R<sub>REF</sub> resistor should be placed as close as possible to the R<sub>REF</sub> pin. In addition, the copper in the plane layers below the R<sub>REF</sub> network should be removed to minimize parasitic capacitance.

### OUTPUT SLEW RATE CONTROL

The LMH0302 output rise and fall times are selectable for either SMPTE 259M or SMPTE 424M / 292M compliance via the SD/HD pin. For slower rise and fall times, or SMPTE 259M compliance, SD/HD is set high. For faster rise and fall times, or SMPTE 424M and SMPTE 292M compliance, SD/HD is set low.

### OUTPUT ENABLE

The SDO/ $\overline{\text{SDO}}$  output driver can be enabled or disabled with the ENABLE pin. When set low, the output driver is powered off. ENABLE has an internal pullup.

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**REVISION HISTORY**

<b>Changes from Revision F (April 2013) to Revision G</b>	<b>Page</b>
<hr/> <ul style="list-style-type: none"><li>• Changed layout of National Data Sheet to TI format .....</li></ul>	<hr/> <b>4</b>

## PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
LMH0302SQ/NOPB	ACTIVE	WQFN	RUM	16	1000	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR	-40 to 85	L0302	<a href="#">Samples</a>
LMH0302SQE/NOPB	ACTIVE	WQFN	RUM	16	250	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR	-40 to 85	L0302	<a href="#">Samples</a>
LMH0302SQX/NOPB	ACTIVE	WQFN	RUM	16	4500	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR	-40 to 85	L0302	<a href="#">Samples</a>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBsolete:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.

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**TAPE AND REEL INFORMATION**

**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**


\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LMH0302SQ/NOPB	WQFN	RUM	16	1000	178.0	12.4	4.3	4.3	1.3	8.0	12.0	Q1
LMH0302SQE/NOPB	WQFN	RUM	16	250	178.0	12.4	4.3	4.3	1.3	8.0	12.0	Q1
LMH0302SQX/NOPB	WQFN	RUM	16	4500	330.0	12.4	4.3	4.3	1.3	8.0	12.0	Q1

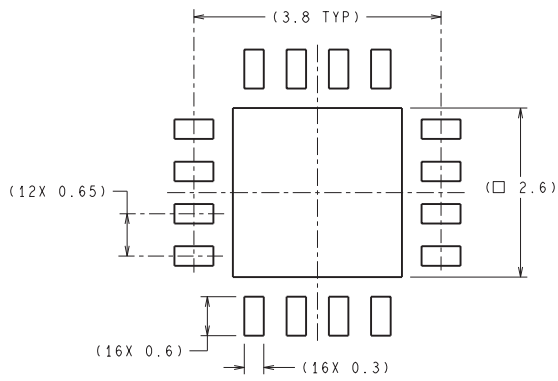
**TAPE AND REEL BOX DIMENSIONS**


\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LMH0302SQ/NOPB	WQFN	RUM	16	1000	213.0	191.0	55.0
LMH0302SQE/NOPB	WQFN	RUM	16	250	213.0	191.0	55.0
LMH0302SQX/NOPB	WQFN	RUM	16	4500	367.0	367.0	35.0

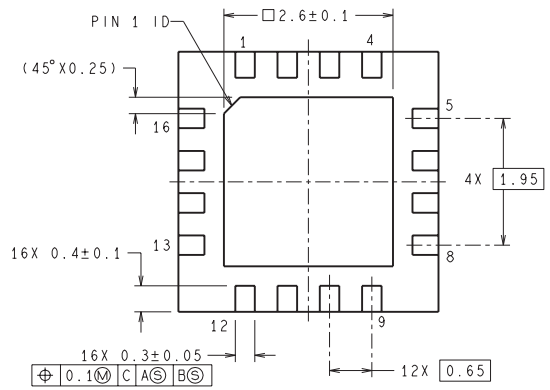
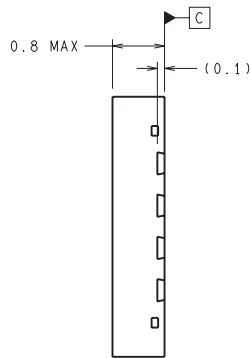
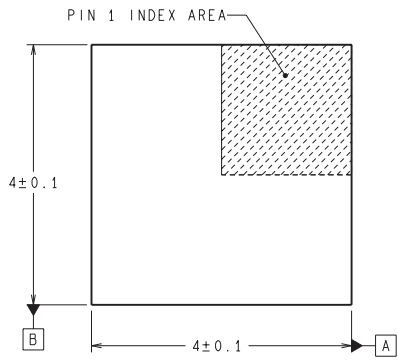
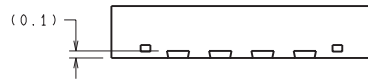


RUM0016A



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