



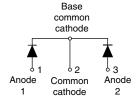
Vishay High Power Products

COMPLIANT

Schottky Rectifier New Generation 3 D-61 Package, 2 x 55 A

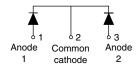
VS-110CNQ045APbF





VS-110CNQ045ASMPbF

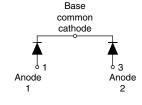




D-61-8-SM

VS-110CNQ045ASLPbF





D-01-0-3L

PRODUCT SUMMARY				
I _{F(AV)}	2 x 55 A			
V _R	45 V			

FEATURES

- 150 °C T_J operation
- Center tap module
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	110	А		
V _{RRM}		45	V		
I _{FSM}	t _p = 5 μs sine	5400	А		
V _F	55 Apk, T _J = 125 °C (per leg)	0.5	V		
T _J	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-110CNQ045APbF	UNITS	
Maximum DC reverse voltage	V_{R}	45	V	
Maximum working peak reverse voltage	V_{RWM}	45	V	

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^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

VS-110CNQ045A PbF Series



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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per le	· .	(AV) 50 % duty cycle at T _C = 125 °C, rectangular waveform		55	Α
See fig. 5 per device	e I _{F(AV)}			110	A
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	5400	
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	800	Α
on-repetitive avalanche energy per leg E_{AS} $T_J = 25$ °C, $I_{AS} = 8$ A, L = 1.7 mH		54	mJ		
Repetitive avalanche current per leg	Deer leg I_{AR} Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		8	А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	55 A	- T _J = 25 °C	0.54	V
		110 A		0.7	
		55 A	T _J = 125 °C	0.5	
		110 A		0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3	mΛ
See fig. 2	IRM (17	T _J = 125 °C		350	mA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz), 25 °C		3800	pF
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body		5.5	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _k		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	ge	T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg			DC operation See fig. 4	0.5		
Maximum thermal resistance junction to case per package	•	R _{thJC}	DC operation	0.25	°C/W	
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approvimento vicialet				7.8	g	
Approximate weight				0.28	OZ.	
Mounting torque	minimum			40 (35)	kgf · cm	
(D-61-8 only) r	maximum			58 (50)	(lbf · in)	
			Case style D-61	110CN	Q045A	
Marking device			Case style D-61-8-SM	110CNQ	110CNQ045ASM	
			Case style D-61-8-SL	110CNQ	045ASL	

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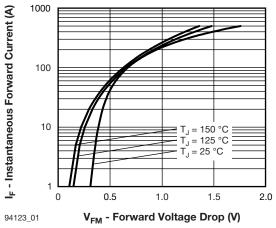


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

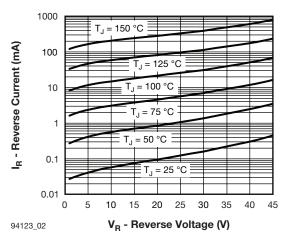


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

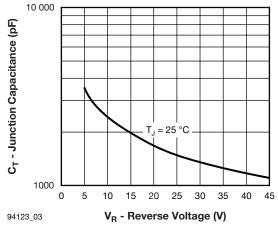


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

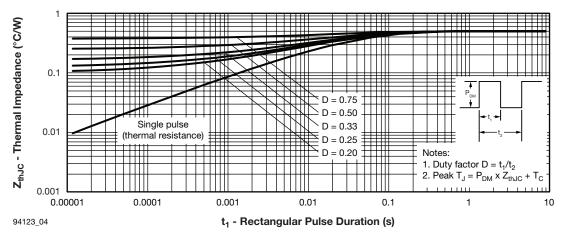


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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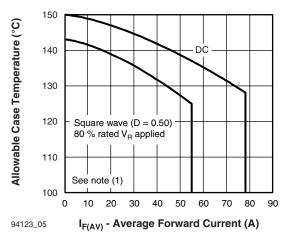


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

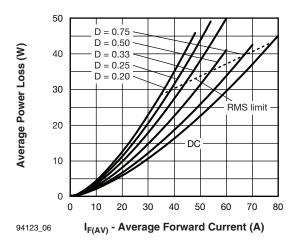


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

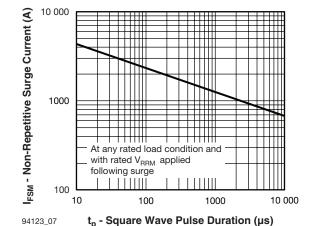


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

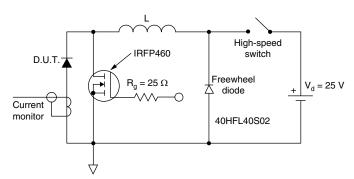


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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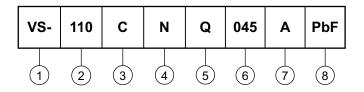


VS-110CNQ045A PbF Series

Schottky Rectifier Vishay High Power Products New Generation 3 D-61 Package, 2 x 55 A

ORDERING INFORMATION TABLE

Device code



1 - HPP product suffix

2 - Current rating (110 = 110 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

6 - Voltage rating (045 = 45 V)

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = Standard production

• PbF = Lead (Pb)-free

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95354</u>				
Part marking information <u>www.vishay.com/doc?95356</u>				

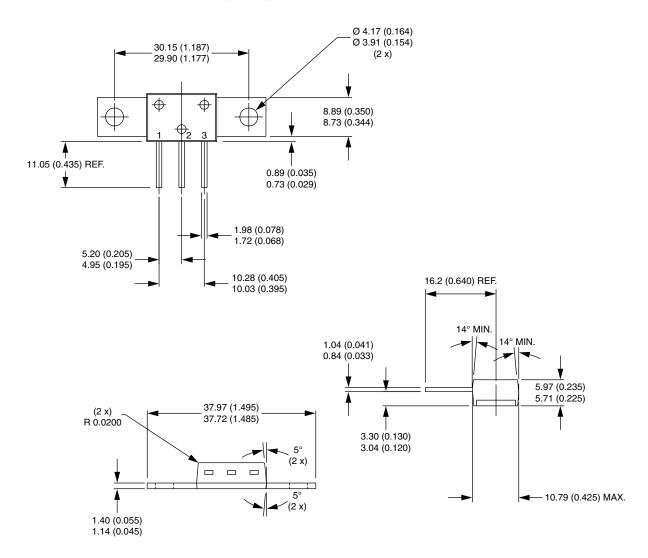
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Vishay Semiconductors

D-61-8, D-61-8-SM, D-61-8-SL

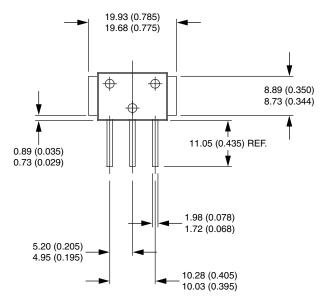
DIMENSIONS - D-61-8 in millimeters (inches)

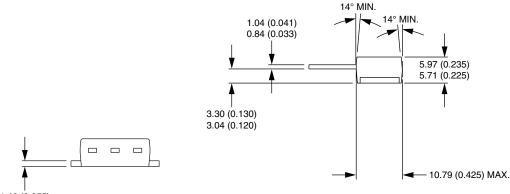




Vishay Semiconductors

DIMENSIONS - D-61-8-SM in millimeters (inches)

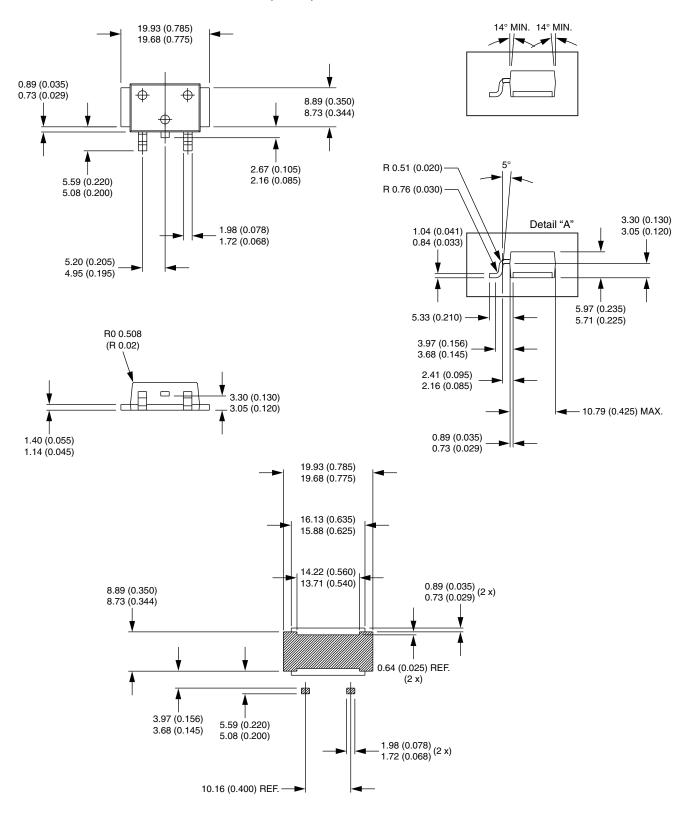






Vishay Semiconductors

DIMENSIONS - D-61-8-SL in millimeters (inches)





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Authorized Distribution Brand:

























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