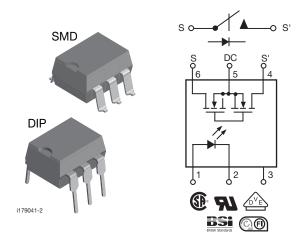
LH1540AAB, LH1540AABTR, LH1540AT

Vishay Semiconductors

1 Form A Solid-State Relay



DESCRIPTION

The LH1540 is robust, ideal for telecom and ground fault applications. It is an SPST normally open switch (form A) that replaces electromechanical relays in many applications. It is constructed using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuitry and MOSFET switches. In addition, it employs current-limiting circuitry which meets lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

FEATURES

- · Current limit protection
- Isolation test voltage 5300 V_{RMS}
- Typical R_{ON} 20 Ω , max. 25 Ω
- Load voltage 350 V
- Load current 120 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- SMD lead available on tape and reel
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- · General telecom switching
- Instrumentation
- Industrial controls

Note

• See "solid-state relays" (application note 56)

AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection

CSA: certification no. 093751
BSI: certification no. 7979/7980

FIMKO: 25419

DIN EN: 60747-5-2 (VDE 0884)/60747-5-5 (pending),

available with option 1

ORDERING INFORMATION			
L H 1 5 4 0 F	TR. PACKAGE TAPE AND		
PACKAGE	UL, CSA, BSI, FIMKO		
SMD-6	LH1540AAB		
SMD-6, tape and reel	LH1540AABTR		
DIP-6, thru hole	LH1540AT		

LH1540AAB, LH1540AABTR, LH1540AT

Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
LED continuous forward current		I _F	50	mA		
LED reverse voltage	I _R ≤ 10 μA	V_{R}	8	V		
OUTPUT						
DC or peak AC load voltage	I _L ≤ 50 μA	V_L	350	V		
Continuous DC load current - bidirectional operation		IL	120	mA		
Continuous DC load current - unidirectional operation		IL	250	mA		
Peak load current (single shot)	t = 100 ms	I _P	(1)	mA		
SSR						
Ambient temperature range		T _{amb}	- 40 to + 85	°C		
Storage temperature range		T _{stg}	- 40 to + 150	°C		
Pin soldering temperature (2)	t = 10 s max.	T _{sld}	260	°C		
Input to output isolation voltage		V _{ISO}	5300	V_{RMS}		
Output power dissipation (continuous)		P _{diss}	550	mW		

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to current limit performance application note for a discussion on relay operation during transient currents.
- (2) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED forward current, switch turn-on	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I _{Fon}		1	2	mA
LED forward current, switch turn-off	$V_{L} = \pm 300 \text{ V}$	I _{Foff}	0.2	0.9		mA
LED forward voltage	I _F = 10 mA	V_{F}	1.15	1.26	1.45	V
OUTPUT						
On-resistance AC/DC: pin 4 (±) to 6 (±)	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	R _{ON}	12	20	25	Ω
On-resistance DC: pin 4, 6 (+) to 5 (±)	$I_F = 5 \text{ mA}, I_L = 100 \text{ mA}$	R _{ON}	3	5	6.25	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.5	5000		GΩ
Current limit AC (1): pin 4 (±) to 6 (±)	$I_F = 5 \text{ mA}, V_L = \pm 6 \text{ V}, t = 5 \text{ ms}$	I _{LMT}	175	210	250	mA
. , , , , , , , , , , , , , , , , , , ,	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Io		0.32	200	nA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 350 \text{ V}$	Io			1	μΑ
Output conscitous a sign 4 to 6	$I_F = 0 \text{ mA}, V_L = 1 \text{ V}$	Co		55		pF
Output capacitance pin 4 to 6	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}$	Co		10		pF
Switch offset	I _F = 5 mA	V _{OS}		0.15		μV
TRANSFER						
Capacitance (input to output)	V _{ISO} = 1 V	C _{IO}		0.8		pF

Notes

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

(1) No DC mode current limit available.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}		1.2	2	ms
Turn-off time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}		0.5	2	ms

PARAMETER		TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification		IEC 68 part 1		40/85/21	
Pollution degree		DIN VDE 0109		2	
Tracking resistance (comparative tracking index	x)	Insulation group IIIa	CTI	175	
Highest allowable overvoltage		Transient overvoltage	V _{IOTM}	8000	V _{peak}
Max. working insulation voltage		Recurring peak voltage	V _{IORM}	890	V _{peak}
Insulation resistance at 25 °C			R _{IS}	≥ 10 ¹²	Ω
Insulation resistance at T _S Insulation resistance at 100 °C		V _{IO} = 500 V	R _{IS}	≥ 10 ⁹	Ω
			R _{IS}	≥ 10 ¹¹	Ω
Partial discharge test voltage		Methode a, V _{pd} = V _{IORM} x 1.875	V_{pd}	1669	V _{peak}
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T _{SI}	175	°C
	Input current		I _{SI}	300	mA
	Output power		P _{SO}	700	mW
Minimum external air gap (clearance)		Measured from input terminals to output terminals, shortest distance through air		≥ 7	mm
Minimum external tracking (creepage)		Measured from input terminals to output terminals, shortest distance path along body		≥ 7	mm

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

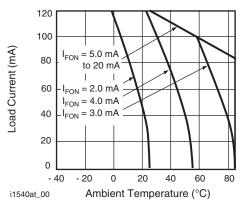


Fig. 1 - Recommended Operating Conditions

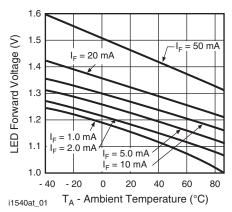


Fig. 2 - LED Voltage vs. Temperature

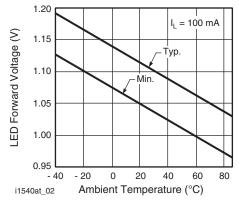


Fig. 3 - LED Dropout Voltage vs. Temperature

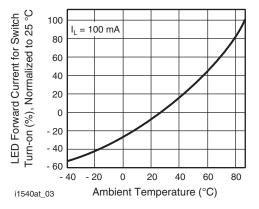


Fig. 4 - LED Current for Switch Turn-on vs. Temperature

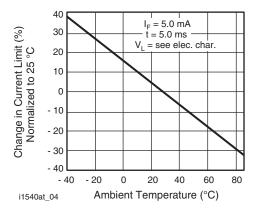


Fig. 5 - Current Limit vs. Temperature

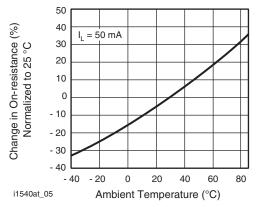


Fig. 6 - On-resistance vs. Temperature

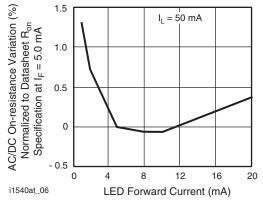


Fig. 7 - Variation in On-resistance vs. LED Current

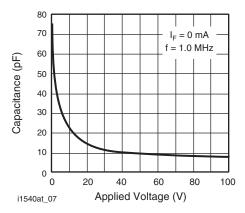


Fig. 8 - Switch Capacitance vs. Applied Voltage

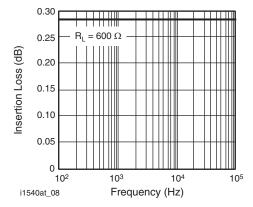


Fig. 9 - Insertion Loss vs. Frequency

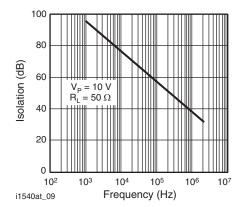


Fig. 10 - Output Isolation

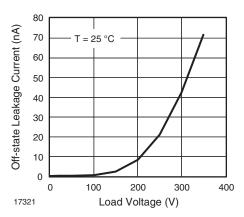


Fig. 11 - Leakage Current vs. Applied Voltage

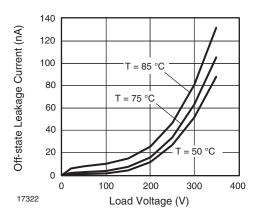


Fig. 12 - Leakage Current vs. Applied Voltage at Elevated Temperatures

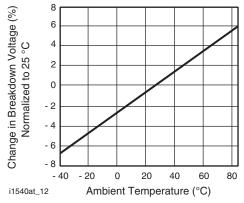


Fig. 13 - Switch Breakdown Voltage vs. Temperature

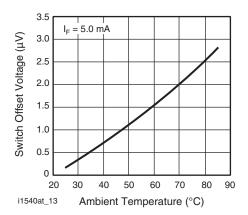


Fig. 14 - Switch Offset Voltage vs. Temperature

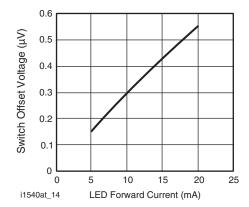


Fig. 15 - Switch Offset Voltage vs. LED Current

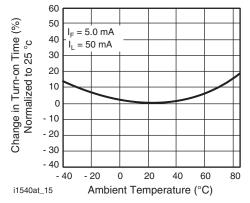
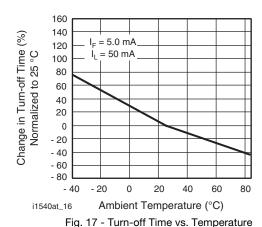


Fig. 16 - Turn-on Time vs. Temperature



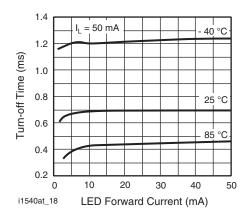


Fig. 19 - Turn-off Time vs. LED Current

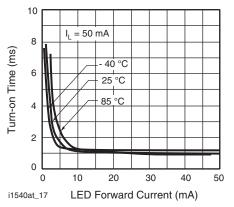
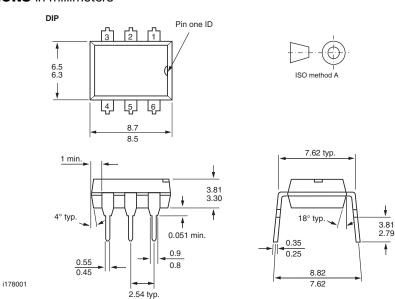


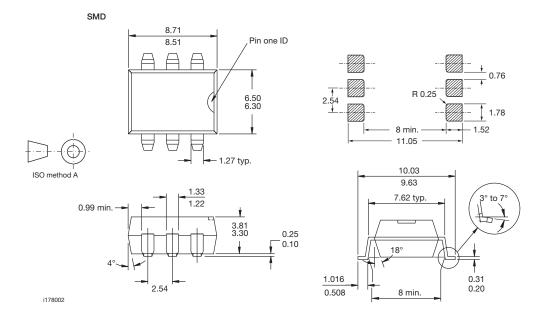
Fig. 18 - Turn-on Time vs. LED Current

PACKAGE DIMENSIONS in millimeters



LH1540AAB, LH1540AABTR, LH1540AT

Vishay Semiconductors



PACKAGE MARKING



Note

• Tape and reel suffix (TR) is not part of the package marking.



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