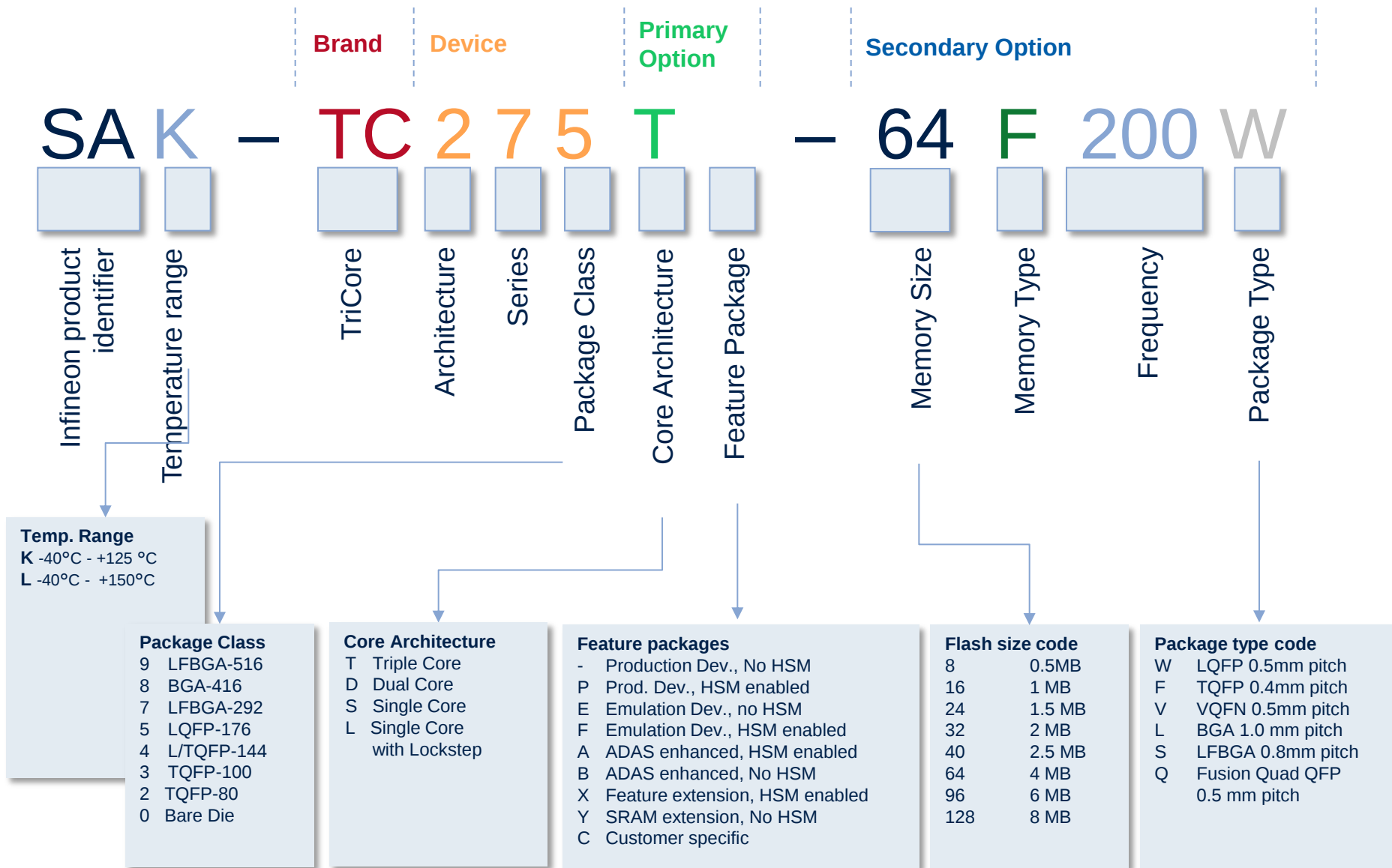


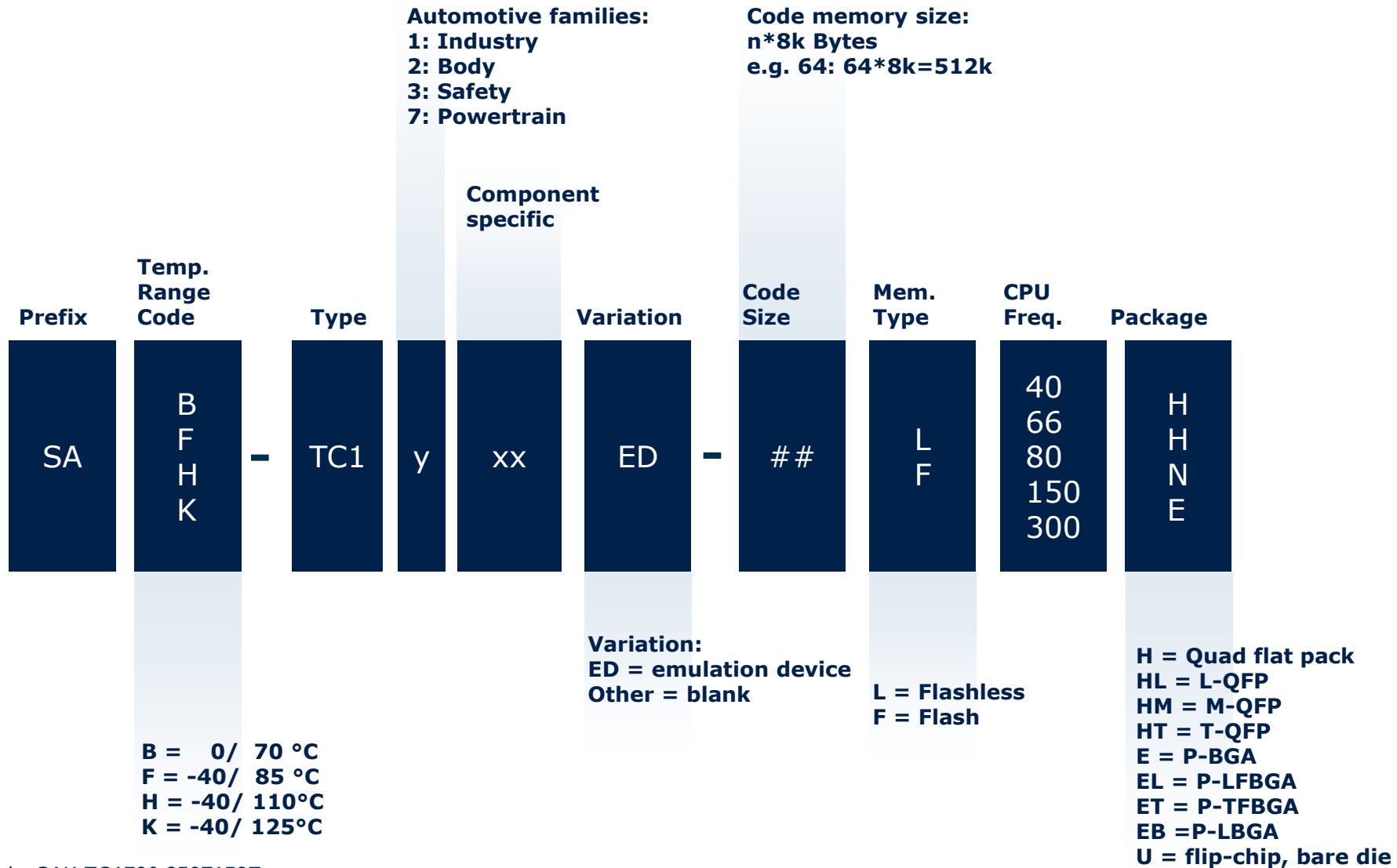
# Naming Convention for **AURIX™** Family



# Naming Convention for **TriCore®** Family



Example: SAK-TC1796-256F150E



Example: SAK-TC1796-256F150E

# Naming Convention for **XMC4000** Family



Example: XMC4500–F144K1024 AC

| Family | Core | Feature          |   |                  | Package     | Pins                   | Temp.       | Flash [kB]                             | Step                                                                        |
|--------|------|------------------|---|------------------|-------------|------------------------|-------------|----------------------------------------|-----------------------------------------------------------------------------|
| XMC    | 4    | 5<br>4<br>2<br>1 | 0 | 0<br>2<br>4<br>8 | E<br>F<br>Q | 144<br>100<br>64<br>48 | K<br>X<br>F | 1024<br>768<br>512<br>256<br>128<br>64 | AA<br>AB<br>AC                                                              |
|        |      |                  |   |                  |             |                        |             |                                        | Silicon Version<br>AA = 01<br>AB = 02<br>AC = 03                            |
|        |      |                  |   |                  |             |                        |             |                                        | K = -40° to 125°C<br>X = -40° to 105°C<br>F = -40° to 85°C                  |
|        |      |                  |   |                  |             |                        |             |                                        | E = LFBGA   F = LQFP   Q = VQFN                                             |
|        |      |                  |   |                  |             |                        |             |                                        | 0 = Full Featured < ..... > 8 = Feature Subset                              |
|        |      |                  |   |                  |             |                        |             |                                        | 1 = Baseline < ..... > 5 = High-End                                         |
|        |      |                  |   |                  |             |                        |             |                                        | ARM® Cortex™-M4 with built in DSP, SPFPU, DMA and MPU                       |
|        |      |                  |   |                  |             |                        |             |                                        | XMC := Industrial Microcontroller Family based on ARM® Cortex™-M Processors |

# Naming Convention for **XMC1000** Family

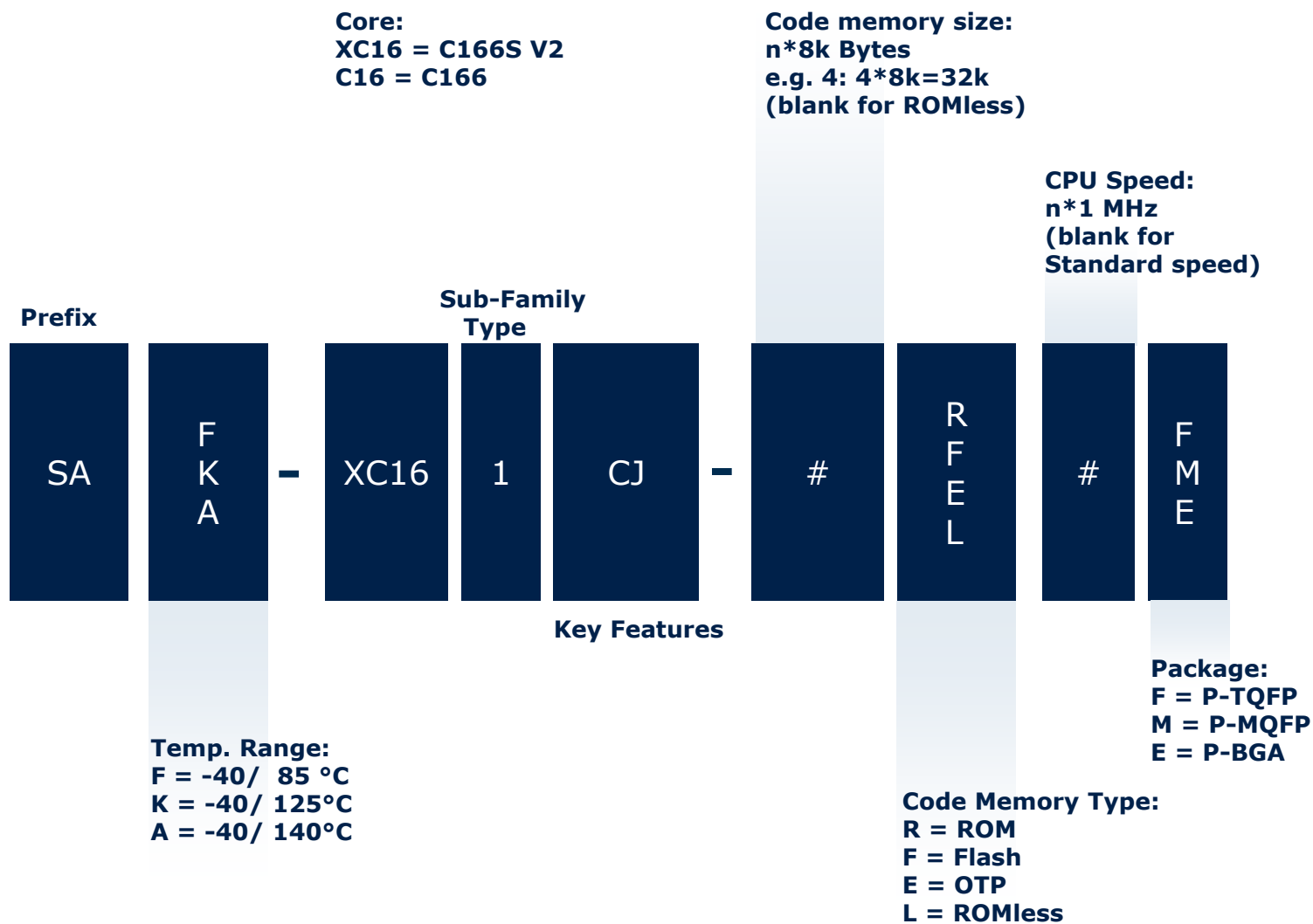
Example: XMC1302-T038X0200 AA

| Family                                                                      | Core | Feature |   |     | Package                                        | Pins | Temp.                                 | Flash [kB] | Step |
|-----------------------------------------------------------------------------|------|---------|---|-----|------------------------------------------------|------|---------------------------------------|------------|------|
| XMC                                                                         | 1    | 3       | 0 | 2   | Q<br>T                                         | 40   | X<br>F                                | 200        | AA   |
|                                                                             |      | 2       |   | 128 |                                                |      |                                       |            |      |
|                                                                             |      | 1       |   | 64  |                                                |      |                                       |            |      |
|                                                                             |      |         |   | 32  |                                                |      |                                       |            |      |
|                                                                             |      |         |   | 16  |                                                |      |                                       |            |      |
|                                                                             |      |         |   | 8   |                                                |      |                                       |            |      |
| Silicon Version<br>AA = 01                                                  |      |         |   |     |                                                |      |                                       |            |      |
|                                                                             |      |         |   |     |                                                |      | X = -40° to 105°C<br>F = -40° to 85°C |            |      |
|                                                                             |      |         |   |     | Q = VQFN   T = TSSOP                           |      |                                       |            |      |
|                                                                             |      |         |   |     | 2 = Full Featured < ..... > 0 = Feature Subset |      |                                       |            |      |
|                                                                             |      |         |   |     | 1 = Baseline < ..... > 3 = High-End            |      |                                       |            |      |
| ARM® Cortex™-M0                                                             |      |         |   |     |                                                |      |                                       |            |      |
| XMC := Industrial Microcontroller Family based on ARM® Cortex™-M Processors |      |         |   |     |                                                |      |                                       |            |      |

# Naming Convention for **16-bit** Family



Example: SAF-XC161CJ-14F20F

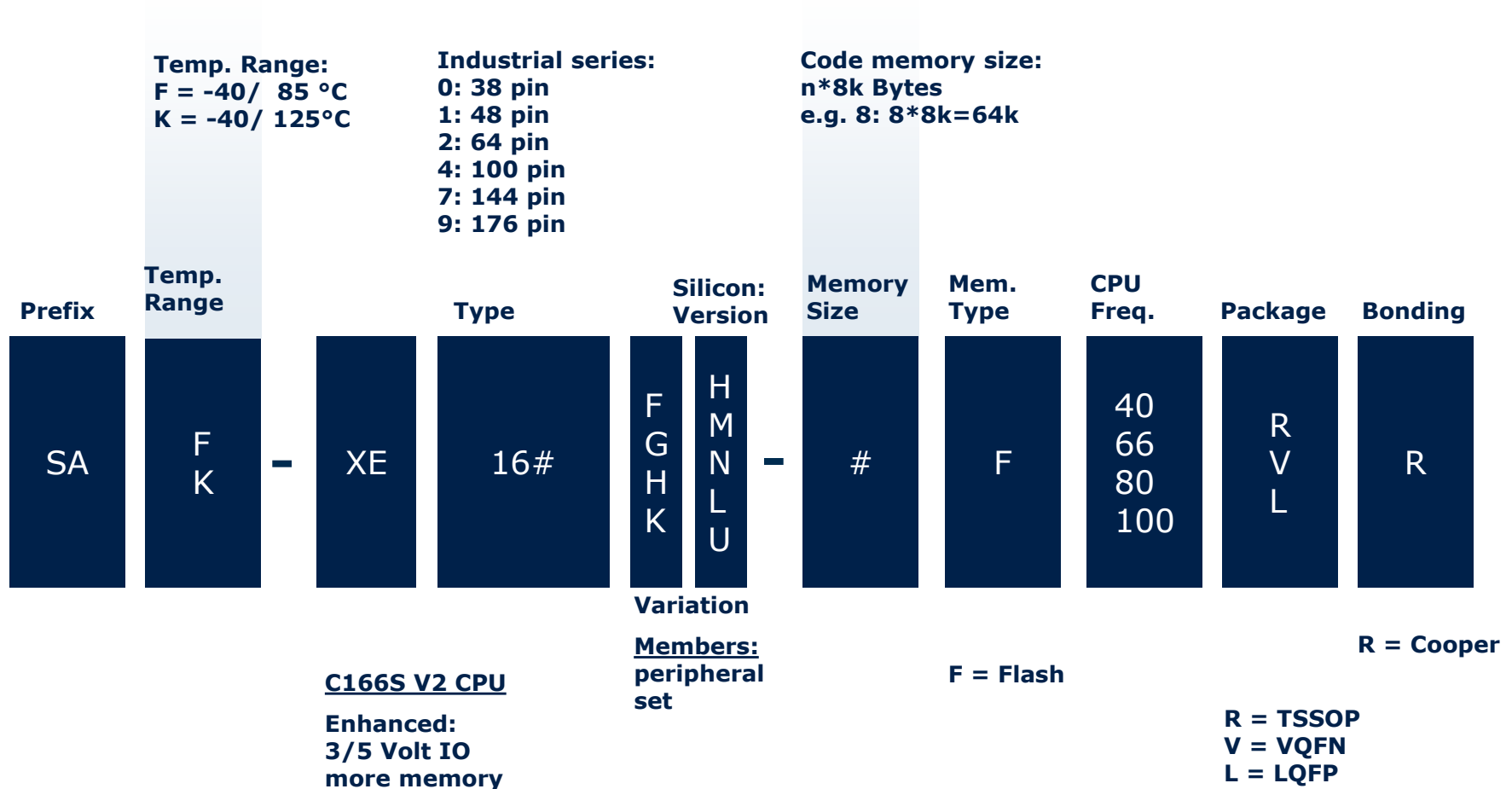




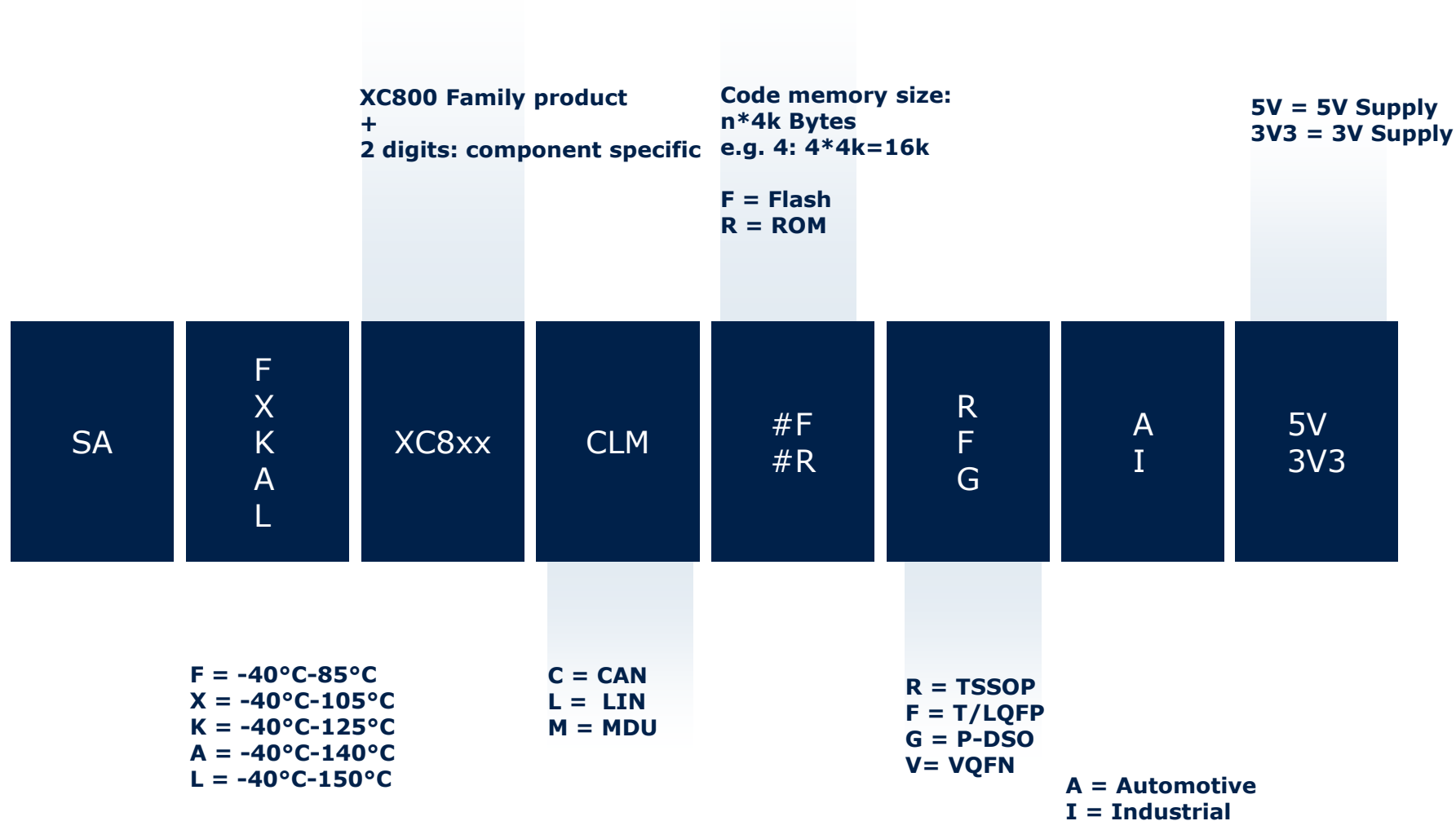
# Naming Convention for **XE166** Family



Example: SAF-XE167F-96F80L



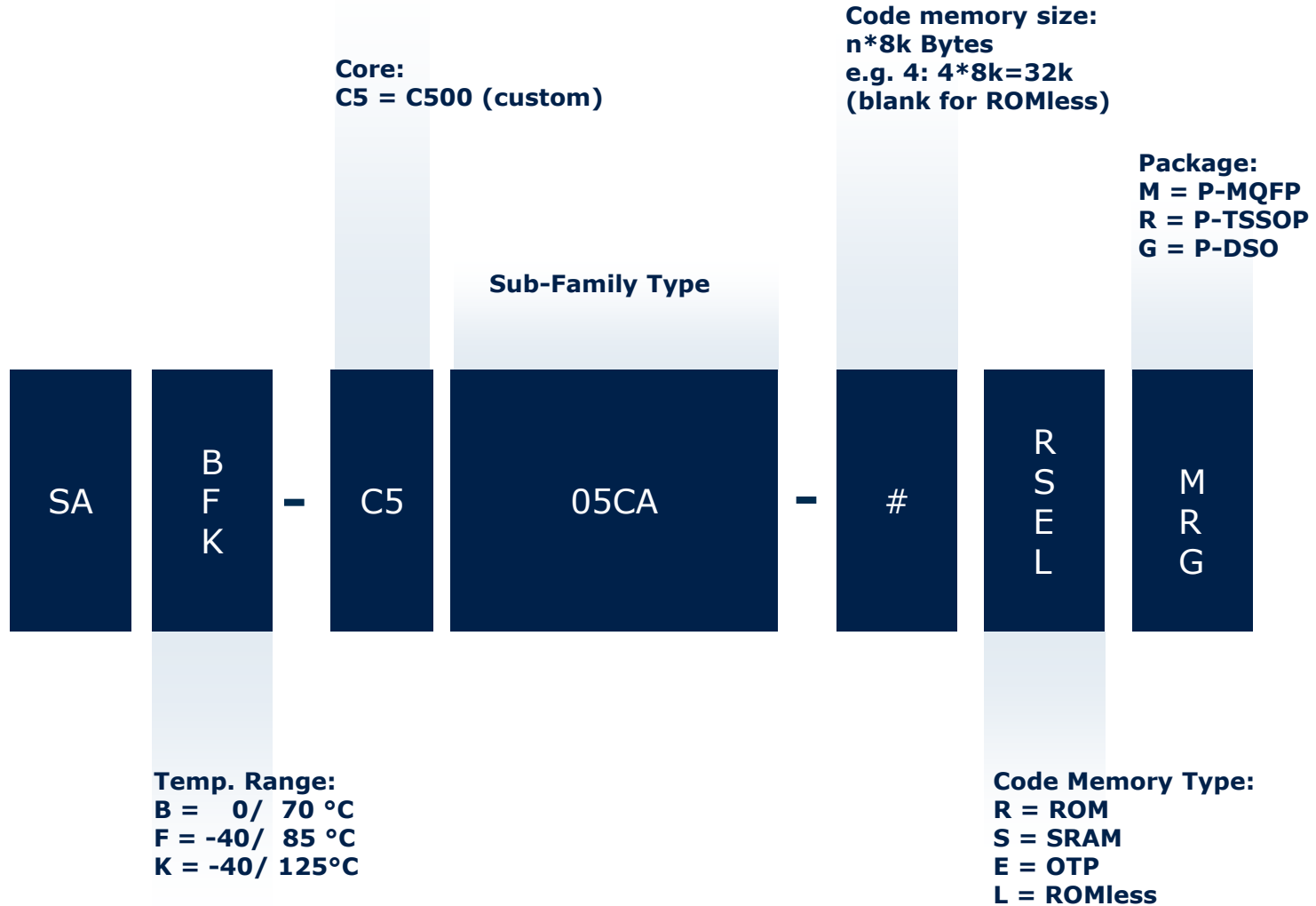
# Naming Convention for **XC800** Family



# Naming Convention for **C500** Family



Example: SAB-C505CA-4EM



# AMEYA360

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