

BTA06T-600CWRG

6 A Snubberless™ Triac

Features

- High static and dynamic commutation
- BTA series is UL1557 certified (File ref.: 81734)
- Package is RoHS (2002/95/EC) compliant
- I_{GT} = 35 mA

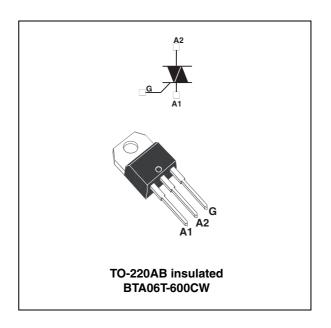
Applications

Specially designed for power tool applications, it can also be used to drive loads like motor speed controller, kitchen equipments such as electro valves, light dimmers and similar.

Description

Available in through-hole package, the Triac BTA06T-600CW is suitable for general purpose ac switching.

Being a fully insulated package, the BTA06T-600CW provides insulation rated at 2500 V rms.



TM: Snubberless is a trademark of STMicroelectronics

Characteristics BTA06T-600CWRG

1 Characteristics

Table 1. Absolute maximum ratings (limiting values)

Symbol	Parameter			Value	Unit	
I _{T(RMS)}	On-state rms current (full sine wave) $T_c = 100 ^{\circ}\text{C}$		6	Α		
l	Non repetitive surge peak on-state current (full	F = 60 Hz	t = 16.7 ms	47	Α	
I _{TSM}	cycle sine wave, T _j initial = 25 °C)	F = 50 Hz	t = 20 ms	45	A	
l ² t	I ² t Value for fusing	t Value for fusing $t_p = 10 \text{ ms}$		13	A ² s	
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$	F = 120 Hz	T _j = 125 °C	50	A/μs	
V _{DSM} /V _{RSM}	Non repetitive surge peak off-state voiltage	t _p = 10 ms	T _j = 25 °C	V _{DRM} /V _{RRM} + 100	V	
I _{GM}	Peak gate current $t_p = 20 \mu s$ $T_j = 125 ^{\circ}C$		4	Α		
$P_{G(AV)}$	Average gate power dissipation $T_j = 125 ^{\circ}\text{C}$		1	W		
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			-40 to +150 -40 to +125	°C	

Table 2. Electrical characteristics, Snubberless (3 quadrants) $(T_i = 25 \, ^{\circ}C, \text{ unless otherwise specified})$

Symbol	Test conditions	Quadrant		Value	Unit
I _{GT} ⁽¹⁾	$V_D = 12 \text{ V R}_L = 30 \Omega$	I - II - III	MAX	35	mA
V _{GT}	$V_D = 12 \text{ V R}_L = 30 \Omega$	I - II - III	MAX	1.3	٧
V_{GD}	$V_D = V_{DRM} R_L = 3.3 \text{ k}\Omega$	1 - 11 - 111	MIN	0.2	V
I _H ⁽²⁾	I _T = 100 mA		MAX	35	mA
	_10v	I - III	MAX	50	mΛ
ال	$I_{G} = 1.2 \times I_{GT}$	II	IVIAA	80	- mA
dV/dt (2)	$V_D = 67\% V_{DRM}$, gate open, $T_j = 125 ^{\circ}\text{C}$		MIN	750	V/µs
(dl/dt)c (2)	Without snubber, T _i = 125 °C			8.0	A/ms

^{1.} Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

Table 3. Static electrical characteristics

Symbol	Test conditions			Value	Unit
V _{TM} ⁽¹⁾	$I_{TM} = 8.5 \text{ A}, t_p = 380 \mu\text{s}$	T _j = 25 °C	MAX	1.6	V
V _{TO} ⁽¹⁾	Threshold voltage	T _j = 125 °C	MAX	0.85	V
R _D ⁽¹⁾	Dynamic resistance	T _j = 125 °C	MAX	80	mΩ
I _{DRM}	V - V	T _j = 25 °C	MAX	5	μΑ
I _{RRM}	$V_{DRM} = V_{RRM}$	T _j = 125 °C	IVIAX	1	mA

^{1.} For both polarities of A2 pin referenced to A1 pin

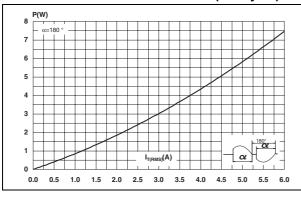
^{2.} For both polarities of A2 pin referenced to A1 pin

BTA06T-600CWRG Characteristics

Table 4. Thermal resistances

Symbol	Parameter Value		Unit
R _{th(j-c)}	Junction to case (ac)	3.4	°C/W
R _{th(j-a)}	Junction to ambient		C/VV

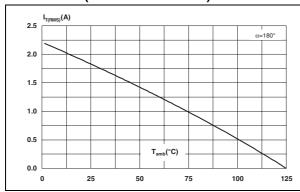
Figure 1. Maximum power dissipation versus Figure 2. On-state current (rms) versus case rms on-state current (full cycle) temperature (full cycle)



7.0 | Tr(PMS)(A) | C=180° | C=

Figure 3. On-state current (rms) versus ambient temperature (free air convection)

Figure 4. Relative variation of thermal impedance versus pulse duration



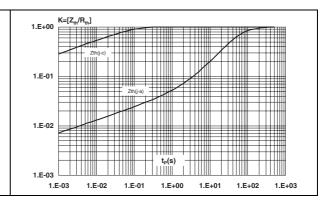
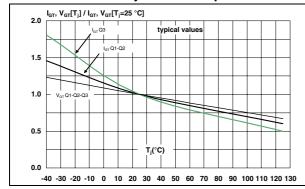
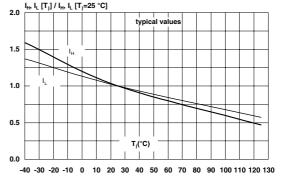


Figure 5. Relative variation of gate trigger current, and gate trigger voltage versus junction temperature

Figure 6. Relative variation of holding current and latching current versus junction temperature





Characteristics BTA06T-600CWRG

Figure 7. Surge peak on-state current versus Figure 8. Non-repetitive surge peak on-state number of cycles current for sinusoidal

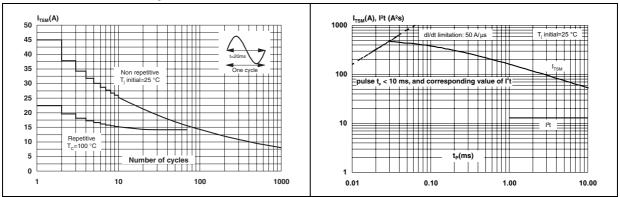


Figure 9. On-state characteristics (maximum values)

Figure 10. Relative variation of critical rate of decrease of main current (di/dt)c versus junction temperature

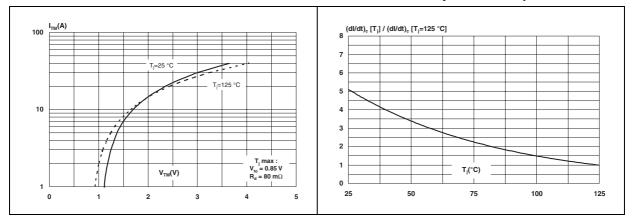
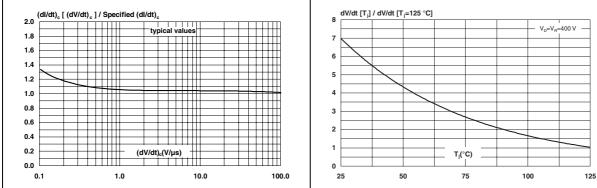


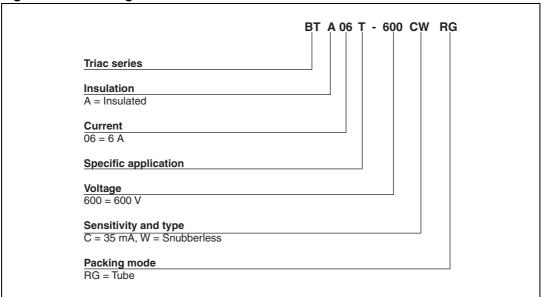
Figure 11. Relative variation of critical rate of decrease of main current (di/dt)c versus reapplied (dV/dt)c

Figure 12. Relative variation of static dV/dt immunity versus junction temperature



2 Ordering information

Figure 13. Ordering information scheme



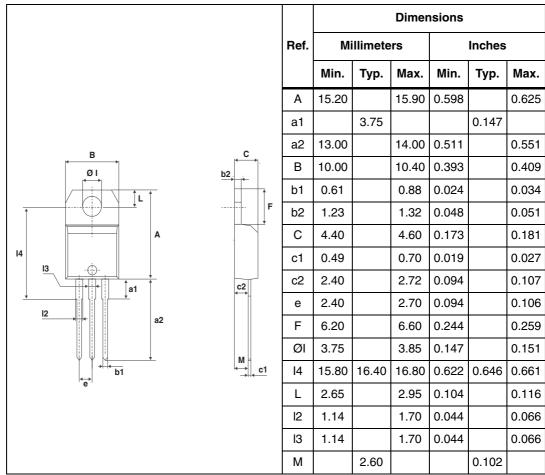
3 Package information

- Epoxy meets UL94, V0
- Lead-free packages

6/8

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Table 5. TO-220AB insulated dimensions



4 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Packing mode
BTA06T-600CWRG	BTA06T-600CW	TO-220AB ins	2.3 g	50	Tube

5 Revision history

Table 7. Document revision history

Date Revision		Changes
15-Nov-2007	1	Initial release.
17-Jun-2010 2		Updated title on page 1. Updated ECOPACK statement.

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8/8 Doc ID 17641 Rev 2

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