DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features

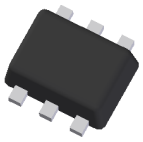
- Dual N-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **ESD Protected up to 2kV**
- **Qualified to AEC-Q101 standards for High Reliability**

Mechanical Data

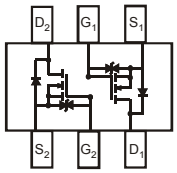
- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.006 grams (approximate)

ESD protected up to 2kV

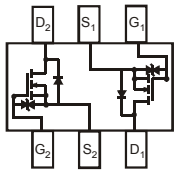
SOT563



Top View



DMN5L06VK



DMN5L06VAK
DMN5010VAK

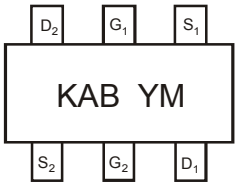
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN5L06VK-7	SOT563	3,000/Tape & Reel
DMN5L06VK-13	SOT563	10,000/Tape & Reel
DMN5L06VAK-7	SOT563	3,000/Tape & Reel
DMN5L06VAK-13	SOT563	10,000/Tape & Reel
DMN5010VAK-7	SOT563	3,000/Tape & Reel
DMN5010VAK-13	SOT563	10,000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

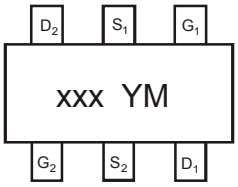
Marking Information (Note 5)

DMN5L06VK



KAB= DMN5L06VK Product Type Marking Code (See Note 5)
YM = Date Code Marking
Y = Year (ex: T = 2006)
M = Month (ex: 9 = September)

DMN5010VAK
DMN5L06VAK



xxx = Product Type Marking Code: KAE or KAC (See Note 5)
YM = Date Code Marking
Y = Year (ex: T = 2006)
M = Month (ex: 9 = September)

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	T	U	V	W	X	Y	Z	A	B	C	D	E

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Note: 5. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways).

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain Source Voltage		V_{DSS}	50	V
Drain-Gate Voltage $R_{GS} \leq 1.0M\Omega$		V_{DGR}	50	V
Gate-Source Voltage	Continuous Pulsed	V_{GSS}	±20 ±40	V
Drain Current (Note 6)	Continuous Pulsed	I_D I_{DM}	280 1.5	mA A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P_D	250	mW
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	500	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage		BV_{DSS}	50	—	—	V	V_{GS} = 0V, I_D = 10μA
Zero Gate Voltage Drain Current	@ T_C = +25°C	I_{DSS}	—	—	60	nA	V_{DS} = 50V, V_{GS} = 0V
Gate-Body Leakage		I_{GSS}	—	—	1 500 50	μA nA nA	V_{GS} = ±12V, V_{DS} = 0V V_{GS} = ±10V, V_{DS} = 0V V_{GS} = ±5V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	@T_J = +25°C @T_J = +0°C~ +85°C (Note 8)	$V_{GS(th)}$	0.49 0.30	—	1.0 1.2	V	V_{DS} = V_{GS}, I_D = 250μA
Static Drain-Source On-Resistance		$R_{DS\ (ON)}$	— — —	— — —	3.0 2.5 2.0	Ω	V_{GS} = 1.8V, I_D = 50mA V_{GS} = 2.5V, I_D = 50mA V_{GS} = 5.0V, I_D = 50mA
On-State Drain Current		$I_{D(ON)}$	0.5	1.4	—	A	V_{GS} = 10V, V_{DS} = 7.5V
Forward Transconductance		$\|Y_{fs}\|$	200	—	—	mS	V_{DS} =10V, I_D = 0.2A
Source-Drain Diode Forward Voltage		V_{SD}	0.5	—	1.4	V	V_{GS} = 0V, I_S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance		C_{iss}	—	—	50	pF	V_{DS} = 25V, V_{GS} = 0V f = 1.0MHz
Output Capacitance		C_{oss}	—	—	25	pF	
Reverse Transfer Capacitance		C_{rss}	—	—	5.0	pF	

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

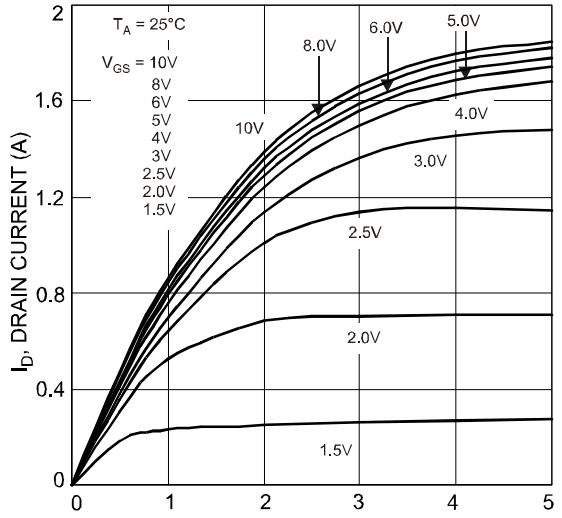


Fig. 1 Typical Output Characteristics

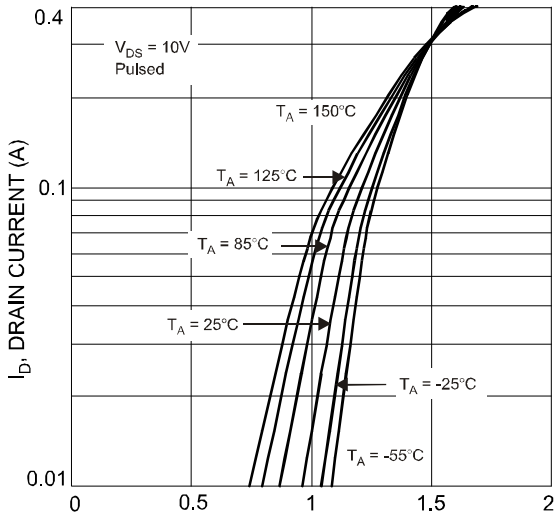


Fig. 2 Typical Transfer Characteristics

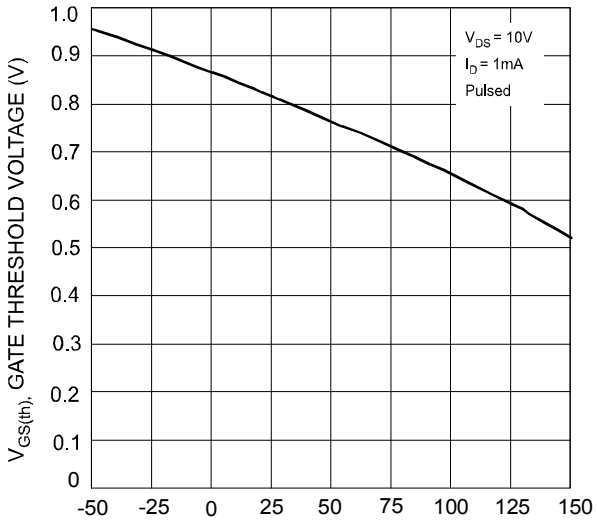


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

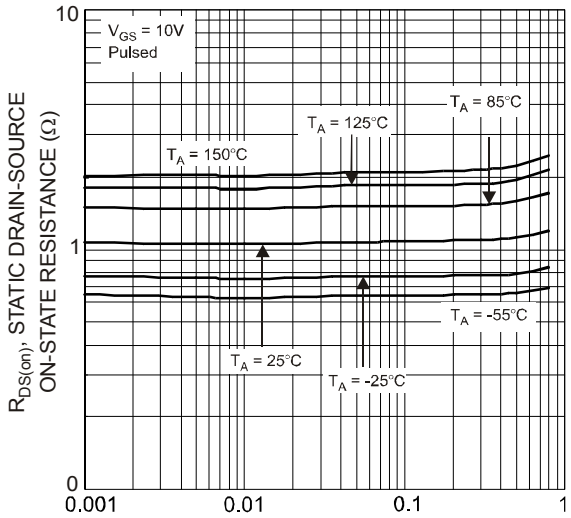


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

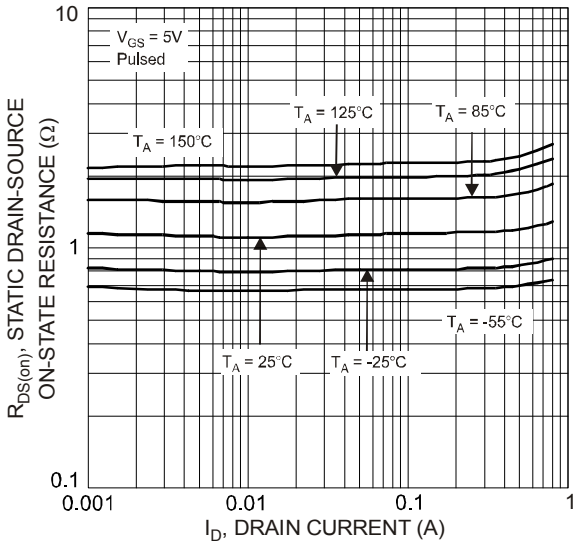


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

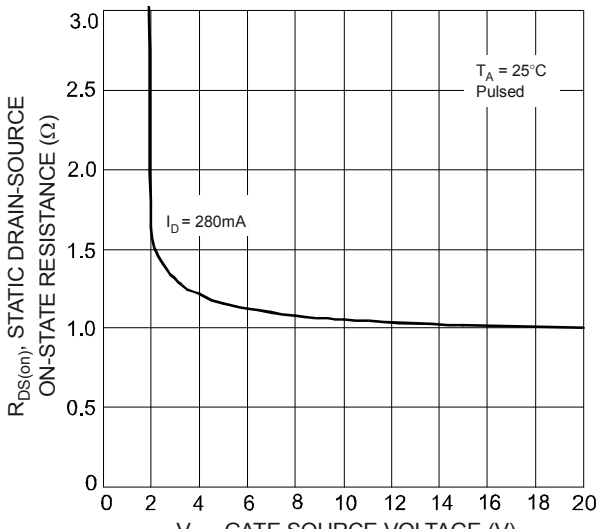


Fig. 6 Static Drain-Source On-Resistance vs. Gate-Source Voltage

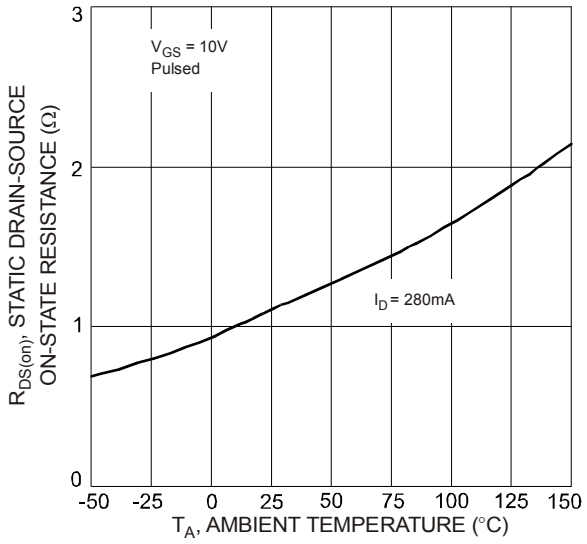


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

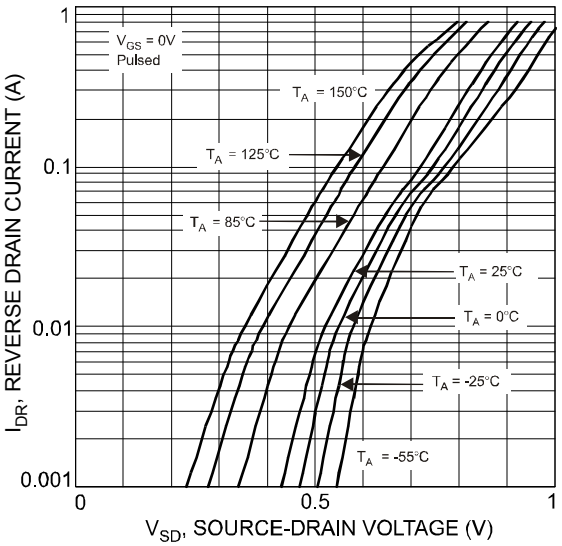


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

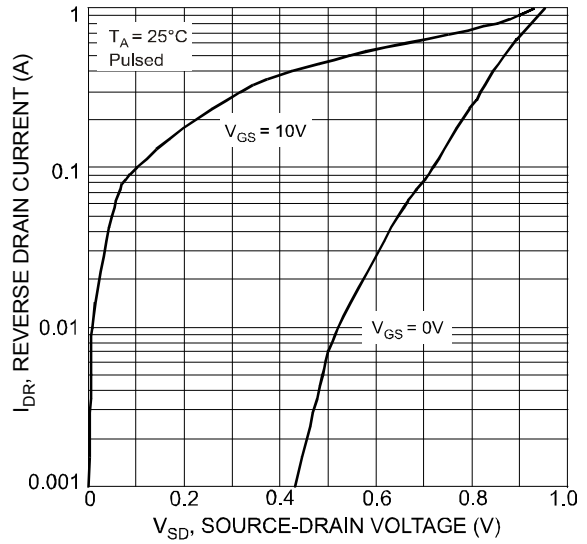


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

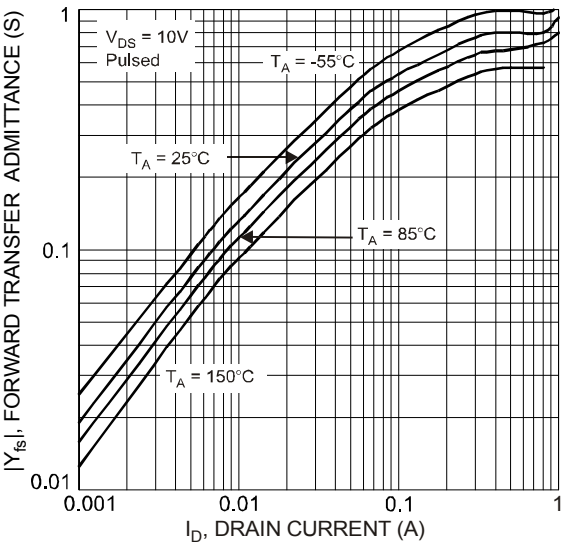


Fig.10 Forward Transfer Admittance vs. Drain Current

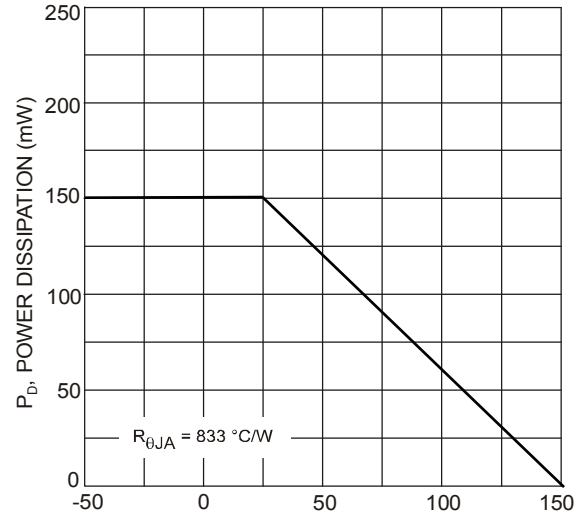
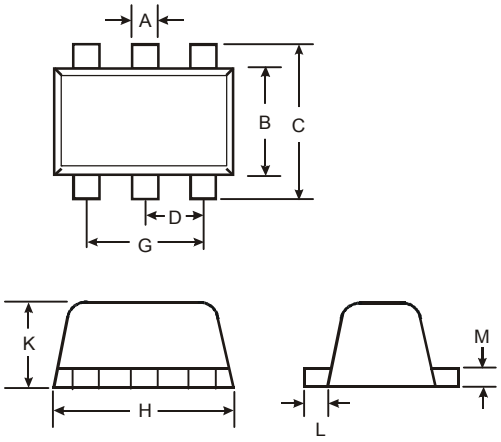


Fig. 11 Derating Curve - Total

Package Outline Dimensions

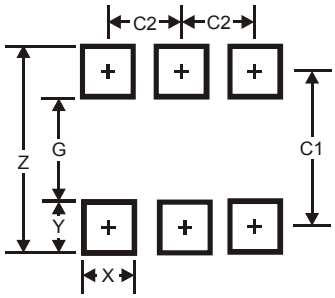
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT563			
Dim	**Min**	**Max**	**Typ**
A	0.15	0.30	0.20
B	1.10	1.25	1.20
C	1.55	1.70	1.60
D	-	-	0.50
G	0.90	1.10	1.00
H	1.50	1.70	1.60
K	0.55	0.60	0.60
L	0.10	0.30	0.20
M	0.10	0.18	0.11
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.2
G	1.2
X	0.375
Y	0.5
C1	1.7
C2	0.5

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or

2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

www.diodes.com

AMEYA360
Components Supply Platform

Authorized Distribution Brand :

Website :

Welcome to visit www.ameya360.com

Contact Us :

- ### Address :

  401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd
  Minhang District, Shanghai , China

- ### Sales :

  Direct +86 (21) 6401-6692

  Email amall@ameya360.com

  QQ 800077892

  Skype ameyasales1 ameyasales2

- ### Customer Service :

  Email service@ameya360.com

- ### Partnership :

  Tel +86 (21) 64016692-8333

  Email mkt@ameya360.com