

TPS54531EVM-530, 5-A Regulator Evaluation Module

This user's guide contains information for the TPS54531EVM-530 evaluation module as well as for the TPS54531. Included are the performance specifications, schematic, and the bill of materials of the TPS54531EVM-530.

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1 Introduction

The TPS54531 dc/dc converter is designed to provide up to a 5-A output from an input voltage source of 3.5 V to 28 V. Rated input voltage and output current range for the evaluation module are given in [Table 1](#).

The TPS54531EVM-530 evaluation module circuit is a single, non-synchronous buck converter providing 5 V at 5 A from an 8-V to 28-V input. The switching frequency is internally set at a nominal 570 kHz. The high-side MOSFET is incorporated inside the TPS54531 package along with the gate-drive circuitry. The low drain-to-source on resistance of the MOSFET allows the TPS54531 to achieve high efficiencies and helps keep the junction temperature low at high output currents. The compensation components are external to the integrated circuit (IC), and an external divider allows for an adjustable output voltage. Additionally, the TPS54531 provides adjustable slow start and undervoltage lockout inputs. The absolute maximum input voltage is 30 V for the TPS54531EVM-530. This user's guide describes the TPS54531EVM-530 performance.

Table 1. Input Voltage and Output Current Summary

EVM	Input Voltage Range	Output Current Range
TPS54531EVM-530	$V_{IN} = 8 \text{ V to } 28 \text{ V}$	0 A to 5 A

2 Performance Specification Summary

A summary of the TPS54531EVM-530 performance specifications is provided in [Table 2](#). Specifications are given for an input voltage of $V_{IN} = 12 \text{ V}$ and an output voltage of 1.05 V, unless otherwise noted. The ambient temperature is 25°C for all measurement, unless otherwise noted.

Table 2. TPS54531EVM-530 Performance Specifications Summary

Specifications	Test Conditions	Min	Typ	Max	Unit
Input voltage range (V_{IN})		8	12	28	V
Output voltage			5		V
Operating frequency			570		kHz
Output current range		0		5	A
Line regulation	$I_O = 2.5 \text{ A}$		± 0.06		%
Load regulation	$V_{IN} = 12 \text{ V}$		± 0.1		%
Overcurrent limit	$V_{IN} = 12 \text{ V}$,	5.5	8.5		A
Output ripple voltage	$V_{IN} = 12 \text{ V}$, $I_O = 5 \text{ A}$		15		mV _{PP}
Maximum efficiency	$V_{IN} = 8 \text{ V}$, $I_O = 0.7 \text{ A}$		95.3		%

3 Modifications

These evaluation modules are designed to provide access to the features of the TPS54531. Some modifications can be made to this module.

3.1 Output Voltage Setpoint

To change the output voltage of the EVMs, it is necessary to change the value of resistor R6. The value of R6 for a specific output voltage above 0.8 V can be calculated using [Equation 1](#).

$$R6 = \frac{R5 \times 0.8V}{V_{OUT} - 0.8V} \quad (1)$$

3.2 Output Filter and Closed-Loop Response

The TPS54531 relies on the output filter characteristics and the compensation network of R3, C6, and C7 to ensure stability of the control loop. Pads for a feed forward capacitor (C11) are also included for increased flexibility.

4 Test Setup and Results

This section describes how to properly connect, set up, and use the TPS54531EVM-530. The section also includes test results typical for the evaluation modules and efficiency, output load regulation, output line regulation, load transient response, output voltage ripple, input voltage ripple, start-up, and switching frequency.

4.1 Input/Output Connections

The TPS54531EVM-530 is provided with input and output connectors and test points as shown in [Table 3](#). A power supply capable of supplying 4 A must be connected to J1 through a pair of 20-AWG wires. The load must be connected to J2 through a pair of 20-AWG wires. The maximum load current capability is 5 A. Wire lengths must be minimized to reduce losses in the wires. Test point TP1 provides a place to monitor the V_{IN} input voltages with TP2 providing a convenient ground reference. TP7 is used to monitor the output voltage with TP8 as the ground reference.

Table 3. Connection and Test Points

Reference Designator	Function
J1	V_{IN} (see Table 1 for V_{IN} range)
J2	V_{OUT} , 5 V at 5-A maximum
JP1	EN control. Jumper JP1-1 (EN) to JP1-2 (GND) to disable, remove jumper to enable
TP1	V_{IN} test point at V_{IN} connector
TP2	GND test point at V_{IN} connector
TP3	EN test point
TP4	SS test point
TP5	Switch node test point
TP6	Loop response measurement test point
TP7	Output voltage test point at V_{OUT} connector
TP8	Ground test point at V_{OUT} connector

4.2 Efficiency

Figure 1 shows the efficiency for the TPS54531EVM-530 at an ambient temperature of 25°C.

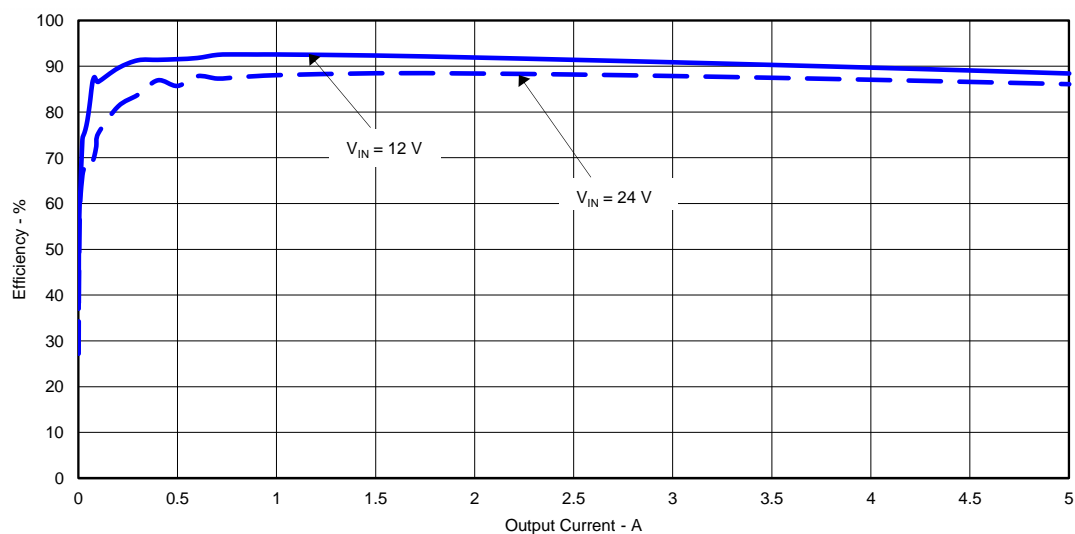


Figure 1. TPS54531EVM-530 Efficiency

Figure 2 shows the efficiency at light loads for the TPS54531EVM-530 at an ambient temperature of 25°C.

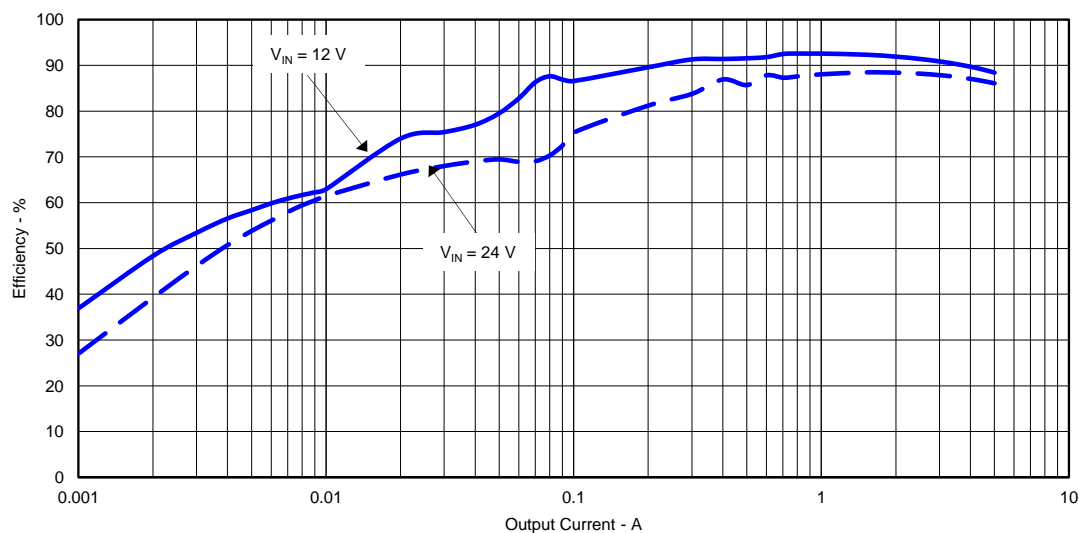


Figure 2. TPS54531EVM-530 Light-Load Efficiency

4.3 Load Regulation

The load regulation for the TPS54531EVM-530 is shown in Figure 3.

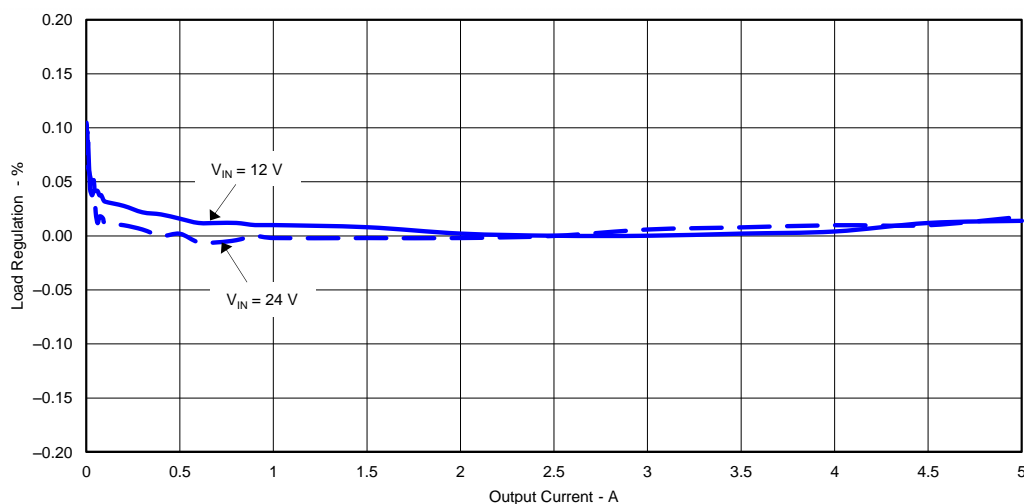


Figure 3. TPS54531EVM-530 Load Regulation, $V_{IN} = 12\text{ V}$ and $V_{IN} = 24\text{ V}$

4.4 Line Regulation

The line regulation for the TPS54531EVM-530 is shown in Figure 4.

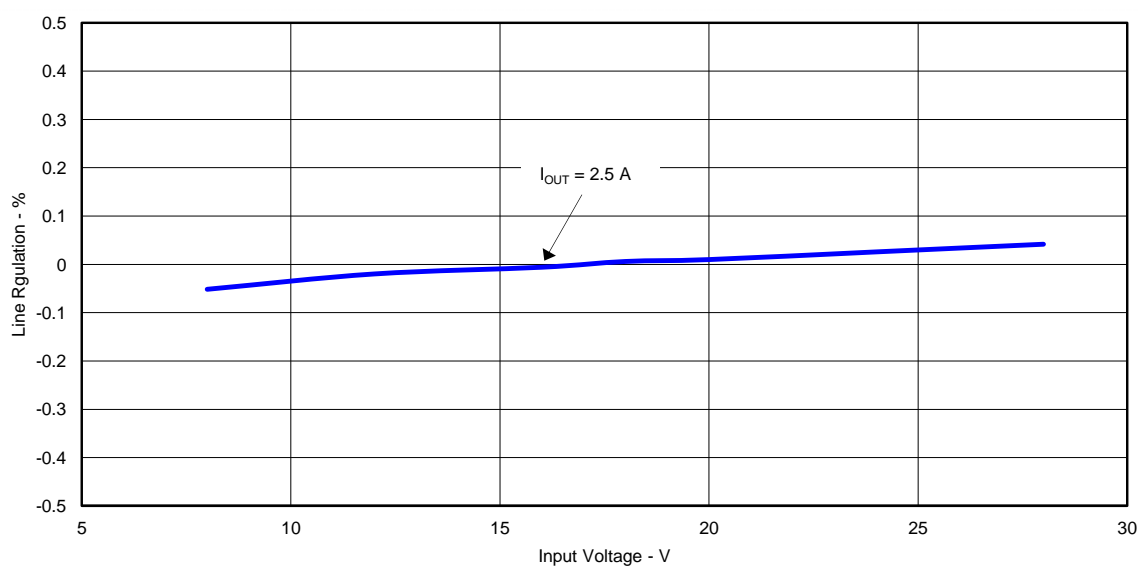


Figure 4. TPS54531EVM-530 Line Regulation

4.5 Loop Response

The TPS54531EVM-530 closed loop response is shown in Figure 5. Input voltage is 12 V and output current is 2.5 A.

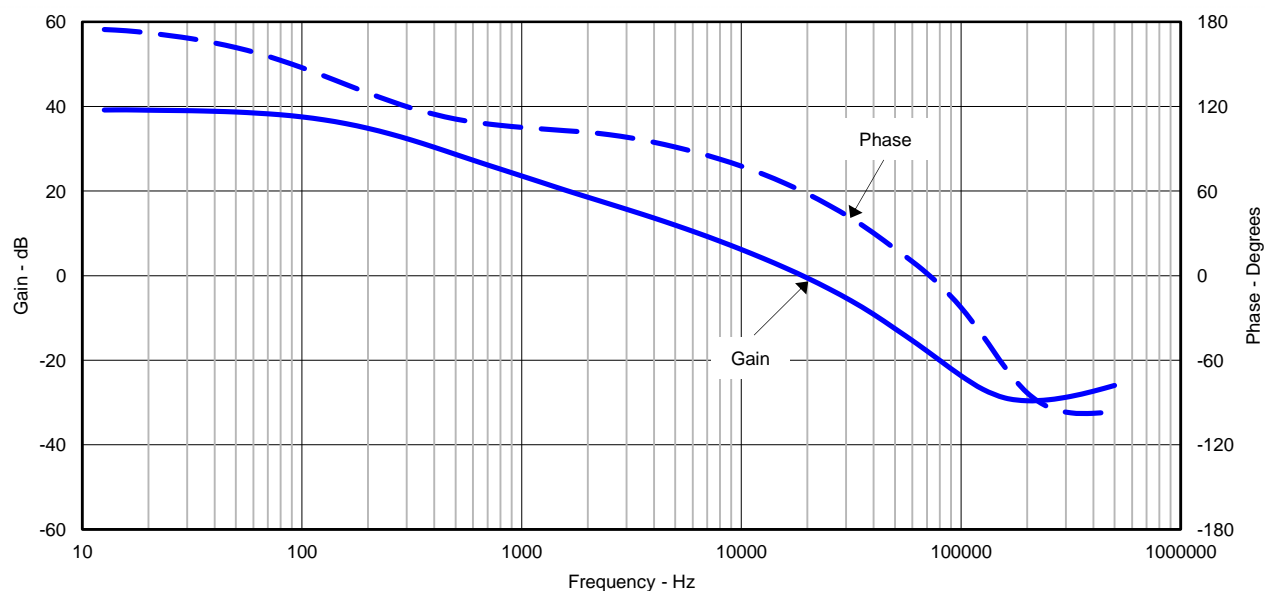


Figure 5. TPS54531EVM-530 Closed Loop Response

4.6 Load Transient Response

The TPS54531EVM-530 response to load transient is shown in Figure 6. The current step is from 1.25 A to 3.75 A. Total peak-to-peak voltage variation is as shown.

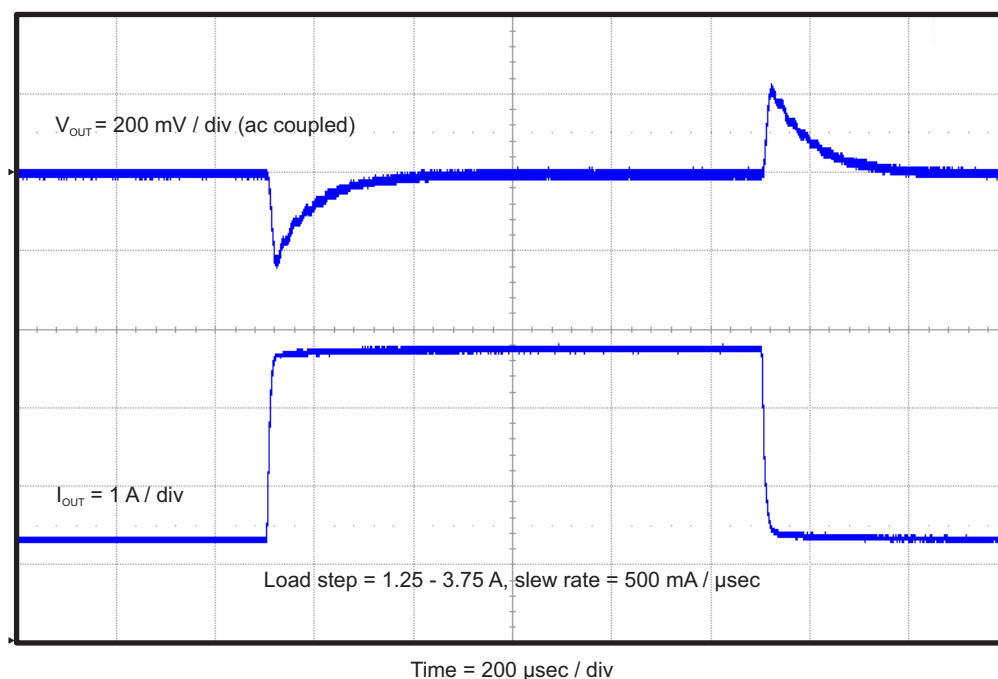


Figure 6. TPS54531EVM-530 Load Transient Response

4.7 Output Voltage Ripple

The TPS54531EVM-530 output voltage ripple is shown in Figure 7. The output current is the rated full load of 5 A.

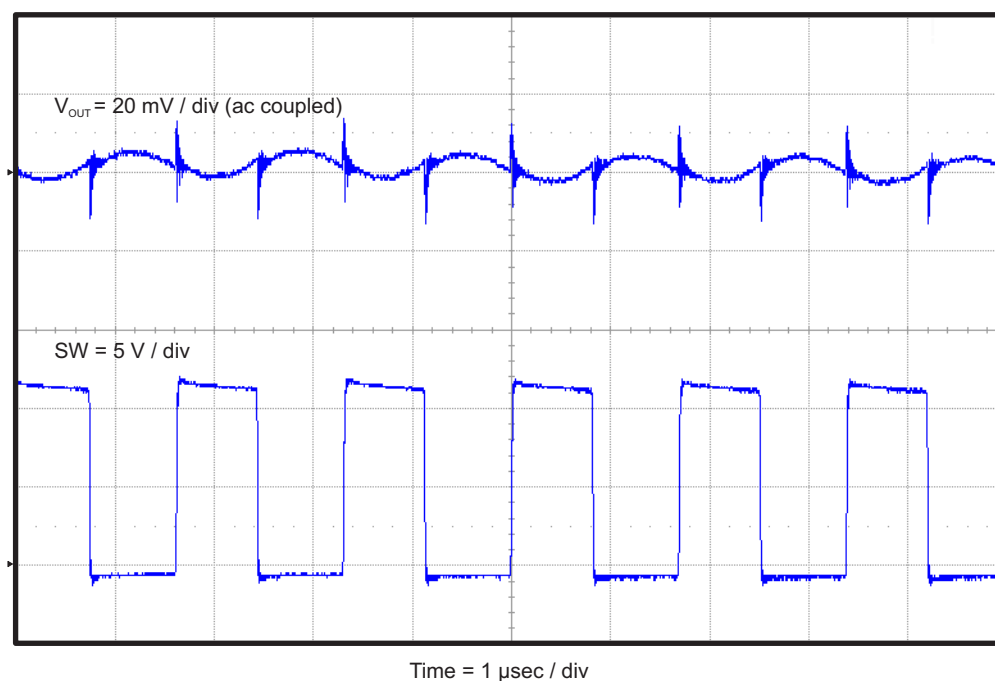


Figure 7. TPS54531EVM-530 Output Voltage Ripple ($I_{OUT} = 5 \text{ A}$)

The TPS54531EVM-530 output voltage ripple is shown in Figure 8. The output current is 1 A.

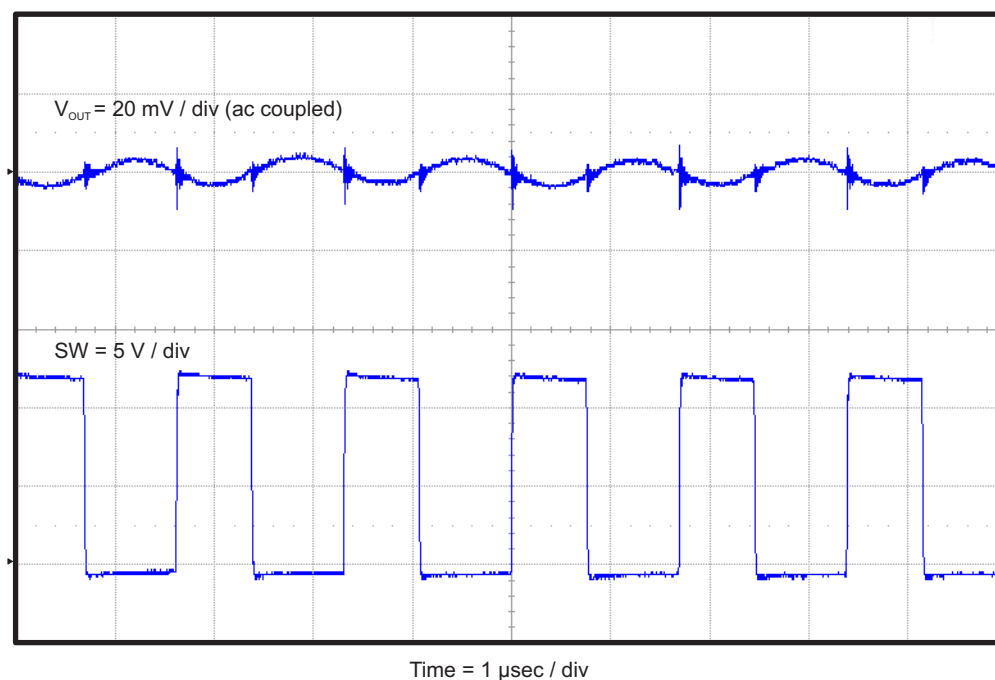


Figure 8. TPS54531EVM-530 Output Voltage Ripple ($I_{OUT} = 1 \text{ A}$)

The TPS54531EVM-530 output voltage ripple is shown in Figure 9. The output current is 0 A (no load).

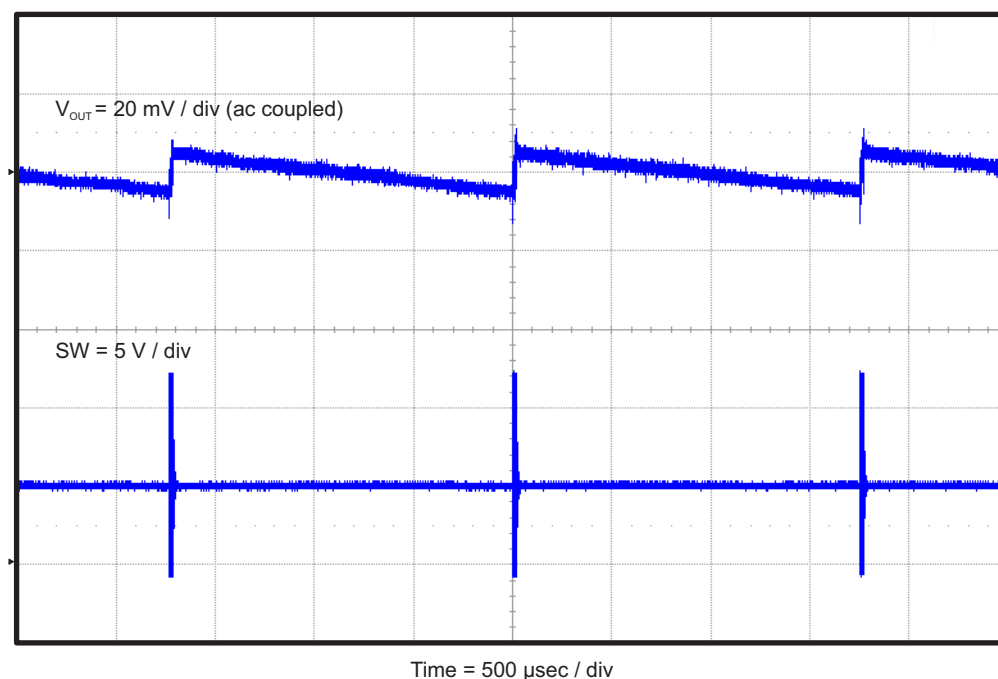


Figure 9. TPS54531EVM-530 Output Voltage Ripple ($I_{OUT} = 0 \text{ A}$)

4.8 Input Voltage Ripple

The TPS54531EVM-530 input voltage ripple is shown in Figure 10. The output current is the rated full load of 5 A.

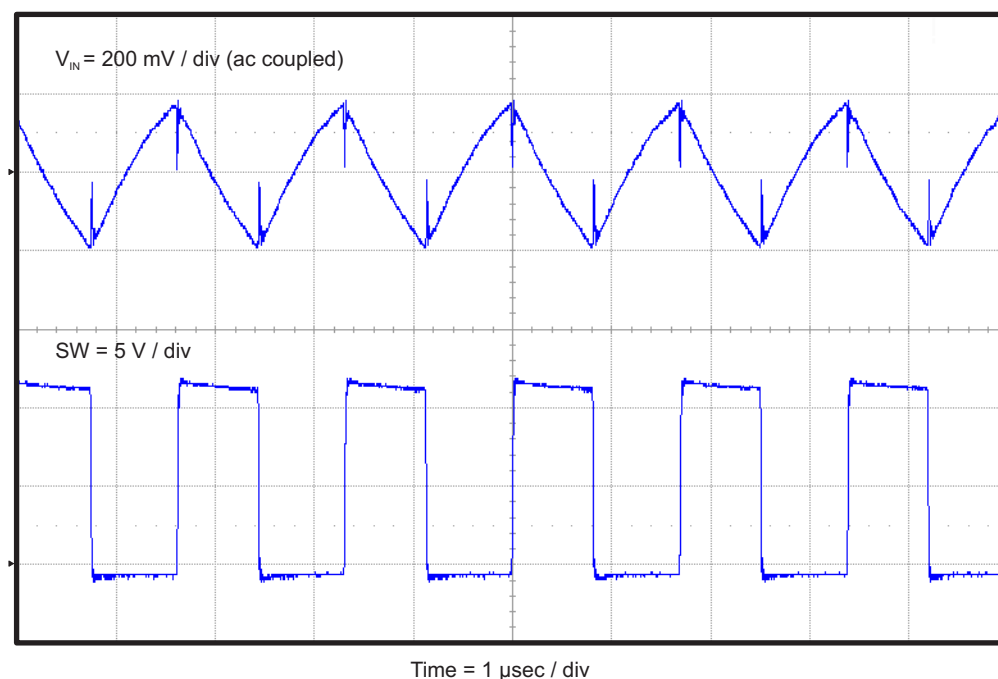


Figure 10. TPS54531EVM-530 Input Voltage Ripple

4.9 Start-Up

The TPS54531EVM-530 start-up waveforms relative to V_{IN} and EN are shown in Figure 11 and Figure 12. $R_{LOAD} = 5\ \Omega$.

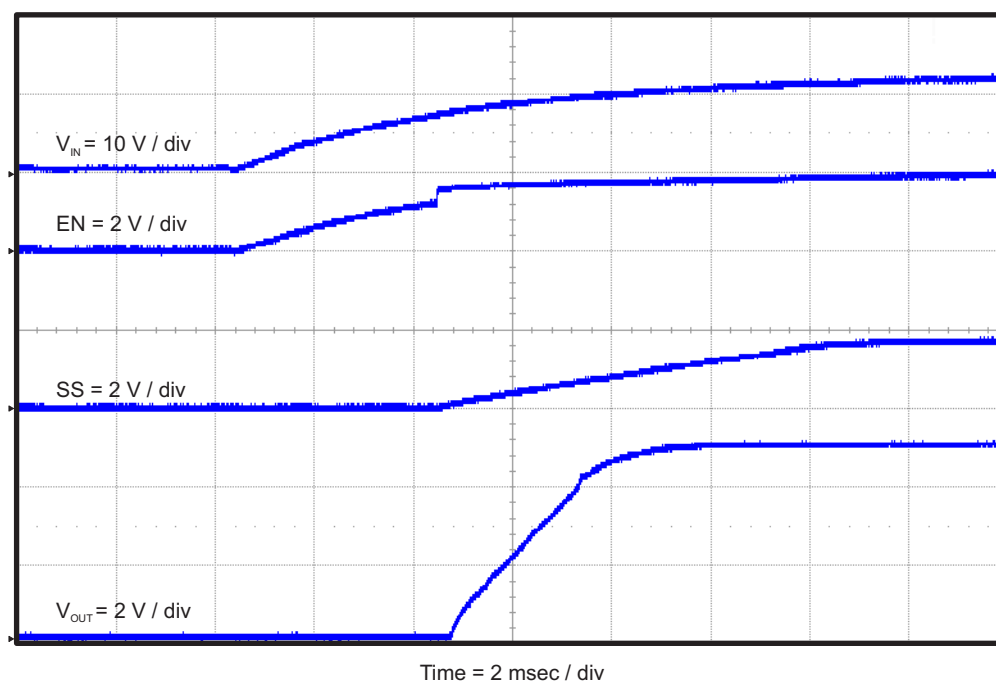


Figure 11. TPS54531EVM-530 Start-Up Relative to V_{IN} with SS

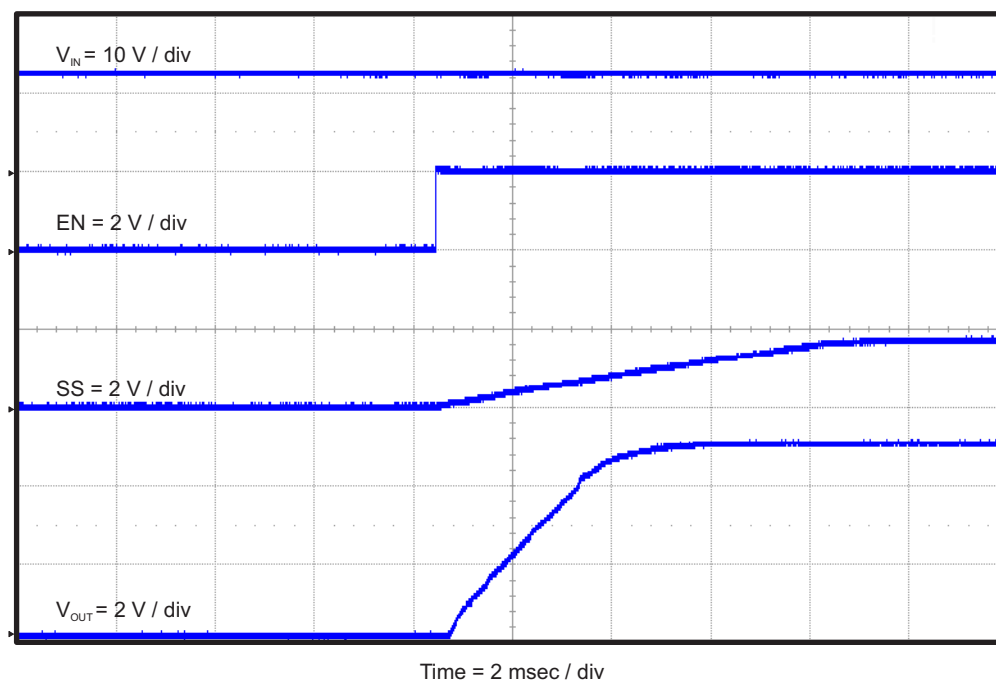


Figure 12. TPS54531EVM-530 Start-Up Relative to EN with SS

4.10 Shut-Down

The TPS54531EVM-530 shut-down waveforms relative to V_{IN} and EN are shown in Figure 13 and Figure 14. $R_{LOAD} = 5\ \Omega$.

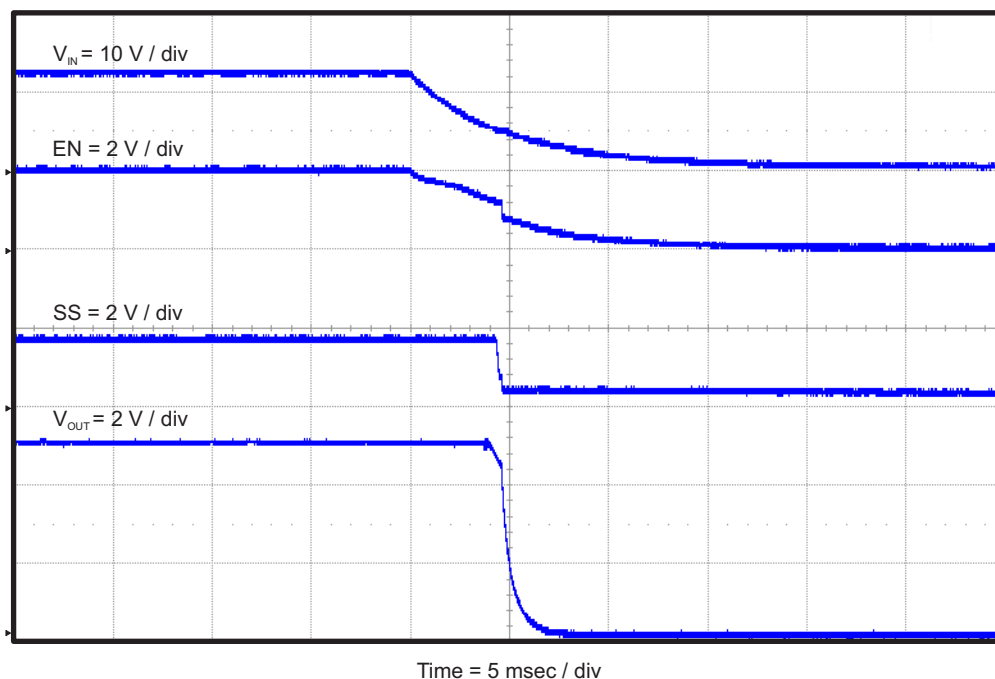


Figure 13. TPS54531EVM-530 Shut-Down Relative to V_{IN} with SS

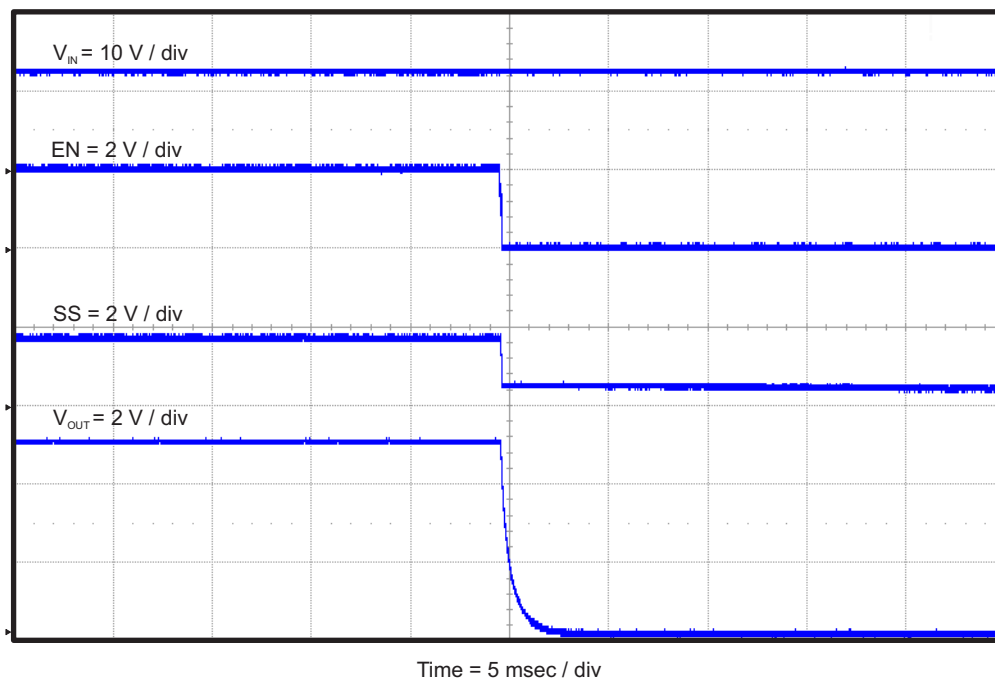


Figure 14. TPS54531EVM-530 Shut-Down Relative to EN with SS

5 Board Layout

This section provides description of the TPS54531EVM-530, board layout, and layer illustrations.

5.1 Layout

The board layout for the TPS54531EVM-530 is shown in Figure 15 through Figure 19. The top layer contains the main power traces for VIN, VOUT, and ground. Also on the top layer are connections for the pins of the TPS54531. Most of the signal traces also are located on the top side. The input decoupling capacitors (C1, C2 and C3) are located as close to the IC as possible. The input and output connectors, test points, and all of the components are located on the top side. An analog ground area is provided on the top side to terminate the sensitive compensation components (C7 and R3) and the output voltage feed back divider (R6). This ground area and the main power ground (GND) are connected at a single point on the top layer at the IC pin 7 (GND). The two internal layers are completely dedicated to power ground planes for heat dissipation. The bottom layer is primarily power ground with traces to connect the BOOT capacitor (C4) and the feedback trace from VOUT to the voltage set point divider network.

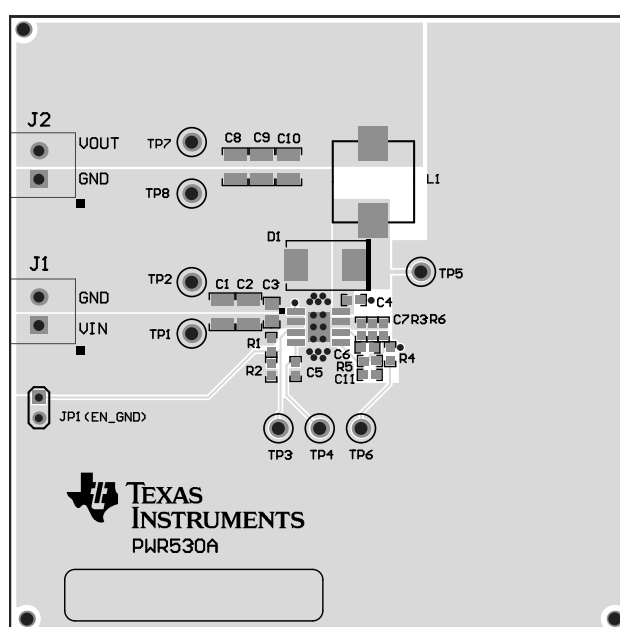


Figure 15. Top Assembly

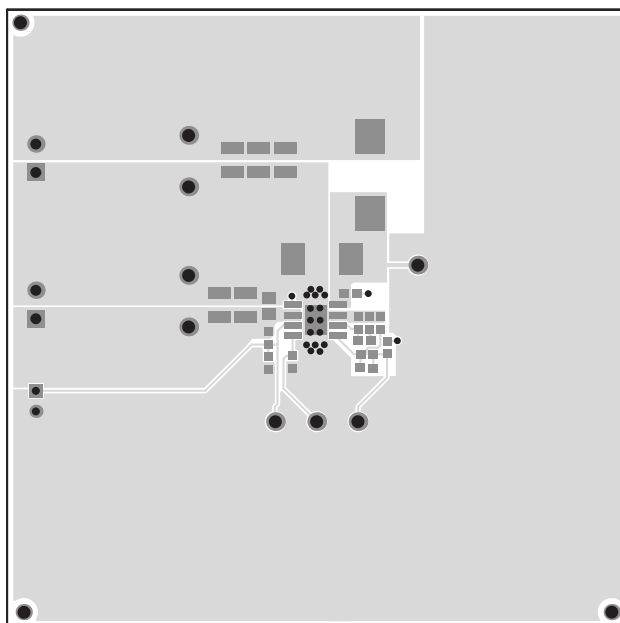


Figure 16. Top Layer

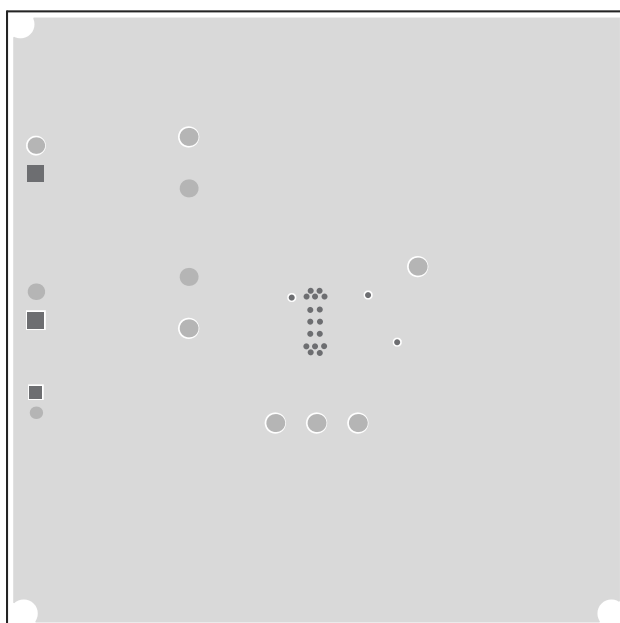


Figure 17. Internal Layer 1

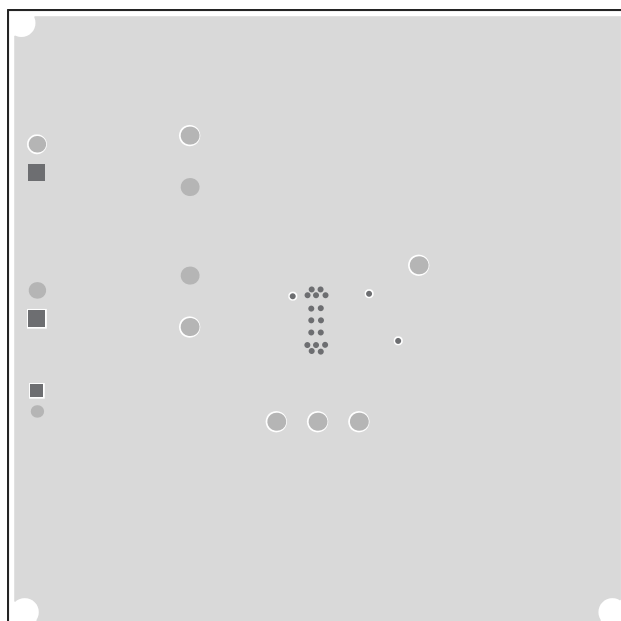


Figure 18. Internal Layer 2

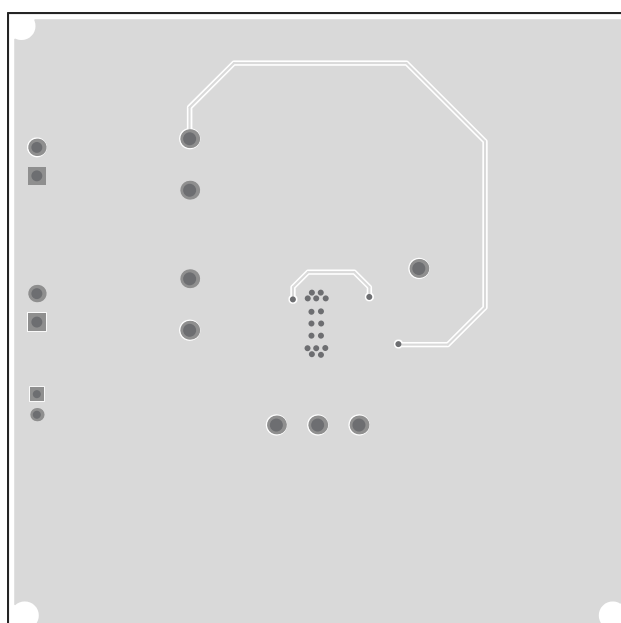


Figure 19. Bottom Layer

6.1 Schematic

Figure 20. TPS54531EVM-530 Schematic Diagram

6.2 Bill of Materials

Table 4. Bill of Materials

Designator	Qty	Value	Description	Package Reference	Part Number	Manufacturer
	1		Printed Circuit Board		PWR530A	Any
C1, C2	2	4.7uF	CAP, CERM, 4.7uF, 50V, +/-10%, X7R, 1210	1210	GRM32ER71H475KA88L	MuRata
C3	1	0.01uF	CAP, CERM, 0.01uF, 50V, +/-10%, X7R, 0805	0805	GRM216R71H103KA01D	MuRata
C4	1	0.1uF	CAP, CERM, 0.1uF, 25V, +/-10%, X7R, 0603	0603	C1608X7R1E104K	TDK
C5	1	0.01uF	CAP, CERM, 0.01uF, 100V, +/-10%, X7R, 0603	0603	06031C103KAT2A	AVX
C6	1	2200pF	CAP, CERM, 2200pF, 50V, +10%, X7R, 0603	0603	GRM188R71H222KA01D	MuRata
C7	1	22pF	CAP, CERM, 22pF, 50V, +5%, C0G/NP0, 0603	0603	GRM1885C1H220JA01D	MuRata
C8, C9	2	47uF	CAP, CERM, 47uF, 10V, +/-10%, X5R, 1210	1210	GRM32ER61A476KE20L	MuRata
C10	0		CAP, CERM,	1210		
C11	0		CAP, CERM,	0603		
D1	1	0.55V	Diode, Schottky, 40V, 5A, SMC	SMC	CDBC540-G	Comchip Technology
J1, J2	2	ED555/2DS	Connector, Male 2 Pole 3.5 mm, 6A, 150V	6.5x6.5 mm	ED555/2DS	On Shore Tech
JP1	1	PEC02SAAN	Header, Male 2-pin, 100mil spacing,	0.100 inch x 2	PEC02SAAN	Sullins
L1	1	4.7 uH	Inductor, 7A, 15 mohm	10mm x 10 mm	74437368047	Würth Elektronik
LBL1	1		Thermal Transfer Printable Labels, 1.250" W x 0.250" H - 10,000 per roll	PCB Label 1.25"H x 0.250"W	THT-13-457-10	Brady
R1	1	665k	RES, 6650k ohm, 1%, 0.1W, 0603	0603	CRCW0603665KFKEA	Vishay-Dale
R2	1	130k	RES, 130k ohm, 1%, 0.1W, 0603	0603	CRCW060130KFKEA	Vishay-Dale
R3	1	37.4k	RES, 37.4k ohm, 1%, 0.1W, 0603	0603	CRCW060337K4FKEA	Vishay-Dale
R4	1	51.1	RES, 51.1 ohm, 1%, 0.1W, 0603	0603	CRCW060351R1FKEA	Vishay-Dale
R5	1	10.2K	RES, 10.2 ohm, 1%, 0.1W, 0603	0603	CRCW060310R2FKEA	Vishay-Dale
R6	1	1.96k	RES, 1.96k ohm, 1%, 0.1W, 0603	0603	CRCW06031K96FKEA	Vishay-Dale
SH-J1	1	1x2	Shunt, 100mil, Gold plated, Black	Shunt	969102-0000-DA	3M
TP1, TP5, TP7	3	Red	Test Point, TH, Compact, Red	Keystone5005	5005	Keystone
TP2, TP8	2	Black	Test Point, TH, Compact, Black	Keystone5006	5006	Keystone
TP3, TP4, TP6	3	White	Test Point, TH, Compact, White	Keystone5007	5007	Keystone
U1	1		IC, DC-DC Converter, 4.5 - 28 Vin, 5A	DDA-8	TPS54531DDA	Texas Instruments

6.3 Reference

1. *TPS54531, 5A, 28-V Input, Step-Down SWIFT™ Converter with Eco-Mode™ data sheet* ([SLVSB15](#))

EVALUATION BOARD/KIT/MODULE (EVM) ADDITIONAL TERMS

Texas Instruments (TI) provides the enclosed Evaluation Board/Kit/Module (EVM) under the following conditions:

The user assumes all responsibility and liability for proper and safe handling of the goods. Further, the user indemnifies TI from all claims arising from the handling or use of the goods.

Should this evaluation board/kit not meet the specifications indicated in the User's Guide, the board/kit may be returned within 30 days from the date of delivery for a full refund. THE FOREGOING LIMITED WARRANTY IS THE EXCLUSIVE WARRANTY MADE BY SELLER TO BUYER AND IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED, IMPLIED, OR STATUTORY, INCLUDING ANY WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE. EXCEPT TO THE EXTENT OF THE INDEMNITY SET FORTH ABOVE, NEITHER PARTY SHALL BE LIABLE TO THE OTHER FOR ANY INDIRECT, SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES.

Please read the User's Guide and, specifically, the Warnings and Restrictions notice in the User's Guide prior to handling the product. This notice contains important safety information about temperatures and voltages. For additional information on TI's environmental and/or safety programs, please visit www.ti.com/esh or contact TI.

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As noted in the EVM User's Guide and/or EVM itself, this EVM and/or accompanying hardware may or may not be subject to the Federal Communications Commission (FCC) and Industry Canada (IC) rules.

For EVMs **not** subject to the above rules, this evaluation board/kit/module is intended for use for ENGINEERING DEVELOPMENT, DEMONSTRATION OR EVALUATION PURPOSES ONLY and is not considered by TI to be a finished end product fit for general consumer use. It generates, uses, and can radiate radio frequency energy and has not been tested for compliance with the limits of computing devices pursuant to part 15 of FCC or ICES-003 rules, which are designed to provide reasonable protection against radio frequency interference. Operation of the equipment may cause interference with radio communications, in which case the user at his own expense will be required to take whatever measures may be required to correct this interference.

General Statement for EVMs including a radio

User Power/Frequency Use Obligations: This radio is intended for development/professional use only in legally allocated frequency and power limits. Any use of radio frequencies and/or power availability of this EVM and its development application(s) must comply with local laws governing radio spectrum allocation and power limits for this evaluation module. It is the user's sole responsibility to only operate this radio in legally acceptable frequency space and within legally mandated power limitations. Any exceptions to this are strictly prohibited and unauthorized by Texas Instruments unless user has obtained appropriate experimental/development licenses from local regulatory authorities, which is responsibility of user including its acceptable authorization.

For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

For EVMs annotated as IC – INDUSTRY CANADA Compliant

This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Concerning EVMs including radio transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concerning EVMs including detachable antennas

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

【Important Notice for Users of this Product in Japan】

This development kit is NOT certified as Confirming to Technical Regulations of Radio Law of Japan

If you use this product in Japan, you are required by Radio Law of Japan to follow the instructions below with respect to this product:

1. Use this product in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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For Feasibility Evaluation Only, in Laboratory/Development Environments. Unless otherwise indicated, this EVM is not a finished electrical equipment and not intended for consumer use. It is intended solely for use for preliminary feasibility evaluation in laboratory/development environments by technically qualified electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems and subsystems. It should not be used as all or part of a finished end product.

Your Sole Responsibility and Risk. You acknowledge, represent and agree that:

1. You have unique knowledge concerning Federal, State and local regulatory requirements (including but not limited to Food and Drug Administration regulations, if applicable) which relate to your products and which relate to your use (and/or that of your employees, affiliates, contractors or designees) of the EVM for evaluation, testing and other purposes.
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3. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.
4. You will take care of proper disposal and recycling of the EVM's electronic components and packing materials.

Certain Instructions. It is important to operate this EVM within TI's recommended specifications and environmental considerations per the user guidelines. Exceeding the specified EVM ratings (including but not limited to input and output voltage, current, power, and environmental ranges) may cause property damage, personal injury or death. If there are questions concerning these ratings please contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, some circuit components may have case temperatures greater than 60°C as long as the input and output are maintained at a normal ambient operating temperature. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors which can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during normal operation, please be aware that these devices may be very warm to the touch. As with all electronic evaluation tools, only qualified personnel knowledgeable in electronic measurement and diagnostics normally found in development environments should use these EVMs.

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