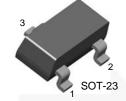


KST42 / KST43 NPN Epitaxial Silicon Transistor

Features

• High-Voltage Transistor



1. Base 2. Emitter 3. Collector

Ordering Information

Part Number	Marking	Package	Packing Method	
KST42MTF	1D	SOT-23 3L	Tape and Reel	
KST43MTF	1E	SOT-23 3L	Tape and Reel	

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter		Value	Unit
V _{CBO} Collector	Collector Page Voltage	KST42	300	V
	Collector-Base Voltage	KST43	200	V
V _{CEO} Collector	Callector Emitter Voltogo	KST42	300	V
	Collector-Emitter Voltage	KST43	200	v
V _{EBO}	Emitter-Base Voltage		6	V
Ι _C	Collector Current - Continuous		500	mA
$T_{J,T_{STG}}$	Junction and Storage Temperature Range		-55 to +150	°C

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
P _C	Collector Power Dissipation	350	mW
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

July 2014

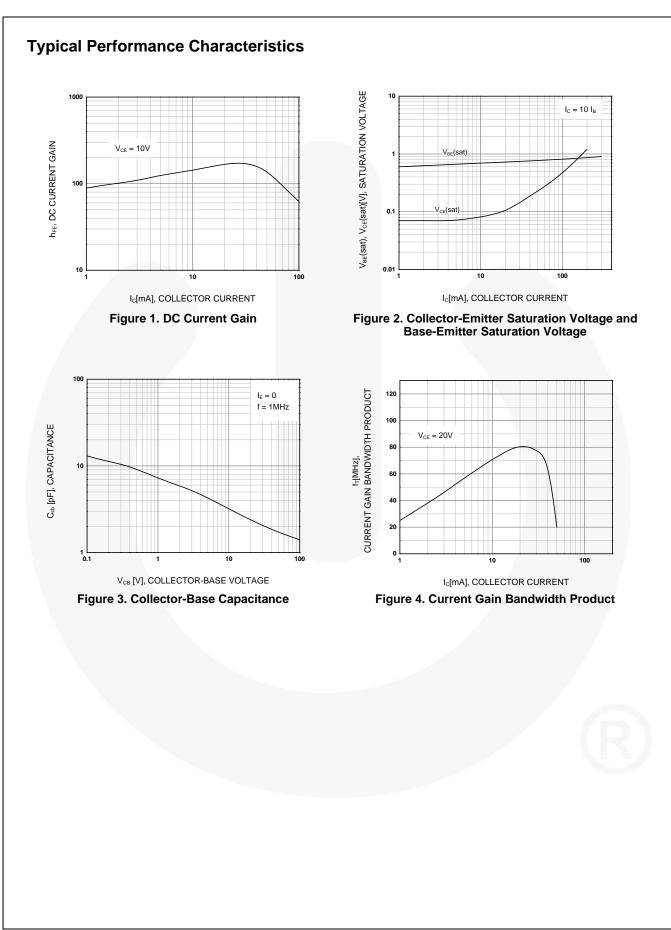
Electrical Characteristics

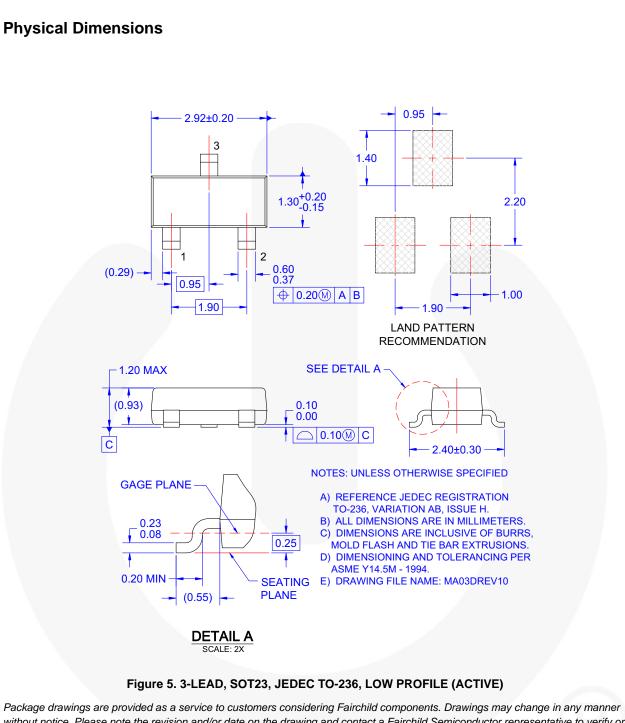
Values are at T_{A} = 25°C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V _{CBO}	Collector-Base Breakdown Voltage	KST42	Ι _C = 100 μA, Ι _E = 0	300		- V
		KST43		200		
	Collector-Emitter Breakdown Voltage ⁽¹⁾	KST42	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$	300		- V
		KST43		200		
V _{EBO}	Emitter-Base Breakdown Voltage		$I_{E} = 100 \ \mu A, I_{C} = 0$	6		V
I _{CBO}	Collector Cut-Off Current		$V_{CB} = 200 \text{ V}, I_E = 0$		0.1	μA
I _{EBO}	Emitter Cut-Off Current		$V_{EB} = 5 V, I_{C} = 0$		0.1	μA
			$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 1 \text{ mA}$	25		
h _{FE}	DC Current Gain ⁽¹⁾		V _{CE} = 10 V, I _C = 10 mA	40		
			$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 30 \text{ mA}$	40		
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾		$I_{\rm C} = 20 \text{ mA}, I_{\rm B} = 2 \text{ mA}$		0.5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽¹⁾		$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 2 {\rm mA}$		0.9	V
C _{ob}	Output Capacitance	KST42	$V_{CB} = 20 \text{ V}, I_{E} = 0,$		3	рF
		KST43	f = 1 MHz		4	4 pr
f _T	Current Gain Bandwidth Produ	uct	$V_{CE} = 20 \text{ V}, I_{C} = 10 \text{ mA}, f = 100 \text{ MHz}$	50		MHz

Note:

1. Pulse test: pulse width \leq 300 $\mu s,$ duty cycle \leq 2%.





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KST42 / KST43 — NPN Epitaxial Silicon Transistor

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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