## 1N4148W-V



**Vishay Semiconductors** 

## Small Signal Fast Switching Diode

### FEATURES

• These diodes are also available in other case styles including the DO-35 case with the type designation 1N4148, the MiniMELF case with the type designation LL4148, and the SOT-23 case with the type designation IMBD4148-V.



RoHS

COMPLIANT

- Silicon epitaxial planar diode
- Fast switching diodes
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

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#### **MECHANICAL DATA**

Case: SOD-123

Weight: approx. 10.3 mg

#### Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS	
1N4148W-V	1N4148W-V-GS18 or 1N4148W-V-GS08	A2	Single diode	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	V	
Repetitive peak reverse voltage		V <sub>RRM</sub>	100	V	
Average rectified current half wave rectification with resistive load <sup>(1)</sup>	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Surge forward current	t < 1 s and $T_j$ = 25 °C	I <sub>FSM</sub>	500	mA	
Power dissipation <sup>(1)</sup>		P <sub>tot</sub>	350	mW	

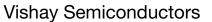
<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air <sup>(1)</sup>		R <sub>thJA</sub>	375	K/W	
Junction temperature		Тj	150	°C	
Storage temperature		T <sub>stg</sub>	- 65 to + 150	°C	

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.

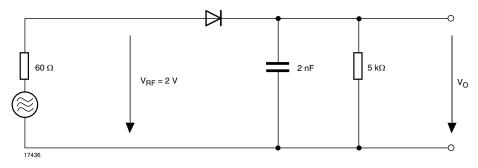
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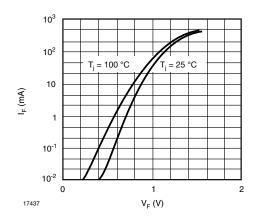


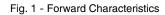
<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>			1000	mV
	I <sub>F</sub> = 100 mA	VF			1200	mV
Leakage current	V <sub>R</sub> = 20 V	I <sub>R</sub>			25	nA
	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μA
	V <sub>R</sub> = 100 V	I <sub>R</sub>			100	μA
	V <sub>R</sub> = 20 V, T <sub>J</sub> = 150 °C	$\begin{array}{c ccccc} 00 \text{ V} & I_{\text{R}} & 100 \\ \hline = 150 ^{\circ}\text{C} & I_{\text{R}} & 50 \end{array}$	50	μA		
Diode capacitance	$V_{\rm F} = V_{\rm R} = 0 \ {\rm V}$	C <sub>D</sub>			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \ \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V <sub>fr</sub>			2.5	V
Reverse recovery time	$I_F = 10 \text{ mA}, i_R = 1 \text{ mA}, V_R = 6 \text{ V}, \\ R_L = 100 \ \Omega$	t <sub>rr</sub>			4	ns
Rectification efficiency	f = 100 MHz, V <sub>RF</sub> = 2 V	ην	0.45			

#### **RECTIFICATION EFFICIENCY MEASUREMENT CIRCUIT**



TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)





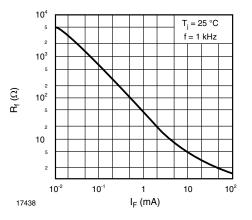


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

2

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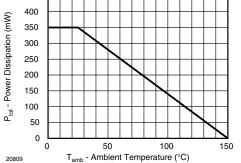


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

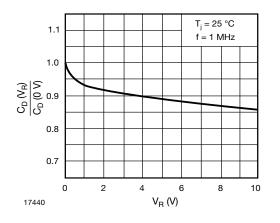


Fig. 4 - Relative Capacitance vs. Reverse Voltage

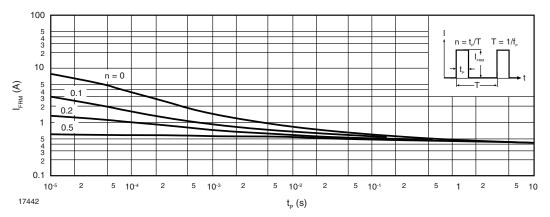


Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

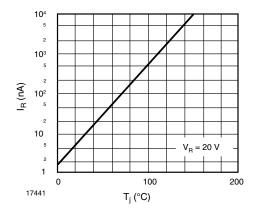


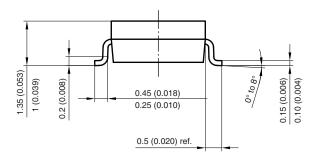
Fig. 5 - Leakage Current vs. Junction Temperature

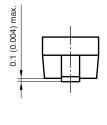
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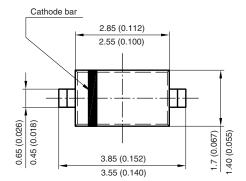


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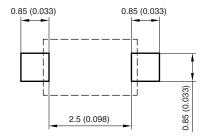
#### PACKAGE DIMENSIONS in millimeters (inches): SOD-123







Mounting Pad Layout



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