

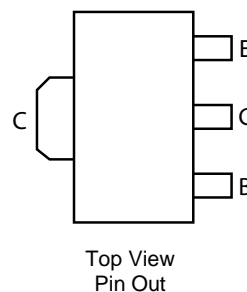
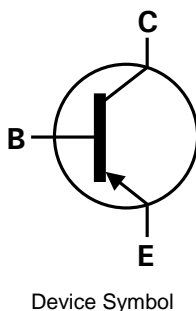
## 150V PNP HIGH VOLTAGE SWITCHING TRANSISTOR IN SOT89

### Features

- $BV_{CEO} > -150V$
- $BV_{CEV} > -180V$
- $I_C = -700mA$  high Continuous Collector Current
- Low saturation voltage  $V_{CE(sat)} < -300mV$  @  $-100mA$
- Complementary NPN type: FCX495
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

### Mechanical Data

- Case: SOT89
- Case material: molded plastic. "Green" molding compound.
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.052 grams (Approximate)

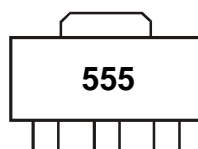


### Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
FCX555TA	555	7	12	1,000

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

### Marking Information



555 = Product Type Marking Code

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Limit	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-180	V
Collector-Emitter Voltage	V <sub>CEV</sub>	-180	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-150	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	I <sub>C</sub>	-0.7	A
Peak Pulse Current	I <sub>CM</sub>	-2	A

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

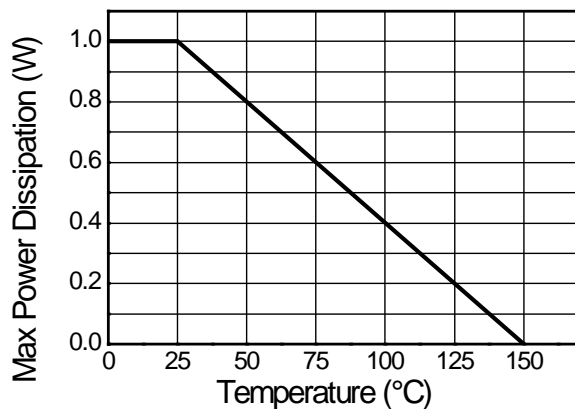
Characteristic	Symbol	Value	Unit
Power Dissipation	P <sub>D</sub>	1	W
		1.5	
		2.1	
Thermal Resistance, Junction to Ambient Air	R <sub>θJA</sub>	125	°C/W
		83	
		60	
Thermal Resistance, Junction to Lead	R <sub>θJL</sub>	10.01	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

## ESD Ratings (Note 9)

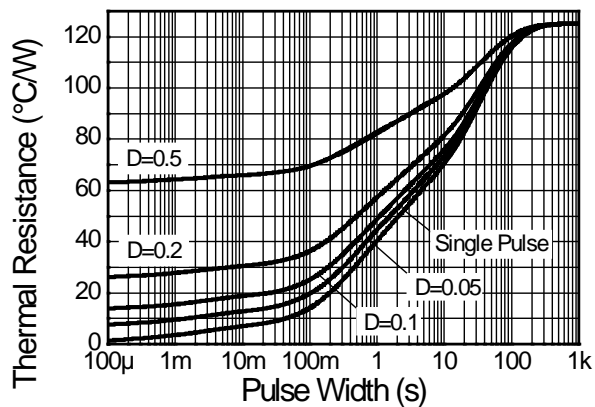
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	≥ 4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	≥ 400	V	C

- Notes:
- For a device mounted with the exposed collector pad on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
  - Same as note (5), except the device is mounted on 25mm x 25mm 1oz copper.
  - Same as note (5), except the device is mounted on 50mm x 50mm 1oz copper.
  - Thermal resistance from junction to solder-point (on the exposed collector pad).
  - Refer to JEDEC specification JESD22-A114 and JESD22-A115.

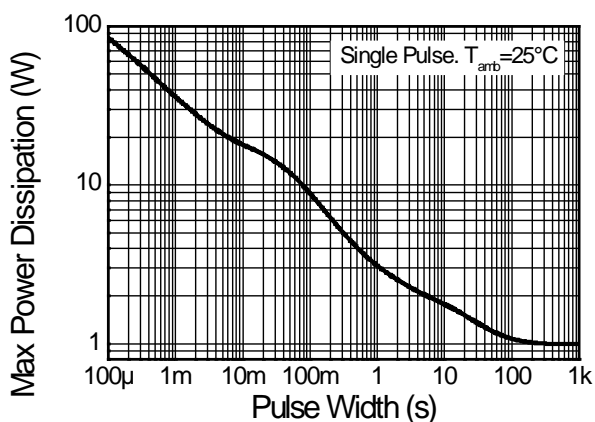
## Thermal Characteristics and Derating Information



**Derating Curve**



**Transient Thermal Impedance**



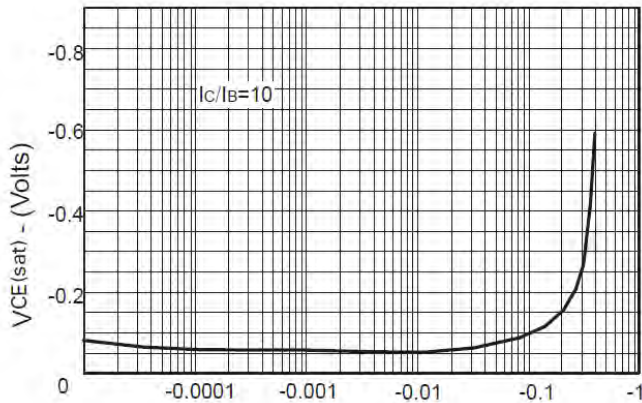
**Pulse Power Dissipation**

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-180	—	—	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage	BV <sub>CEV</sub>	-180	—	—	V	I <sub>C</sub> = -1μA, -0.3V < V <sub>BE</sub> < 1V
Collector-Emitter Breakdown Voltage	BV <sub>CER</sub>	-180	—	—	V	I <sub>C</sub> = -1μA, R <sub>B</sub> ≤ 1kΩ
Collector-Emitter Breakdown Voltage (Note 10)	BV <sub>CEO</sub>	-150	—	—	V	I <sub>C</sub> = -1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-7	-8.1	—	V	I <sub>E</sub> = -100μA
Collector Cutoff Current	I <sub>CBO</sub>	—	<1 -	-20 -10	nA μA	V <sub>CB</sub> = -144V V <sub>CB</sub> = -144V, T <sub>A</sub> = +100°C
Emitter Cutoff Current	I <sub>EBO</sub>	—	<1	-20	nA	V <sub>EB</sub> = -6V
DC current transfer Static ratio (Note 10)	h <sub>FE</sub>	100 100	—	— 300	—	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -5V I <sub>C</sub> = -100mA, V <sub>CE</sub> = -5V
Collector-Emitter Saturation Voltage (Note 10)	V <sub>CE(sat)</sub>	— —	— —	-300 -400	mV	I <sub>C</sub> = -100mA, I <sub>B</sub> = -10mA I <sub>C</sub> = -250mA, I <sub>B</sub> = -25mA
Base-Emitter Saturation Voltage (Note 10)	V <sub>BE(sat)</sub>	—	—	-1000	mV	I <sub>C</sub> = -250mA, I <sub>B</sub> = -25mA
Base-Emitter Turn-on Voltage (Note 10)	V <sub>BE(on)</sub>	—	—	-950	mV	I <sub>C</sub> = -250mA, V <sub>CE</sub> = -5V
Transitional Frequency	f <sub>T</sub>	—	100	—	MHz	I <sub>E</sub> = -50mA, V <sub>CE</sub> = -10V f = 100MHz
Output capacitance	C <sub>obo</sub>	—	—	10	pF	V <sub>CB</sub> = -10V, f = 1MHz,

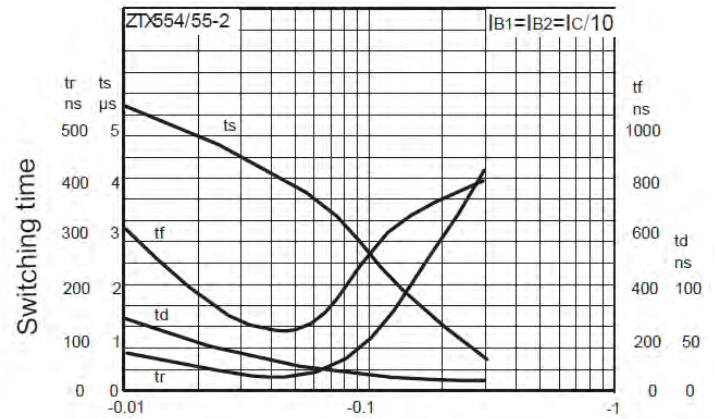
Note: 10. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

**Typical Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)



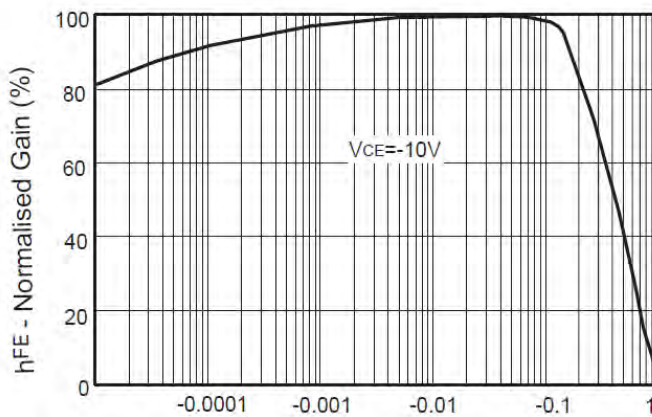
$I_C$  - Collector Current (Amps)

**$V_{CE(sat)}$  v  $I_C$**



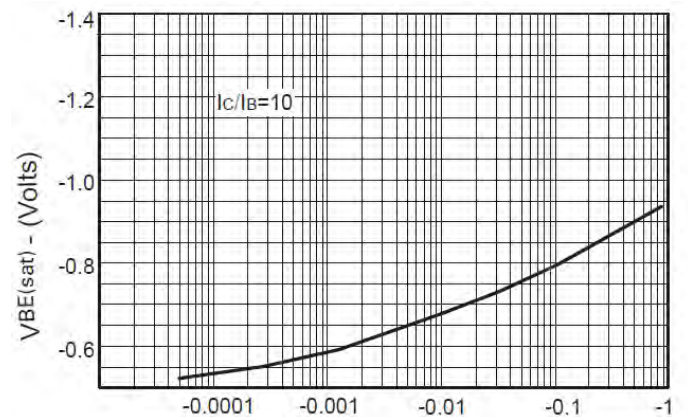
$I_C$  - Collector Current (Amps)

**Switching Speeds**



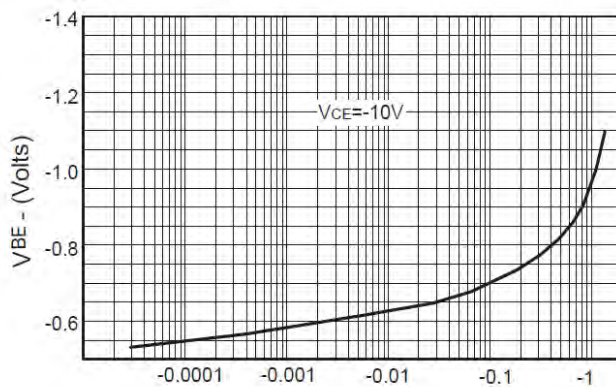
$I_C$  - Collector Current (Amps)

**$h_{FE}$  v  $I_C$**



$I_C$  - Collector Current (Amps)

**$V_{BE(sat)}$  v  $I_C$**

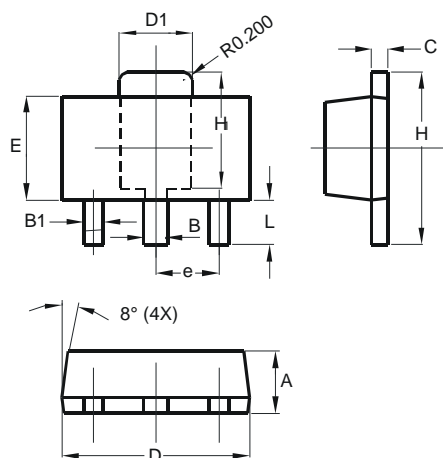


$I_C$  - Collector Current (Amps)

**$V_{BE(on)}$  v  $I_C$**

## Package Outline Dimensions

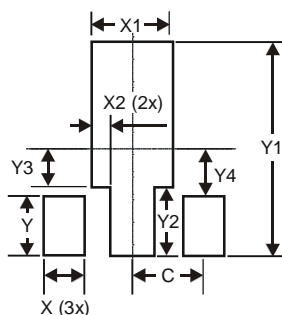
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT89		
Dim	Min	Max
A	1.40	1.60
B	0.44	0.62
B1	0.35	0.54
C	0.35	0.44
D	4.40	4.60
D1	1.62	1.83
E	2.29	2.60
e	1.50 Typ	
H	3.94	4.25
H1	2.63	2.93
L	0.89	1.20
All Dimensions in mm		

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
X	0.900
X1	1.733
X2	0.416
Y	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
C	1.500

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to voltage spacing between terminals.

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➤ Address :

401 Building No.5, JiuGe Business Center, Lane 2301, Yishan Rd  
Minhang District, Shanghai , China

➤ Sales :

Direct     +86 (21) 6401-6692  
  
Email       amall@ameya360.com  
  
QQ         800077892  
  
Skype      ameyasales1 ameyasales2

➤ Customer Service :

Email       service@ameya360.com

➤ Partnership :

Tel         +86 (21) 64016692-8333  
  
Email      mkt@ameya360.com